



DMT6005LFG

60V N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI3333-8

## **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> Max T <sub>C</sub> = +25°C (Note 9)
	4.1mΩ @ V <sub>GS</sub> = 10V	100A
60V	6.3mΩ @ V <sub>GS</sub> = 6V	81A
	7mΩ @ V <sub>GS</sub> = 4.5V	77A

**Description and Applications** 

This MOSFET is designed to minimize the on-state resistance ( $R_{DS(ON)}$ ), yet maintain superior switching performance, making it ideal for high efficiency power management applications.

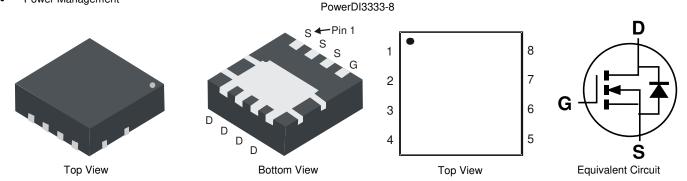
- Synchronous Rectification
- Motor Control
- DC-DC Converters
- Power Management

## **Features and Benefits**

- 100% Unclamped Inductive Switching (UIS) Test in Production Ensures More Reliable and Robust End Application
- Low RDS(ON) Ensures On-State Losses are Minimized
- Small Form Factor Thermally Efficient Package Enables Higher Density End Products
- Occupies Just 33% of the Board Area Occupied by SO-8 Enabling Smaller End Product
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. <u>https://www.diodes.com/quality/product-definitions/</u>

## **Mechanical Data**

- Case: PowerDI<sup>®</sup>3333-8
- Case Material: Molded Plastic, "Green" Molding Compound.
  UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.029 grams (Approximate)



## Ordering Information (Note 4)

Part Number	Case	Packaging
DMT6005LFG-7	PowerDI3333-8	2,000/Tape & Reel
DMT6005LFG-13	PowerDI3333-8	3,000/Tape & Reel

Notes: 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied. 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

SK6 = Product Type Marking Code YYWW = Date Code Marking

WW = Week Code (01 to 53)

YY = Last Two Digits of Year (ex: 20 = 2020)

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

## **Marking Information**



PowerDI is a registered trademark of Diodes Incorporated. DMT6005LFG Document number: DS40894 Rev. 3 - 2

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# Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	VDSS	60	V	
Gate-Source Voltage		V <sub>GSS</sub>	±20	V
Continuous Drain Current (Notes 6 & 9) V <sub>GS</sub> = 10V	Tc = +25°C Tc = +70°C	lр	100 80	А
Continuous Drain Current (Note 5) V <sub>GS</sub> = 10V	TA = +25°C TA = +70°C	ID	18 14	А
Maximum Continuous Body Diode Forward Current (Note 6)		Is	100	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	Idм	400	A	
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle	I <sub>SM</sub>	400	A	
Avalanche Current, L = 1mH		las	18.5	А
Avalanche Energy, L = 1mH		Eas	171	mJ

## **Thermal Characteristics**

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T <sub>A</sub> = +25°C	PD	1.98	W
Thermal Resistance, Junction to Ambient (Note 5)		R <sub>0JA</sub>	63	°C/W
Total Power Dissipation (Note 6) $T_{C} = +25^{\circ}C$		PD	62.5	W
Thermal Resistance, Junction to Case (Note 6)		Rejc	2.0	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

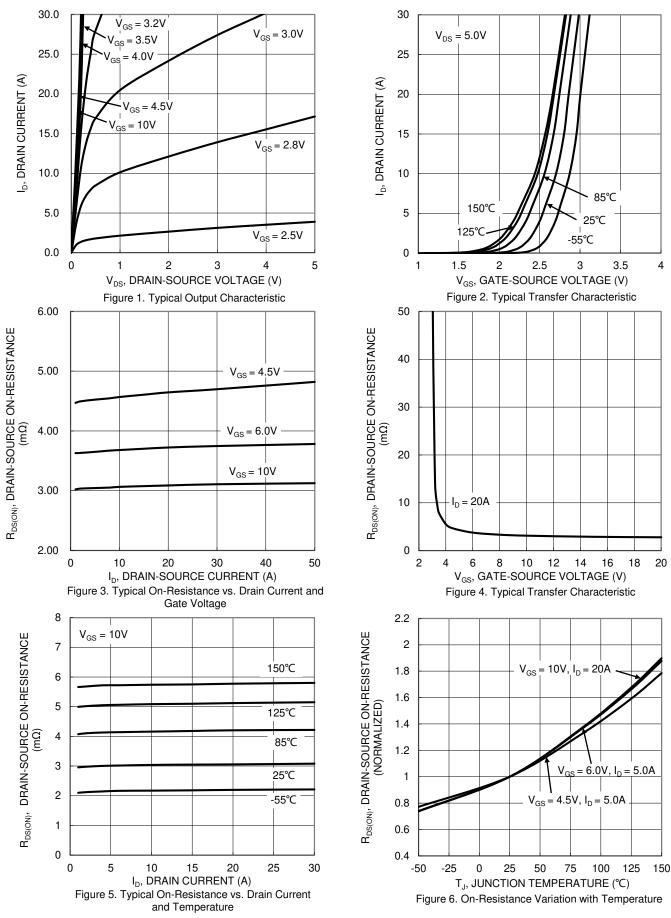
## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

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	Symbol	Min	Тур	Мах	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)	51	00					
Drain-Source Breakdown Voltage	BVDSS	60	—	<u> </u>	V	$V_{GS} = 0V, I_D = 1mA$	
Zero Gate Voltage Drain Current	IDSS	_	_	1	μA	$V_{DS} = 48V, V_{GS} = 0V$	
Gate-Source Leakage	lgss	—	—	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	VGS(TH)	1		2.5	V	$V_{DS} = V_{GS}$ , $I_D = 250 \mu A$	
		_	3.1	4.1	mΩ	$V_{GS} = 10V, I_{D} = 20A$	
Static Drain-Source On-Resistance	RDS(ON)	_	3.6	6.3	mΩ	VGS = 6V, ID = 20A	
		_	4.4	7	mΩ	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 20A	
Diode Forward Voltage	V <sub>SD</sub>	_	0.8	1.2	V	$V_{GS} = 0V, I_{S} = 20A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss	_	3150	_		$V_{DS} = 30V, V_{GS} = 0V,$ f = 1MHz	
Output Capacitance	Coss		1036		pF		
Reverse Transfer Capacitance	Crss	_	69	_			
Gate Resistance	Rg		0.7		Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	
Total Gate Charge (V <sub>GS</sub> = 10V)	Qg	_	48.7				
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Qg	_	23.6		nC	$V_{DD}=30V,\ I_D=50A$	
Gate-Source Charge	Qgs	_	7.0		nc		
Gate-Drain Charge	Q <sub>gd</sub>	_	11.2	_			
Turn-On Delay Time	t <sub>D(ON)</sub>	_	7.3	_			
Turn-On Rise Time	tR	_	11.3	_		$V_{DD} = 30V, V_{GS} = 10V,$ $I_D = 30A, R_G = 3.3\Omega$	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	26.0	—	ns		
Turn-Off Fall Time	tF	—	11.0	—	1		
Bodyy Diode Reverse Recovery Time	trr	_	40.8	—	ns		
Body Diode Reverse Recovery Charge	Q <sub>RR</sub>	_	51.5	—	nC	— I <sub>F</sub> = 30A, di/dt = 100A/μs	

 Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
 Thermal resistance from junction to soldering point (on the exposed drain pad).
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.
 Limited by Package. Notes:

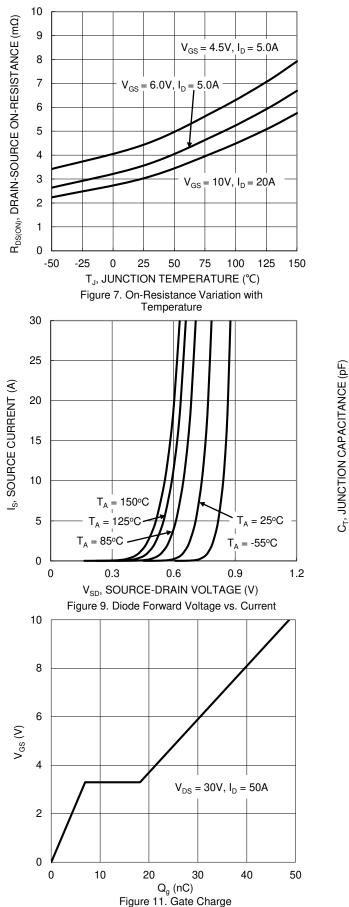


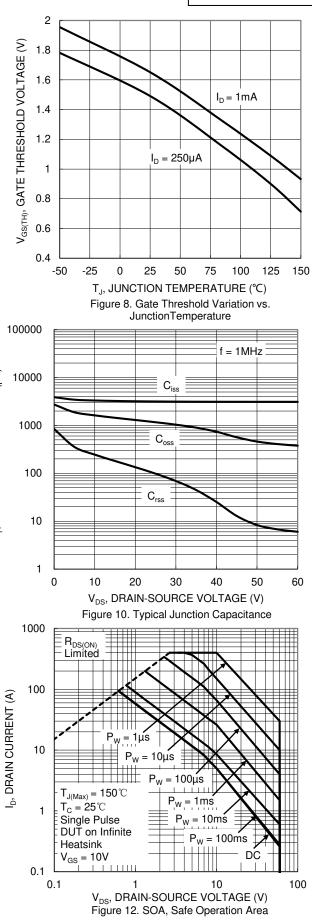
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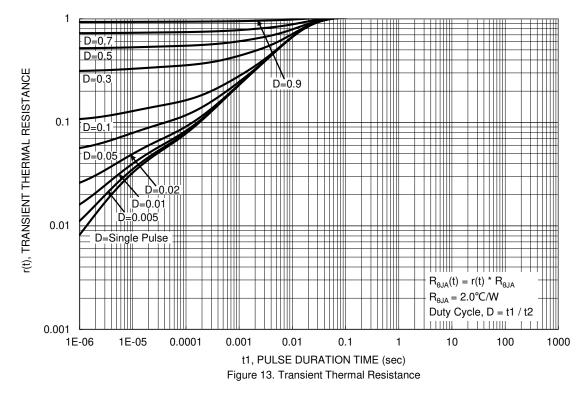
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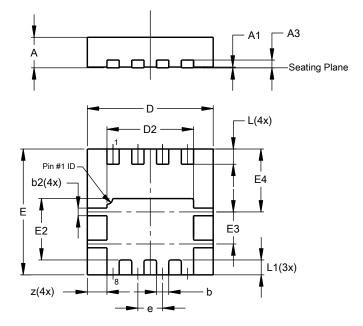




## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### PowerDI3333-8

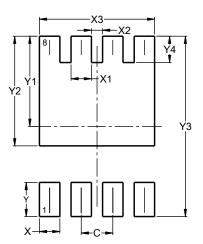


PowerDI3333-8						
Dim	Min	Max	Тур			
Α	0.75	0.85	0.80			
A1	0.00	0.05	0.02			
A3	-	-	0.203			
b	0.27	0.37	0.32			
b2	0.15	0.25	0.20			
D	3.25	3.35	3.30			
D2	2.22	2.32	2.27			
E	3.25	3.35	3.30			
E2	1.56	1.66	1.61			
E3	0.79	0.89	0.84			
E4	1.60	1.70	1.65			
е	-	-	0.65			
L	0.35	0.45	0.40			
L1	_	_	0.39			
z	_	-	0.515			
All Dimensions in mm						

## Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

### PowerDI3333-8



Dimensions	Value (in mm)			
С	0.650			
Х	0.420			
X1	0.420			
X2	0.230			
X3	2.370			
Y	0.700			
Y1	1.850			
Y2	2.250			
Y3	3.700			
Y4	0.540			



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