# General purpose (dual transistors)

# **IMX8**

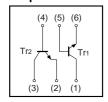
#### Features

- 1) Two 2SC3906K chips in an SMT package.
- 2) High breakdown voltage.

# ● Package, marking, and packaging specifications

Part No.	IMX8
Package	SMT6
Marking	X8
Code	T108
Basic ordering unit (pieces)	3000

# ●Equivalent circuit



### ● Absolute maximum ratings (Ta=25°C)

●Equivalent circuit  (4) (5) (6)  Tr2  Tr1  Tr1  (3) (2) (1)  ■Absolute maximum ratings	(Ta=25°C)					
Parameter	Symbol	Limits	Unit			
Collector-base voltage	Vсво	120	V			
Collector-emitter voltage	VCEO	120	V			
Emitter-base voltage	Vево	5	V			
Collector current	lc /	50	mA			
Power dissipation	Pc	300(TOTAL)	mW *			
Junction temperature	Ţj	150	°C			
Storage temperature	Tstg	-55 to +150	°C			
* 000m/M = = = alam = = t == = = t = = =						

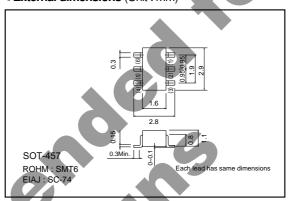
<sup>200</sup>mW per element must not be exceeded

# ●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	120	_	_	V	Ic=50μA
Collector-emitter breakdown voltage	BVceo	120	-	-	V	Ic=1mA
Emitter-base breakdown voltage	BVEBO	5	_	_	V	Iε=50μA
Collector cutoff current	Ісво	_	-	0.5	μΑ	Vcb=100V
Emitter cutoff current	ІЕВО	-	-	0.5	μА	V <sub>EB</sub> =4V
DC current transfer ratio	hfe	180	-	820	-	VcE=6V, Ic=2mA
Transition frequency	f⊤	_	140	_	MHz	VcE=12V, IE= -2mA, f=100MHz *
Collector-emitter saturation voltage	VCE(sat)	_	-	0.5	V	Ic/I <sub>B</sub> =10mA/1mA

<sup>\*</sup>Transition frequency of the device

# ●External dimensions (Unit:mm)



#### Electrical characteristics

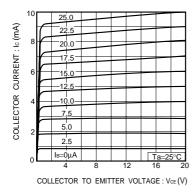


Fig.1 Ground emitter output characteristics

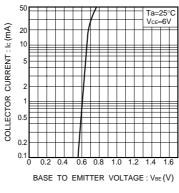


Fig.2 Ground emitter propagation characteristics

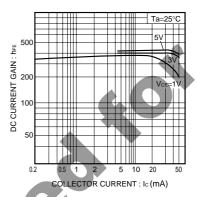


Fig.3 DC current gain vs. collector current

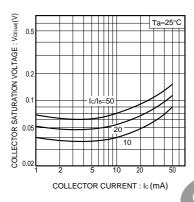


Fig.4 Collector-emitter saturation voltage vs. collector current ( I )

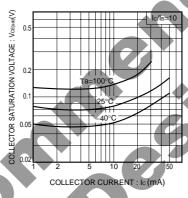


Fig.5 Collector-emitter saturation voltage vs. collector current ( II )

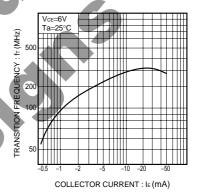


Fig.6 Gain bandwidth product vs. emitter current

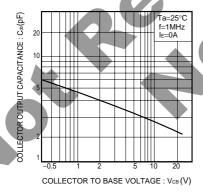


Fig.7 Collector output capacitance vs. collector-base voltage

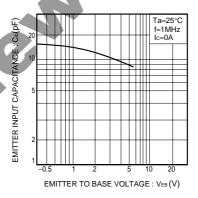


Fig.8 Emitter input capacitance vs. emitter-base voltage

Rev.A

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