ΡΛΝ	JIT
	SEMI
	CONDUCTOR

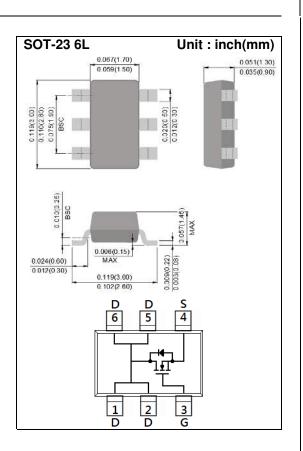


#### Features

- RDS(ON) , VGS@4.5V, ID@6.6A<36m $\Omega$
- Rds(on) , Vgs@2.5V, Id@4.1A<52m $\Omega$
- RDS(ON) , VGS@1.8V, ID@1.9A<92m $\Omega$
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc..
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

#### **Mechanical Data**

- Case: SOT-23 6L Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0005 ounces, 0.014 grams
- Marking: S14



#### Maximum Ratings and Thermal Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V <sub>DS</sub>	20	V
Gate-Source Voltage	V <sub>GS</sub>	<u>+</u> 12	V	
Continuous Drain Current		I <sub>D</sub>	6.6	А
Pulsed Drain Current		Ідм	26.4	А
Power Dissipation	T <sub>a</sub> =25°C	PD	2	W
	Derate above 25°C		16	mW/°C
Operating Junction and Storage Temperature Range		TJ,TSTG	-55~150	٥C
Typical Thermal Resistance - Junction to Ambient <sup>(Note 3)</sup>		R <sub>eja</sub>	62.5	°C/W



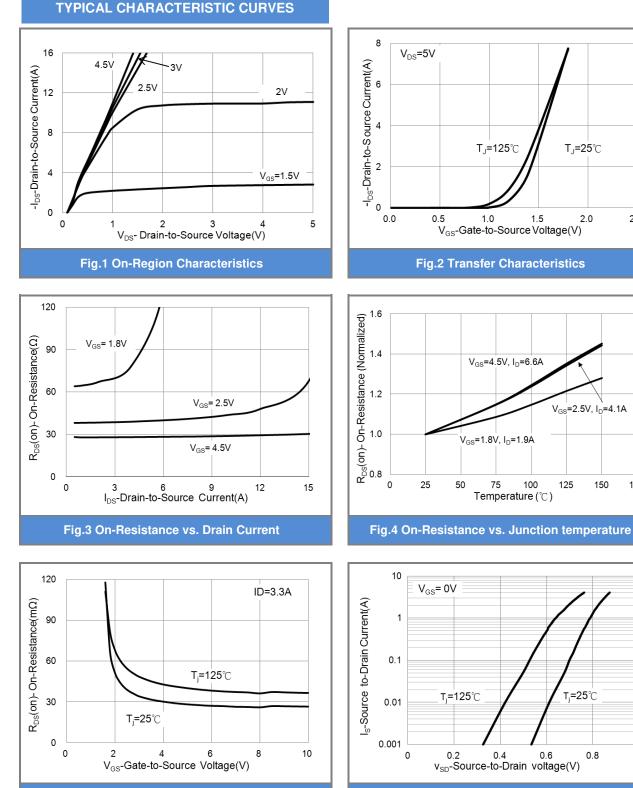
#### Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static		•				
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	20	-	_	V
Gate Threshold Voltage	$V_{GS(th)}$	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	0.5	0.74	1.2	V
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =6.6A	-	29	36	mΩ
		$V_{GS}=2.5V, I_{D}=4.1A$	-	40	52	
		V <sub>GS</sub> =1.8V, I <sub>D</sub> =1.9A	-	66	92	
Zero Gate Voltage Drain Current	IDSS	$V_{DS}=20V, V_{GS}=0V$	-	0.01	1	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = <u>+</u> 12V, V <sub>DS</sub> =0V	-	<u>+</u> 10	<u>+</u> 100	nA
Dynamic						
Total Gate Charge	Qg	V <sub>DS</sub> =10V, I <sub>D</sub> =6.6A, V <sub>GS</sub> =4.5V <sup>(Note 1,2)</sup>	-	4.1	-	nC
Gate-Source Charge	$Q_{gs}$		-	1.1	-	
Gate-Drain Charge	$Q_{gd}$		-	0.7	-	
Input Capacitance	Ciss	V <sub>DS</sub> =10V, V <sub>GS</sub> =0V, f=1.0MHZ	-	400	-	
Output Capacitance	Coss		-	54	-	pF
Reverse Transfer Capacitance	Crss	I=I.UMHZ	-	40	-	
Switching						
Turn-On Delay Time	td <sub>(on)</sub>		-	14	-	
Turn-On Rise Time	tr	V <sub>DD</sub> =10V, I <sub>D</sub> =6.6A, V <sub>GS</sub> =4.5V,	-	10	-	ns
Turn-Off Delay Time	td <sub>(off)</sub>		-	30	-	
Turn-Off Fall Time	tf	$R_G=6\Omega^{(Note 1,2)}$	-	7	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	ls		_	-	2.0	А
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =1.0A, V <sub>GS</sub> =0V	-	0.73	1.2	V

NOTES :

- 1. Pulse width <300us, Duty cycle <2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. ReJA is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
- 4. The maximum current rating is package limited







1

T\_=25℃

2.0

150

T,=25℃

0.8

**Fig.6 Body Diode Characteristics** 

175

2.5



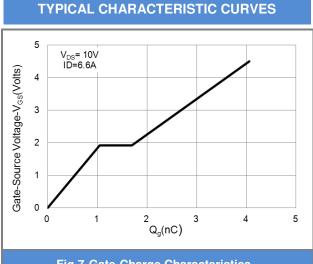


Fig.7 Gate-Charge Characteristics

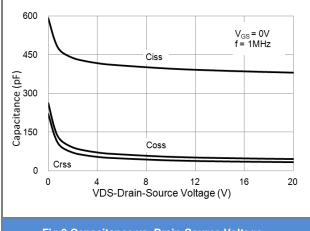


Fig.9 Capacitance vs. Drain-Source Voltage.

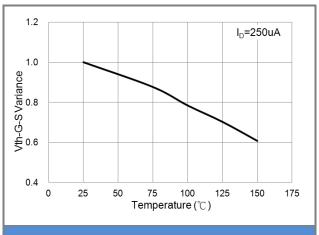


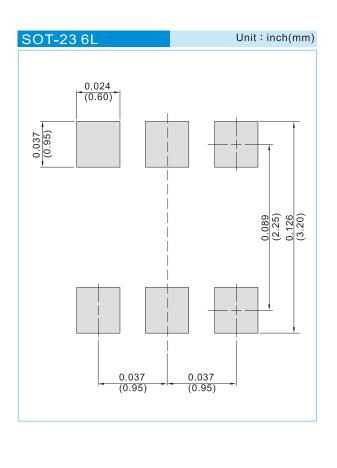
Fig.8 Threshold Voltage Variation with Temperature.



### PART NO. PACKING CODE VERSION

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJS6414_S1_00001	SOT-23 6L	3K pcs / 7" reel	S14	Halogen free RoHS compliant
PJS6414_S2_00001	SOT-23 6L	10K pcs / 13" reel	S14	Halogen free RoHS compliant

### **MOUNTING PAD LAYOUT**







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