

## **TPS62748EVM-706 Evaluation Module**

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This user's guide describes the characteristics, operation, and use of TI's TPS62748 evaluation module (EVM). This EVM is designed to help the user easily evaluate and test the operation and functionality of the TPS62748. The EVM converts a 2.0-V to 5.5-V input voltage to a regulated output voltage that is set either 1.2 V or 1.8 V at up to 300 mA. The TPS62748 also includes a load and has an ultra-low quiescent current of 360 nA. This user's guide includes setup instructions for the hardware, a printed-circuit board (PCB) layout for the EVM, a schematic diagram, and the bill of materials (BOM).

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## 1 Introduction

The TPS62748 is a 300-mA, synchronous, step-down converter in a tiny 1.6 x 0.9-mm, 8-ball DSBGA package. The output voltage is fixed inside the device by the connection of the VSEL pin.

### 1.1 Background

The TPS62748EVM-706 uses the TPS62748 device. The EVM operates with full-rated performance with an input voltage between 2.15 V and 5.5 V.

## 2 Evaluation Module Setup

This section describes how to properly use the TPS62748EVM-706.

### 2.1 Input/Output Connector Descriptions

<b>J1, Pin 1 and 2 – VIN</b>	Positive input connection from the input supply for the EVM
<b>J1, Pin 3 and 4 – S+/S-</b>	Input voltage sense connections. Measure the input voltage at this point.
<b>J1, Pin 5 and 6 – GND</b>	Return connection from the input supply for the EVM
<b>J2, Pin 1 and 2 – VOUT</b>	Output voltage connection
<b>J2, Pin 3 and 4 – S+/S-</b>	Output voltage sense connections. Measure the output voltage at this point.
<b>J2, Pin 5 and 6 – GND</b>	Output return connection
<b>J3 – SW and GND</b>	Measurement connection for the Switch Node, including Ground connection
<b>J4 - VOUT Sense</b>	Output Voltage SMA Measurement connection
<b>J5, Pin 1 and 2 - VSW</b>	Load switch output connection
<b>J5, Pin 3 and 4 - S+/S-</b>	Load switch output voltage sense connections. Measure the load switch output voltage at this point.
<b>J5, Pin 5 and 6 - GND</b>	Load switch output return connection
<b>JP1 - SW_CTRL</b>	CTRL pin input jumper. Place the supplied jumper across HIGH and CTRL to activate (close) the internal load switch. Place the jumper across LOW and CTRL to de-activate (open) the internal load switch.
<b>JP2 - VSEL</b>	Output Voltage Selection Pin. Chose VSEL = LOW for 1.2 VOUT or VSEL = HIGH for 1.8 VOUT.
<b>JP3 - VHIGH</b>	High Level Selection Jumper. The high level can be selected as VIN or VOUT.
<b>JP4 - EN</b>	EN pin input jumper. Place the supplied jumper across ON and EN to turn on the IC. Place the jumper across OFF and EN to turn off the IC.

### 2.2 Setup

To operate the EVM, set jumpers JP1 through JP4 to the desired positions per [Section 2.1](#). Connect the input supply to J1 and J3 and connect the load to J4 and J6.

### 3 Common Efficiency Measurement Errors with Ultra-Low Iq Devices

Efficiency is a common measurement for a power supply. With an ultra-low quiescent current device, such as the TPS62748, measurement errors can have a large impact on the measured efficiency, especially at very low load currents ( $< 100 \mu\text{A}$ ).

#### 3.1 Efficiency Measurement Setup

To accurately measure the efficiency of the TPS62748EVM-706, use the setup described in [SLVA236](#), Figure 6. The 'Additional Input Capacitor' referred to in that application note is not needed as C5 is already included on the TPS62748EVM-706. Any additional input capacitance is not recommended as it incurs increased leakage on the input which lowers the measured efficiency.

When measuring efficiency through the setup in [SLVA236](#), special care must be taken to remove the current consumed by the measurement instruments from the efficiency calculations. Such measurement instruments typically include the input voltage and output voltage multimeters as well as the input power supply's remote sense lines (if it has this capability). The current into these points affects the measured efficiency at very light loads. Two possible methods to overcome this are: 1) measuring the current into these points (measure the current into the multimeters and/or remote sense lines) and then subtracting this current from the efficiency calculation or 2) simply removing these instruments from the test setup. At very light load currents, it is typically best to remove the remote sense lines of the input power supply and then measure the current into the input and output voltage multimeters to get the most accurate efficiency measurement.

#### 3.2 Pullup and Pulldown Resistors

In addition to the input capacitor and remote sense lines noted in [Section 3.1](#), any pullup or pulldown resistors can draw significant current and affect the measured efficiency. For example, if the VSEL pin were pulled up to the input voltage with a 1-M $\Omega$  resistor and the pin were tied low through JP2, this would draw an extra 3.6  $\mu\text{A}$  from the input source at a 3.6-V input voltage. This would greatly affect the efficiency at very light loads. For this reason, no pullup or pulldown resistors have been used on the TPS62748EVM-706. The final application circuit should ensure that all digital inputs to the TPS62748 are terminated either high or low and not left floating, per the device data sheet.

## 4 Board Layout

This section provides the TPS62748EVM-706 board layout and illustrations.

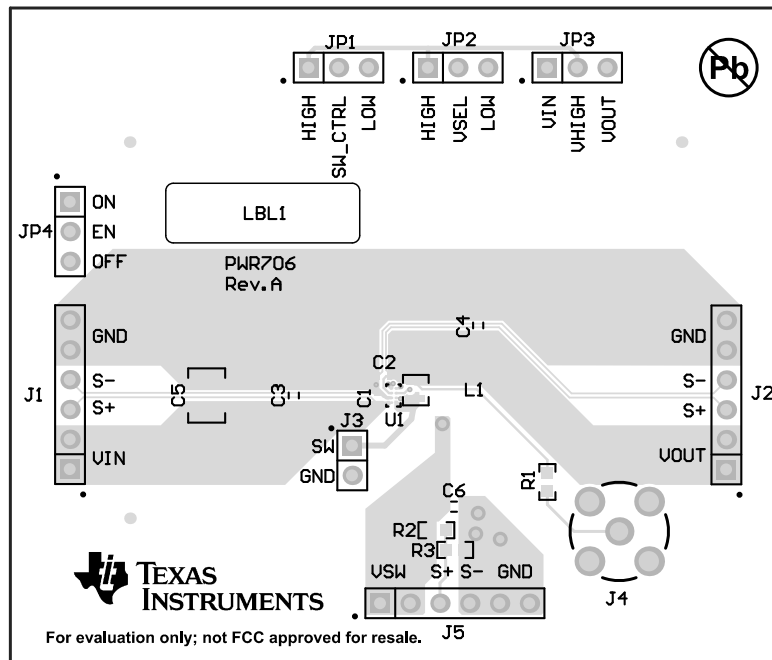


Figure 1. Assembly Layer

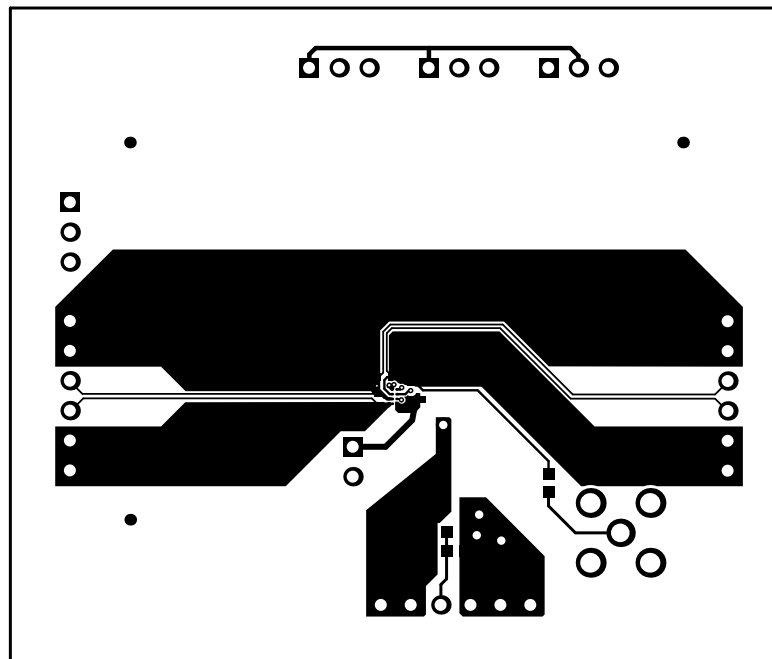


Figure 2. Top Layer

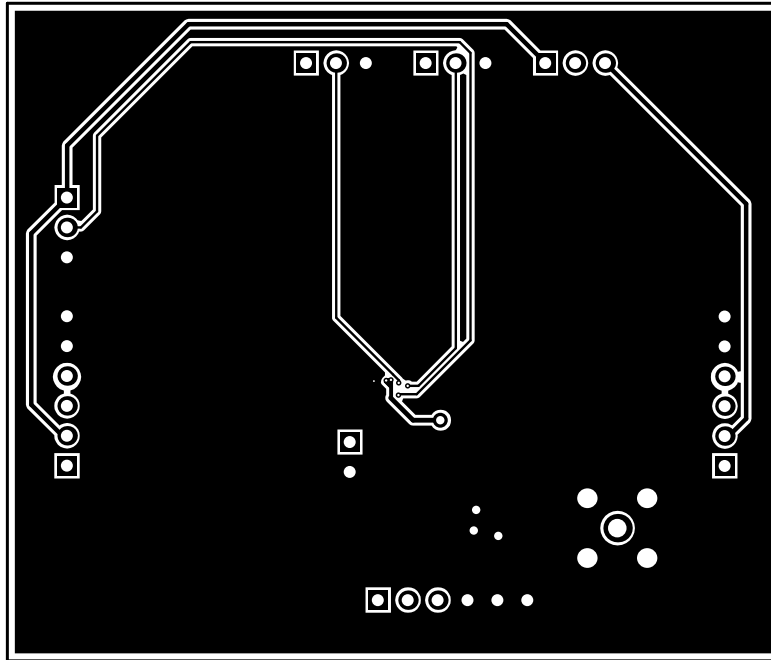


Figure 3. Bottom Layer

## 5 TPS62748EVM-706 Configuration

TPS62748 features an integrated VIN switch VINSW. This switch connects/disconnects the pin VINSW with the input of the TPS62748 VIN.

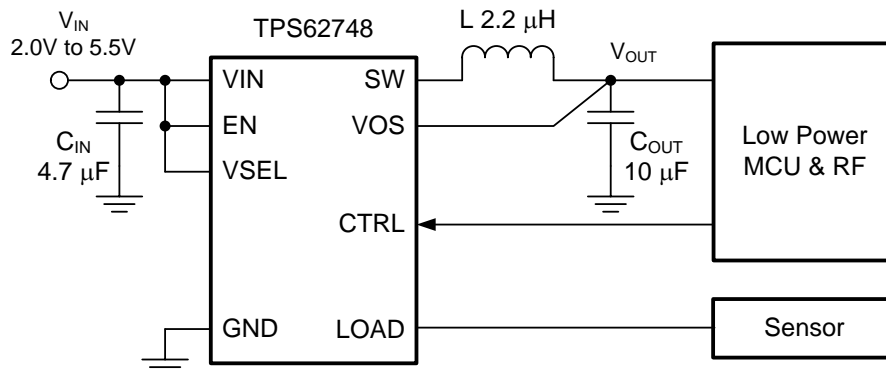


Figure 4. TPS62748 Typical Application Circuit

### 5.1 Schematic

Figure 5 shows the TPS62748EVM-706 schematic.

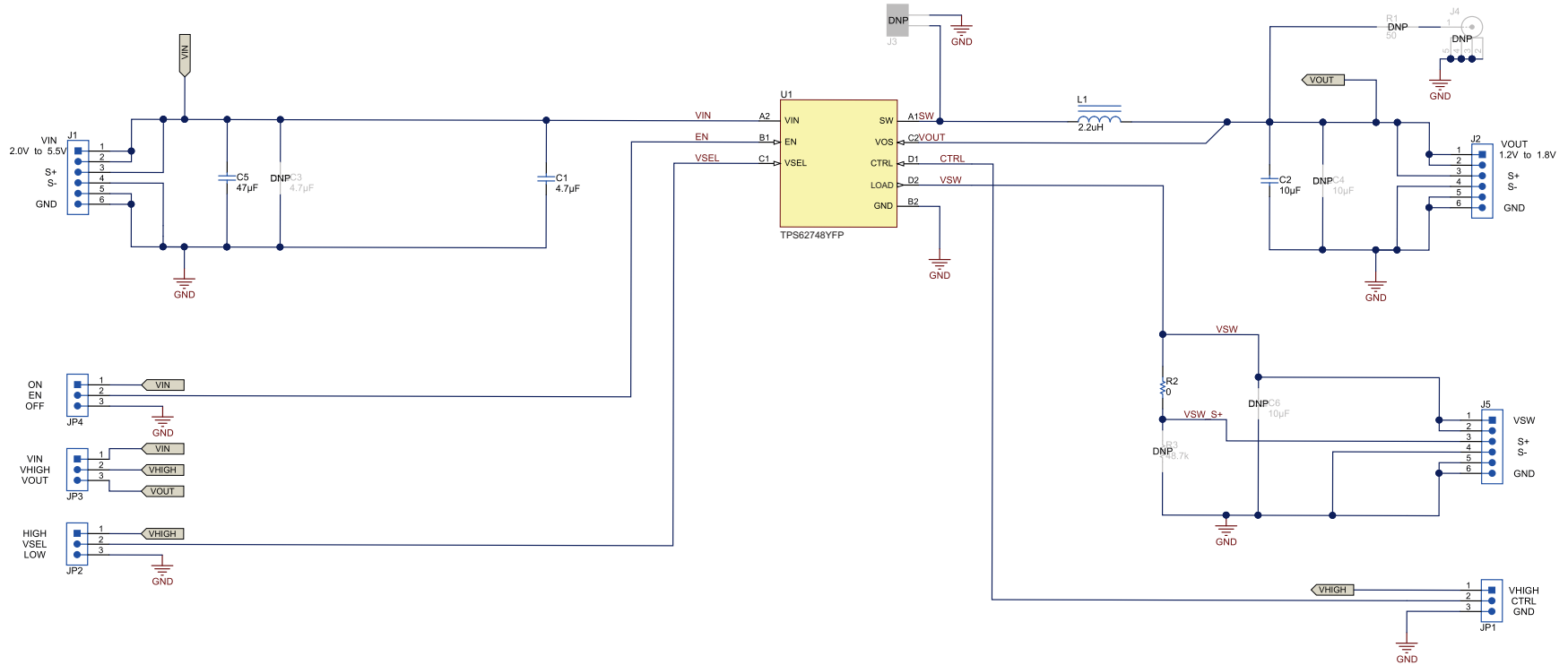


Figure 5. TPS62748EVM-706 Schematic

## 5.2 Bill of Materials

Table 1 lists the TPS62748EVM-706 BOM.

**Table 1. TPS62748EVM-706 Bill of Materials**

Count	RefDes	Value	Description	Size	Part Number	MFR
1	C1	4.7 $\mu$ F	Capacitor, Ceramic, X5R, 16V, 20%	0402	GRM155R61C475ME15	Murata
1	C2	10 $\mu$ F	Capacitor, Ceramic, X5R, 6.3V, 20%	0402	GRM155R60J106ME11	Murata
1	C5	47 $\mu$ F	Capacitor, Ceramic, X5R, 16V, 20%	1210	GRM32ER61C476ME15L	Murata
1	L1	2.2 $\mu$ H	Inductor, SMT, 1.2A, 204-m $\Omega$	2016	1285AS-H-2R2M	Toko
1	U1	TPS62748	IC, 360 nA I <sub>Q</sub> Step Down Converter with VIN Switch	1.6mm x 0.9mm	TPS62748YFP	TI

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- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
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2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
3. Use of EVMs only after User obtains the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to EVMs. Also, do not transfer EVMs, unless User gives the same notice above to the transferee. Please note that if User does not follow the instructions above, User will be subject to penalties of Radio Law of Japan.

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Only those TI components which TI has specifically designated as military grade or "enhanced plastic" are designed and intended for use in military/aerospace applications or environments. Buyer acknowledges and agrees that any military or aerospace use of TI components which have **not** been so designated is solely at the Buyer's risk, and that Buyer is solely responsible for compliance with all legal and regulatory requirements in connection with such use.

TI has specifically designated certain components as meeting ISO/TS16949 requirements, mainly for automotive use. In any case of use of non-designated products, TI will not be responsible for any failure to meet ISO/TS16949.

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