



## Bus Blaster v3

SKU: 102990047

### Description

Bus Blaster v3 is an experimental, high-speed JTAG debugger from Dangerous Prototypes

Thanks to a reprogrammable buffer, a simple update over USB makes Bus Blaster compatible with many different JTAG debugger types in the most popular open source software.

- Based on FT2232H with high-speed USB 2.0
- Buffered interface works with 3.3volt to 1.8volt targets
- Reprogrammable buffer is compatible with multiple debugger types
- Compatible with 'jtagkey', 'KT-link' programmer settings in OpenOCD,
- urJTAG, and more
- ships with JTAGkey compatible buffer image pre-programmed
- Should support Serial Wire Debug when available
- Mini-CPLD development board: self programmable, extra CPLD pins to header
- Open source (CC-BY-SA)

#### Updates in v3:

- Fitted in a DP8049 (80x49 mm) standard PCB  
([http://dangerousprototypes.com/docs/Sick\\_of\\_Beige\\_standard\\_PCB\\_sizes\\_v1.0](http://dangerousprototypes.com/docs/Sick_of_Beige_standard_PCB_sizes_v1.0)), case available here  
([http://www.seeedstudio.com/depot/bus-blaster-v3-acrylic-case-v1-sobv1dp8049-p-1315.html?cPath=178\\_184](http://www.seeedstudio.com/depot/bus-blaster-v3-acrylic-case-v1-sobv1dp8049-p-1315.html?cPath=178_184))
- Added series resistors to input and output pins to protect against damage and noise
- Swapped FT2232 clock output to CPLD pin with global clock feature for potential logic analyzer mode

**Each unit is tested before it ships.**