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Please note: As part of the Fairchild Semiconductor integration, some of the Fairchild orderable part numbers will need to change in order to meet ON Semiconductor's system requirements. Since the ON Semiconductor product management systems do not have the ability to manage part nomenclature that utilizes an underscore (_), the underscore (_) in the Fairchild part numbers will be changed to a dash (-). This document may contain device numbers with an underscore (_). Please check the ON Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.onsemi.com. Please email any questions regarding the system integration to Fairchild_questions@onsemi.com.

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June 2014



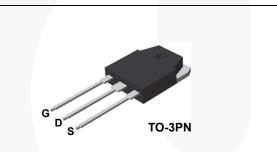
FQA24N60 N-Channel QFET[®] MOSFET 600 V, 23.5 A, 240 mΩ

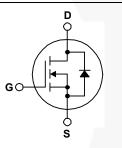
Description

This N-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, active power factor correction (PFC), and electronic lamp ballasts.

Features

- 23.5 A, 600 V, $R_{DS(on)}$ = 240 m Ω (Max.) @ V_{GS} = 10 V, I_{D} = 11.8 A
- Low Gate Charge (Typ. 110 nC)
- Low Crss (Typ. 56 pF)
- 100% Avalanche Tested





Absolute Maximum Ratings T_c = 25°C unless otherwise noted.

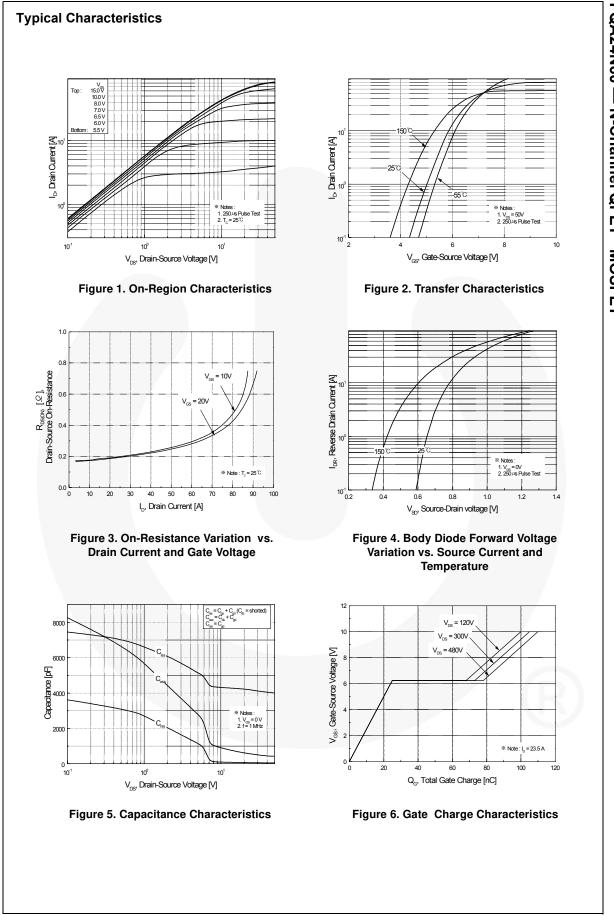
Symbol	Parameter	FQA24N60	Unit	
V _{DSS}	Drain-Source Voltage		600	V
I _D	Drain Current - Continuous ($T_C = 25^{\circ}C$)	23.5	А	
	- Continuous (T _C = 100°C)	14.9	A
DM	Drain Current - Pulsed	(Note 1)	94	А
V _{GSS}	Gate-Source Voltage	± 30	V	
AS	Single Pulsed Avalanche Energy	(Note 2)	1300	mJ
AR	Avalanche Current	(Note 1)	23.5	A
E _{AR}	Repetitive Avalanche Energy	(Note 1)	31	mJ
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	4.5	V/ns
D	Power Dissipation (T _C = 25°C) - Derate above 25°C		310	W
			2.5	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range	-55 to +150	°C	
ΓL	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds	300	°C	

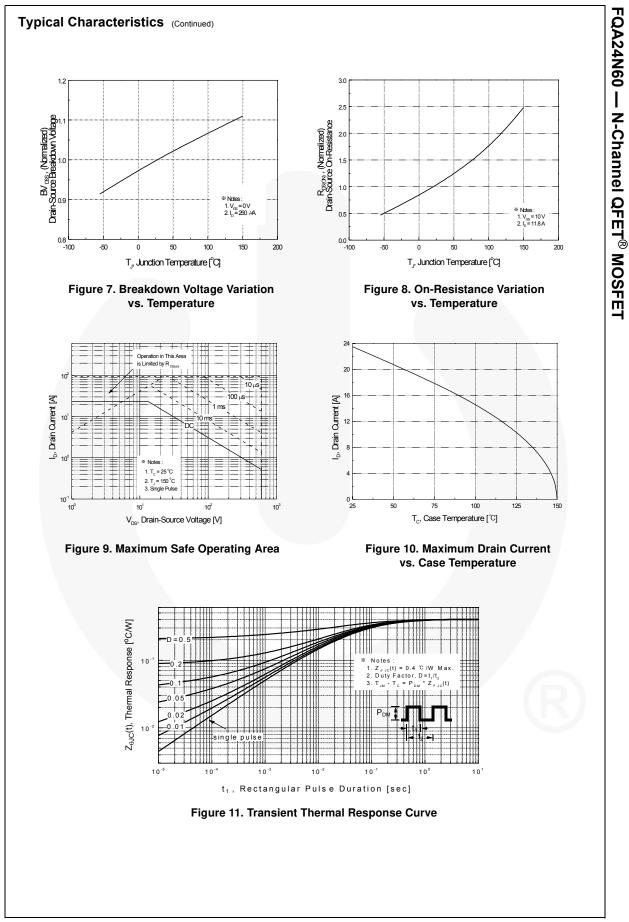
Thermal Characteristics

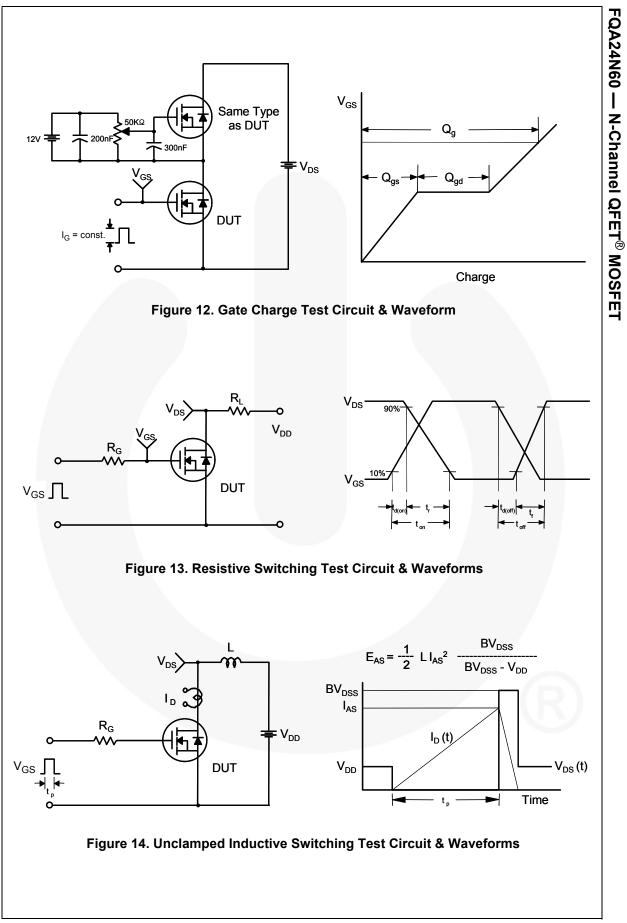
Symbol	Parameter	FQA24N60	Unit	
$R_{ ext{ heta}JC}$	Thermal Resistance, Junction-to-Case, Max.	0.4	°C/W	
$R_{\theta CS}$	Thermal Resistance, Case-to-Sink, Typ.	0.24	°C/W	
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient, Max.	40	°C/W	

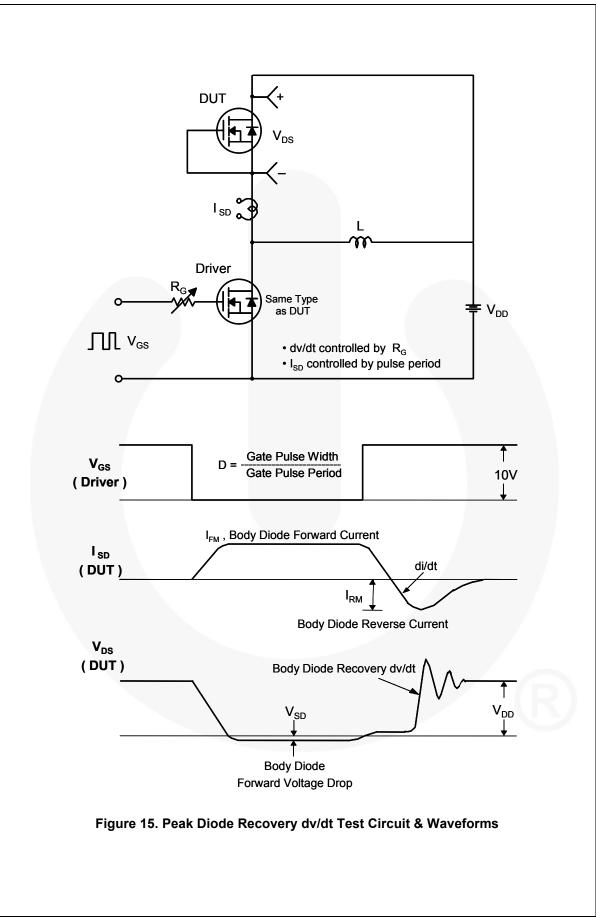
Part Nu	umber	Top Mark	Pack	age	Packing Method	Reel	Size	Tape W	idth	Quantity
FQA2	•		TO-3			A	N/A		30 units	
Electri	cal Ch	aracteristics	T., = 25°C un	less otherwis	se noted.					
Symbol		Parameter	C · · · ·		Test Conditions		Min.	Тур.	Max.	Unit
	aracteris						ļ			-
BV _{DSS}	1	ource Breakdown Vol	tage	$V_{GS} = 0$) V, I _D = 250 μA		600			V
∆BV _{DSS}		akdown Voltage Temperature					0.0		24/80	
/ ΔT _J	Coefficient		$I_D = 250 \ \mu A$, Referenced to $25^{\circ}C$			0.6		V/°C		
I _{DSS}	Zoro Co	Zero Gate Voltage Drain Current Gate-Body Leakage Current, Forward		V _{DS} = 6	600 V, V _{GS} = 0 V				10	μA
	Zelo Ga			V _{DS} = 480 V, T _C = 125°C					100	μA
I _{GSSF}	Gate-Bo			$V_{GS} = 30 V, V_{DS} = 0 V$					100	nA
I _{GSSR}	Gate-Bo	dy Leakage Current,	Reverse	V _{GS} = -	30 V, V _{DS} = 0 V				-100	nA
On Cha	aracteris	stics								
V _{GS(th)}		reshold Voltage		V _{DS} = V	V _{GS} , I _D = 250 μA	-	3.0		5.0	V
R _{DS(on)}	Static Dr On-Resi	ain-Source stance		V _{GS} = 1	10 V, I _D = 11.8 A			0.18	0.24	Ω
9 _{FS}	Forward	Transconductance		V _{DS} = 5	50 V, I _D = 11.8 A	_		22.5		S
	ie Cherr									
Dynam C _{iss}	1	acteristics pacitance		V - 2				4200	5500	pF
C _{oss}	-	Capacitance		V _{DS} = 25 V, V _{GS} = 0 V, f = 1.0 MHz			550	720	pF	
C _{rss}	•	Transfer Capacitanc	e	f = 1.0 MHz				56	75	pF
	-	racteristics		1			1			
t _{d(on)}		Delay Time			300 V, I _D = 23.5 A,			90	190	ns
t _r		Rise Time		R _G = 25	δΩ			270	550	ns
t _{d(off)}		Delay Time				(Note 4)		200	410	ns
t _f		Fall Time				()		170	350	ns
Q _g		te Charge			80 V, I _D = 23.5 A,			110 25	145	nC
Q _{gs}		urce Charge ain Charge		V _{GS} = 1		(Note 4)		53		nC nC
Q _{gd}	Gale-Dia					(1000-1)		55		
Drain-S	Source D	Diode Character	istics ar	nd Max	imum Ratings					
I _S	Maximur	aximum Continuous Drain-Source Diode Forward Current							23.5	А
I _{SM}	Maximur	n Pulsed Drain-Sour	ce Diode F						94	А
V _{SD}	Drain-Sc	ource Diode Forward	Voltage) V, I _S = 23.5 A				1.4	V
t _{rr}		Recovery Time) V, I _S = 23.5 A,			470		ns
Q _{rr}	Reverse	Recovery Charge		dl _F / dt	= 100 A/μs			6.2		μC
2. L = 4.3 mH 3. I _{SD} ≤ 23.5 A	, I _{AS} = 23.5 A, A, di/dt ≤ 200 /	width limited by maximum ju $V_{DD} = 50 V$, $R_{c} = 25 \Omega$, sta A/μ_s , $V_{DD} \le BV_{DSS}$, starting of operating temperature.	rting T _J = 25°							

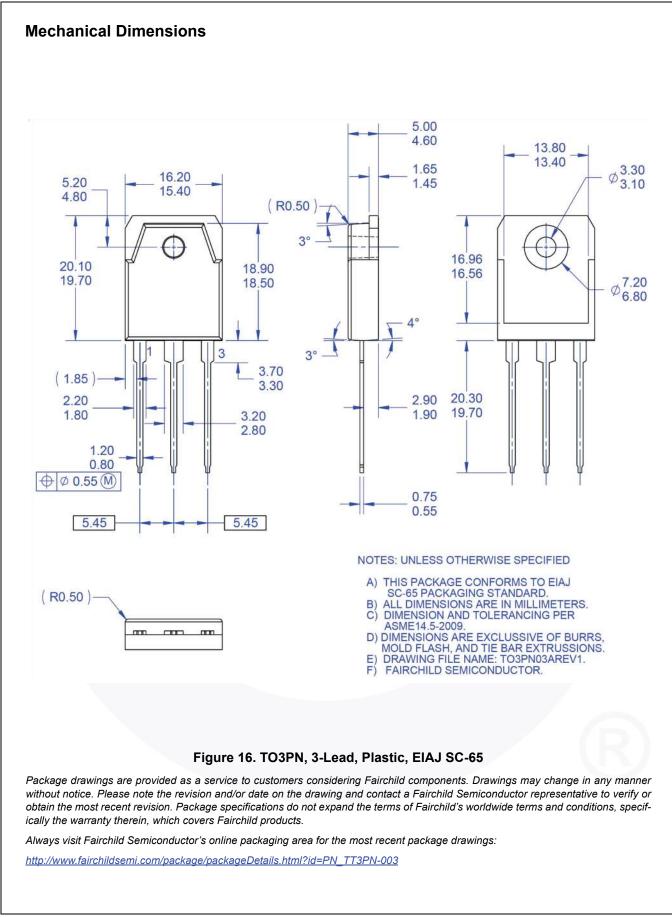
FQA24N60 — N-Channel QFET[®] MOSFET











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PRODUCT STATUS DEFINITIONS Definition of Terms

Datasheet Identification	Product Status	Definition			
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.			
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.			
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.			
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.			

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