

BUK6E2R3-40C

N-channel TrenchMOS intermediate level FET

Rev. 1 — 18 August 2010

Product data sheet

1. Product profile

1.1 General description

Intermediate level gate drive N-channel enhancement mode Field-Effect Transistor (FET) in a plastic package using advanced TrenchMOS technology. This product has been designed and qualified to the appropriate AEC Q101 standard for use in high performance automotive applications.

1.2 Features and benefits

- AEC Q101 compliant
- Suitable for intermediate level gate drive sources
- Suitable for thermally demanding environments due to 175 °C rating

1.3 Applications

- 12 V Automotive systems
- Electric and electro-hydraulic power steering
- Motors, lamps and solenoid control
- Start-Stop micro-hybrid applications
- Transmission control
- Ultra high performance power switching

1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C	-	-	40	V
I _D	drain current	$V_{GS} = 10 \text{ V}; T_{mb} = 25 \text{ °C};$ see Figure 1	-	-	120	Α
P _{tot}	total power dissipation	T _{mb} = 25 °C; see <u>Figure 2</u>	-	-	306	W
Static characteristics						
R _{DSon}	drain-source on-state resistance	$V_{GS} = 10 \text{ V}; I_D = 25 \text{ A};$ $T_j = 25 \text{ °C}; \text{ see } \frac{\text{Figure 11}}{\text{Figure 12}};$ see $\frac{\text{Figure 12}}{\text{Figure 12}}$	-	2	2.3	mΩ



Table 1. Quick reference data ...continued

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Avalanche	ruggedness					
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	$I_D = 120 \text{ A}; V_{sup} \le 40 \text{ V};$ $R_{GS} = 50 \Omega; V_{GS} = 10 \text{ V};$ $T_{j(init)} = 25 ^{\circ}C; \text{ unclamped}$	-	-	1.02	J
Dynamic characteristics						
Q_{GD}	gate-drain charge	$I_D = 25 \text{ A}$; $V_{DS} = 32 \text{ V}$; $V_{GS} = 10 \text{ V}$; see Figure 13; see Figure 14	-	72	-	nC

^[1] Continuous current is limited by package.

2. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		<u>-</u>
2	D	Drain	mb	D
3	S	source		G (EA)
mb	D	mounting base; connected to drain		mbb076 S
			SOT226 (I2PAK)	

3. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BUK6E2R3-40C	I2PAK	plastic single-ended package (I2PAK); TO-262	SOT226

4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
-				IVIIII		
V_{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C		-	40	V
V_{GS}	gate-source voltage	DC	<u>[1]</u>	-16	16	V
		Pulsed	[2]	-20	20	V
I_D	drain current	$T_{mb} = 25 ^{\circ}C; V_{GS} = 10 V; \text{see } \underline{\text{Figure 1}}$	[3]	-	120	Α
		$T_{mb} = 100 ^{\circ}C; V_{GS} = 10 V; \text{see } \frac{\text{Figure 1}}{}$	[3]	-	120	Α
I _{DM}	peak drain current	T_{mb} = 25 °C; $t_p \le 10 \mu s$; pulsed; see Figure 3		-	1006	Α
P _{tot}	total power dissipation	T _{mb} = 25 °C; see <u>Figure 2</u>		-	306	W
T _{stg}	storage temperature			-55	175	°C
Tj	junction temperature			-55	175	°C
Source-drain	diode					
Is	source current	T _{mb} = 25 °C	[3]	-	120	Α
I _{SM}	peak source current	$t_p \le 10 \ \mu s$; pulsed; $T_{mb} = 25 \ ^{\circ}C$		-	1006	Α
Avalanche rug	gedness					
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	I_D = 120 A; $V_{sup} \le$ 40 V; R_{GS} = 50 Ω; V_{GS} = 10 V; $T_{j(init)}$ = 25 °C; unclamped		-	1.02	J
E _{DS(AL)R}	repetitive drain-source avalanche energy		[4][5][6]	-	-	J

^{[1] -16}V accumulated duration not to exceed 168 hrs

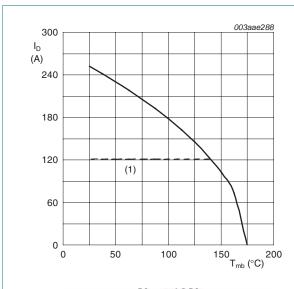
^[2] Accumulated pulse duration not to exceed 5mins.

^[3] Continuous current is limited by package.

^[4] Single-pulse avalanche rating limited by maximum junction temperature of 175 $^{\circ}$ C.

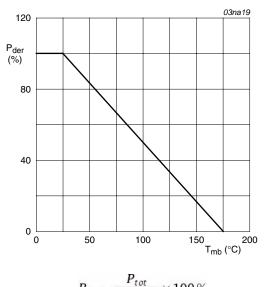
^[5] Repetitive avalanche rating limited by an average junction temperature of 170 °C.

^[6] Refer to application note AN10273 for further information.



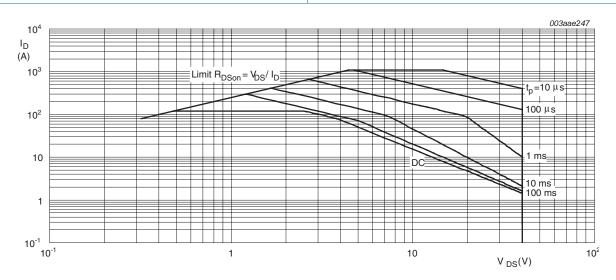
 $V_{GS} \ge 10\,V$ (1) Capped at 120 A due to package.

Continuous drain current as a function of mounting base temperature



$$P_{der} = \frac{P_{tot}}{P_{tot(25^{\circ}C)}} \times 100\%$$

Fig 2. Normalized total power dissipation as a function of mounting base temperature



 $T_{mb} = 25$ °C; I_{DM} is a single pulse

Safe operating area; continuous and peak drain currents as a function of drain-source voltage Fig 3.

5. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-mb)}	thermal resistance from junction to mounting base	see Figure 4	-	-	0.49	K/W
R _{th(j-a)}	thermal resistance from junction to ambient	vertical in free air	-	60	-	K/W

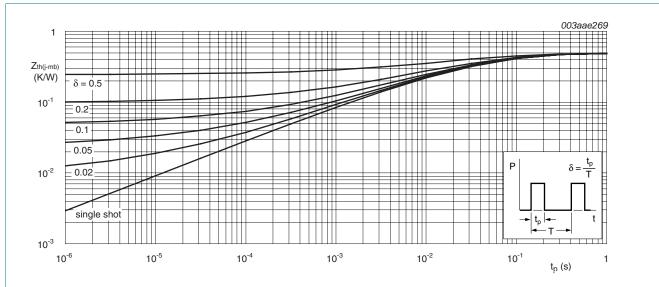


Fig 4. Transient thermal impedance from junction to mounting base as a function of pulse duration

6. Characteristics

Table 6. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics					
V _{(BR)DSS}	drain-source	$I_D = 250 \ \mu A; \ V_{GS} = 0 \ V; \ T_j = 25 \ ^{\circ}C$	40	-	-	V
	breakdown voltage	$I_D = 250 \ \mu A; \ V_{GS} = 0 \ V; \ T_j = -55 \ ^{\circ}C$	36	-	-	V
$V_{GS(th)}$ gate-source threshold voltage	gate-source threshold voltage	$I_D = 1$ mA; $V_{DS} = V_{GS}$; $T_j = 25$ °C; see <u>Figure 9</u> ; see <u>Figure 10</u>	1.8	2.3	2.8	V
		$I_D = 1$ mA; $V_{DS} = V_{GS}$; $T_j = -55$ °C; see <u>Figure 10</u>	-	-	3.3	V
		I_D = 2.5 mA; V_{DS} = V_{GS} ; T_j = 175 °C; see <u>Figure 10</u>	8.0	-	-	V
I _{DSS}	drain leakage current	$V_{DS} = 40 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 175 \text{ °C}$	-	-	500	μΑ
		$V_{DS} = 40 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	0.02	1	μΑ
I_{GSS}	gate leakage current	$V_{DS} = 0 \text{ V}; V_{GS} = 20 \text{ V}; T_j = 25 \text{ °C}$	-	2	100	nA
		$V_{DS} = 0 \text{ V}; V_{GS} = -20 \text{ V}; T_j = 25 \text{ °C}$	-	2	100	nA
R _{DSon} drain-source or resistance	drain-source on-state resistance	V_{GS} = 10 V; I_D = 25 A; T_j = 25 °C; see <u>Figure 11</u> ; see <u>Figure 12</u>	-	2	2.3	mΩ
		$V_{GS} = 5 \text{ V}; I_D = 25 \text{ A}; T_j = 25 \text{ °C};$ see <u>Figure 11</u> ; see <u>Figure 12</u>	-	2.5	3.1	mΩ
		V_{GS} = 4.5 V; I_D = 25 A; T_j = 25 °C; see <u>Figure 11</u> ; see <u>Figure 12</u>	-	2.8	3.6	mΩ
		$V_{GS} = 10 \text{ V}; I_D = 25 \text{ A}; T_j = 175 \text{ °C};$ see <u>Figure 11</u>	-	-	5	mΩ
Dynamic ch	naracteristics					
Q _{G(tot)} total gate charge		$I_D = 25 \text{ A}$; $V_{DS} = 32 \text{ V}$; $V_{GS} = 10 \text{ V}$; see <u>Figure 13</u> ; see <u>Figure 14</u>	-	260	-	nC
		$I_D = 25 \text{ A}$; $V_{DS} = 32 \text{ V}$; $V_{GS} = 5 \text{ V}$; see Figure 13; see Figure 14	-	147	-	nC
Q _{GS}	gate-source charge	$I_D = 25 \text{ A}; V_{DS} = 32 \text{ V}; V_{GS} = 10 \text{ V};$	-	38	-	nC
Q _{GD}	gate-drain charge	see Figure 13; see Figure 14	-	72	-	nC
C _{iss}	input capacitance	$V_{GS} = 0 \text{ V}; V_{DS} = 25 \text{ V}; f = 1 \text{ MHz};$	-	11.3	15.1	nF
C _{oss}	output capacitance	T _j = 25 °C; see <u>Figure 16</u>	-	1447	1750	рF
C _{rss}	reverse transfer capacitance		-	1014	1390	pF
t _{d(on)}	turn-on delay time	$V_{DS} = 30 \text{ V}; R_L = 1.2 \Omega; V_{GS} = 10 \text{ V};$	-	60	-	ns
r	rise time	$R_{G(ext)} = 10 \Omega$	-	140	-	ns
t _{d(off)}	turn-off delay time		-	234	-	ns
t _f	fall time		-	416	-	ns
L _D	internal drain inductance	from drain lead 6 mm from package to centre of die; $T_j = 25 ^{\circ}C$	-	4.5	-	nΗ
L _S	internal source inductance	from source lead to source bond pad; T _i = 25 °C	-	7.5	-	nΗ

Table 6. Characteristics ... continued

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Source-drain	n diode					
V _{SD}	source-drain voltage	$I_S = 25 \text{ A}; V_{GS} = 0 \text{ V}; T_j = 25 ^{\circ}\text{C};$ see <u>Figure 15</u>	-	0.8	1.2	V
t _{rr}	reverse recovery time	$I_S = 20 \text{ A}$; $dI_S/dt = -100 \text{ A/}\mu s$; $V_{GS} = 0 \text{ V}$;	-	63	-	ns
Q _r	recovered charge	V _{DS} = 25 V	-	127	-	nC

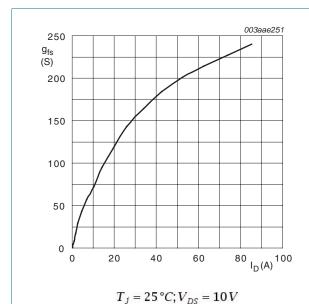


Fig 5. Forward transconductance as a function of drain current; typical values

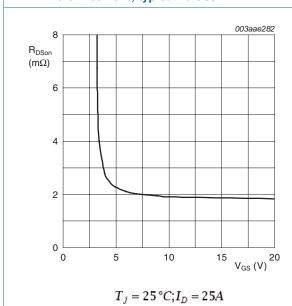


Fig 7. Drain-source on-state resistance as a function of gate-source voltage; typical values

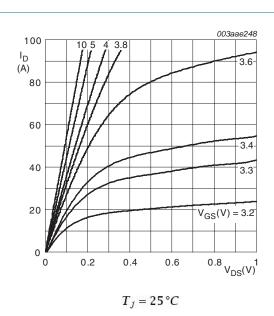


Fig 6. Output characteristics: drain current as a function of drain-source voltage; typical values

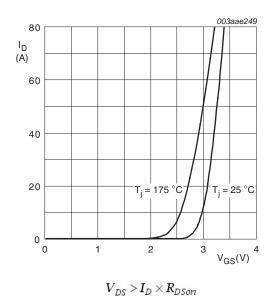


Fig 8. Transfer characteristics: drain current as a function of gate-source voltage; typical values

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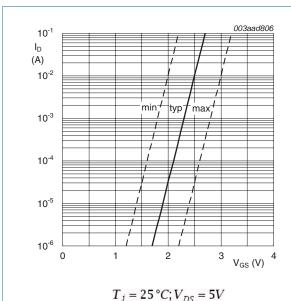


Fig 9. Sub-threshold drain current as a function of gate-source voltage

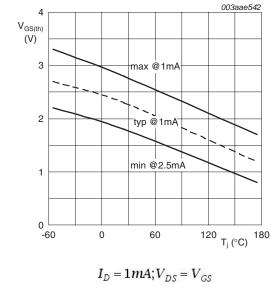


Fig 10. Gate-source threshold voltage as a function of junction temperature

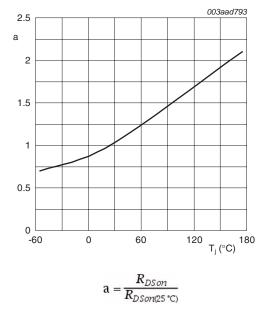


Fig 11. Normalized drain-source on-state resistance factor as a function of junction temperature

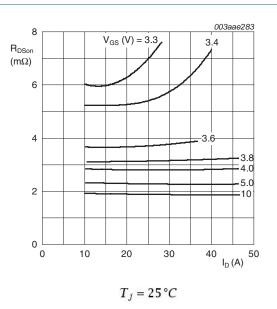
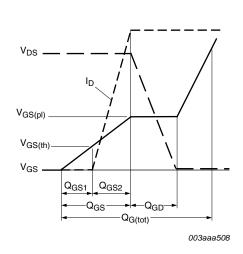
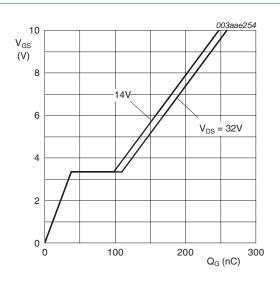


Fig 12. Drain-source on-state resistance as a function of drain current; typical values





 $T_j = 25 \,^{\circ}C; I_D = 25A$

Fig 13. Gate charge waveform definitions

Fig 14. Gate-source voltage as a function of gate charge; typical values

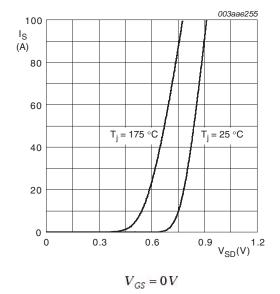
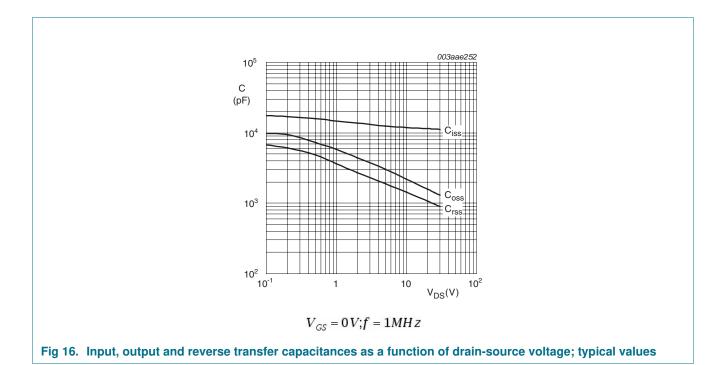


Fig 15. Source current as a function of source-drain voltage; typical values



7. Package outline

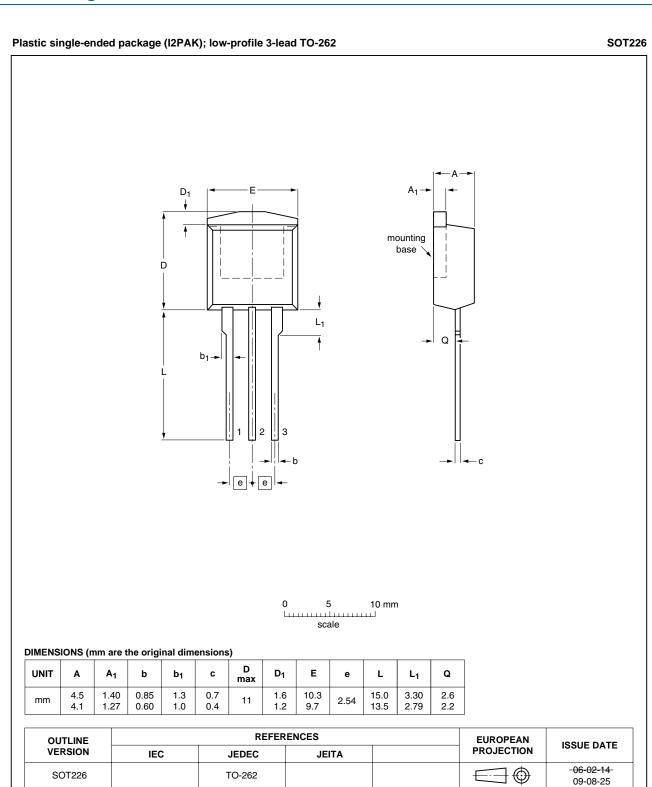


Fig 17. Package outline SOT226 (I2PAK)

8. Revision history

Table 7. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BUK6E2R3-40C v.1	20100818	Product data sheet	-	-

9. Legal information

9.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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Nexperia

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N-channel TrenchMOS intermediate level FET

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