Power MOSFET –30V, 98mΩ, –3.5A, Single P-Channel

This Power MOSFET is produced using ON Semiconductor's trench technology, which is specifically designed to minimize gate charge and low on resistance. This device is suitable for applications with low gate charge driving or low on resistance requirements.

Features

- Low On-Resistance
- 4V drive
- Pb-Free, Halogen Free and RoHS compliance
- Ultra small package MCPH6 (2.0mm×2.1mm×0.85mmt)

Typical Applications

• Load Switch

SPECIFICATIONS

ABSOLUTE MAXIMUM RATING at Ta = 25°C (Note 1, 2)

Parameter	Symbol	Value	Unit
Drain to Source Voltage	VDSS	-30	V
Gate to Source Voltage	VGSS	±20	V
Drain Current (DC)	ID	-3.5	Α
Drain Current (Pulse) PW ≤ 10µs, duty cycle ≤ 1%	IDP	-14	Α
Power Dissipation When mounted on ceramic substrate (1200mm² × 0.8mm)	PD	1.5	W
Junction Temperature	Tj	150	°C
Storage Temperature	Tstg	-55 to +150	°C

- Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.
 - be assumed, damage may occur and reliability may be affected.

 2: This product is designed to "ESD immunity<200V*", so please take care when handling.
 - *Machine Model

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Value	Unit
Junction to Ambient When mounted on ceramic substrate (1200mm² × 0.8mm)	R _{θJA}	83.3	°C/W

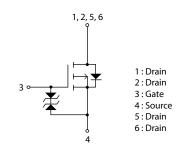


ON Semiconductor®

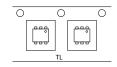
www.onsemi.com

VDSS	R _{DS} (on) Max	ID Max
	98mΩ@ –10V	
-30V	171mΩ@ –4.5V	-3.5A
	199mΩ@ –4V	

ELECTRICAL CONNECTION P-Channel



PACKING TYPE: TL MARKING





ORDERING INFORMATION

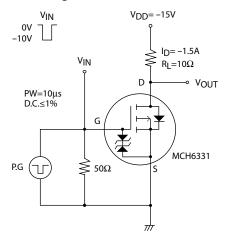
See detailed ordering and shipping information on page 5 of this data sheet.

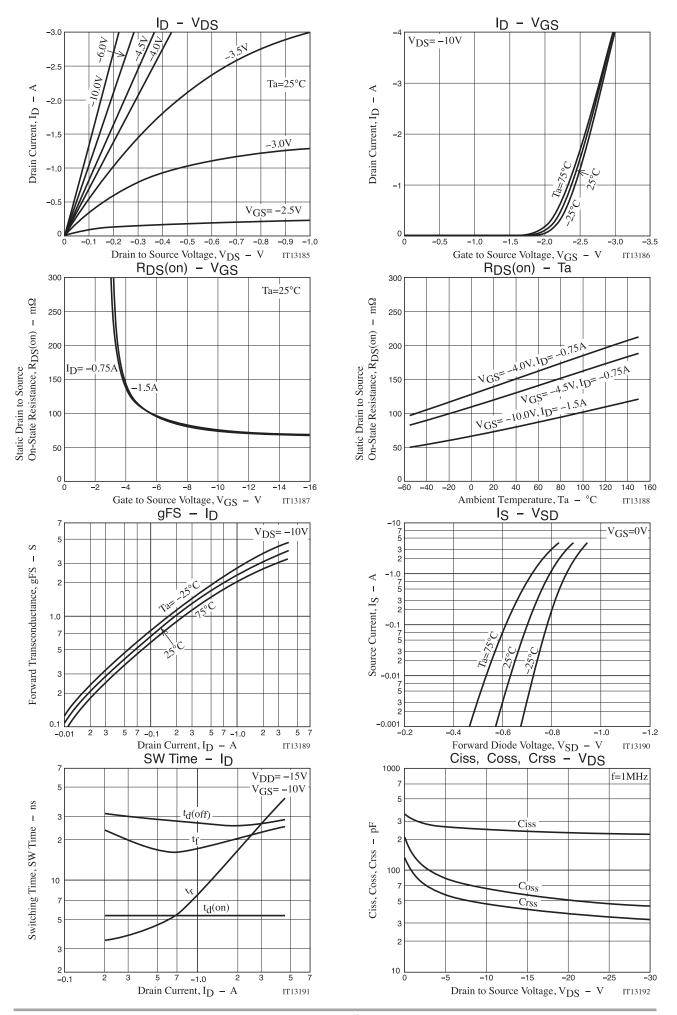
ELECTRICAL CHARACTERISTICS at Ta = 25°C (Note 3)

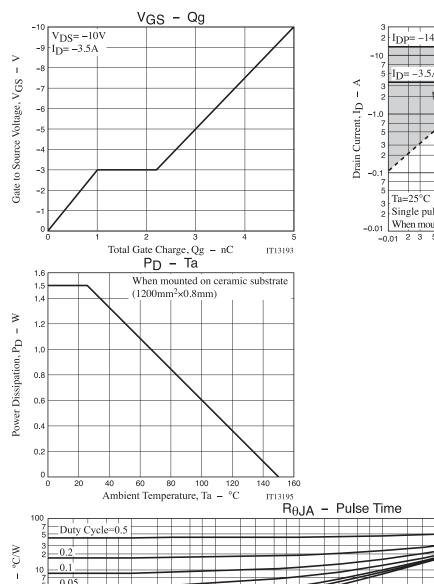
Daramatan	Current ed	O a malifitia ma	Value			1.1
Parameter	Symbol	Conditions	min	typ	max	Unit
Drain to Source Breakdown Voltage	V(BR)DSS	I _D =-1mA, V _G S=0V	-30			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =-30V, V _{GS} =0V			-1	μА
Gate to Source Leakage Current	IGSS	V _{GS} =±16V, V _{DS} =0V			±10	μА
Gate Threshold Voltage	VGS(th)	V _{DS} =-10V, I _D =-1mA	-1.2		-2.6	V
Forward Transconductance	gFS	V _{DS} =-10V, I _D =-1.5A	1.68	2.8		S
0.1.0.0.0.1	R _{DS} (on)1	I _D =-1.5A, V _G S=-10V		75	98	mΩ
Static Drain to Source On-State Resistance	R _{DS} (on)2	I _D =-0.75A, V _G S=-4.5V		122	171	mΩ
Resistance	R _{DS} (on)3	I _D =-0.75A, V _G S=-4V		142	199	mΩ
Input Capacitance	Ciss			250		pF
Output Capacitance Coss	V _{DS} =-10V, f=1MHz		65		pF	
Reverse Transfer Capacitance	Crss			46		pF
Turn-ON Delay Time	t _d (on)			5.4		ns
Rise Time	t _r	Con an arified Took Circuit		12		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit		26		ns
Fall Time	tf			19		ns
Total Gate Charge	Qg			5.0		nC
Gate to Source Charge	Qgs	V _{DS} =-10V, V _{GS} =-10V, I _D =-3.5A		1.0		nC
Gate to Drain "Miller" Charge	Qgd			1.2		nC
Forward Diode Voltage	V _{SD}	I _S =-3.5A, V _G S=0V		-0.86	-1.5	V

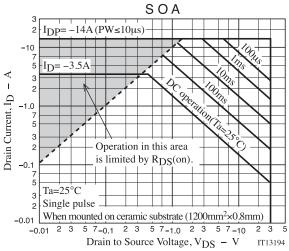
Note 3 : Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

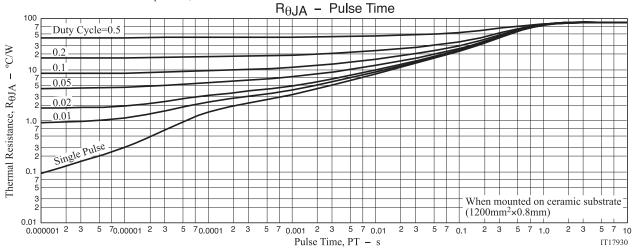
Switching Time Test Circuit







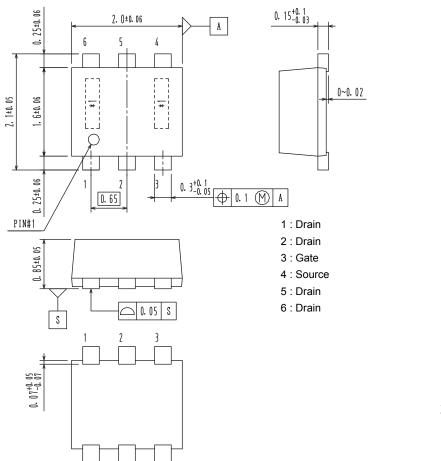




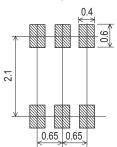
PACKAGE DIMENSIONS

unit : mm

SC-88FL / MCPH6 CASE 419AS ISSUE O



Recommended Soldering Footprint



ORDERING INFORMATION

ORDERING IN ORMATION						
Device Marking		Package	Shipping (Qty / Packing)			
MCH6331-TL-H	YF	SC-88FL / MCPH6 (Pb-Free / Halogen Free)	2 000 / Tana 8 Dani			
MCH6331-TL-W	TF		3,000 / Tape & Reel			

[†] For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D. http://www.onsemi.com/pub_link/Collateral/BRD8011-D.PDF

Note on usage: Since the MCH6331 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

ON Semiconductor and the ON logo are registered trademarks of Semiconductor Components Industries, LLC (SCILLC) or its subsidiaries in the United States and/or other countries. SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent re