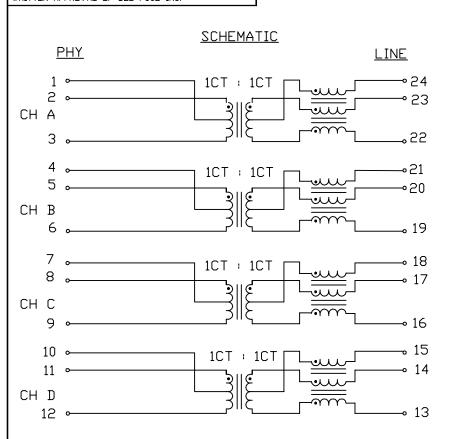
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ELECTRICAL SPECIFICATION @25°C





TURNS RATIO (2-1-3) : (23-24-22)1CT : 1CT (5-4-6) : (20-21-19) 1CT : 1CT (8-7-9):(17-18-16)1CT : 1CT (11-10-12) : (14-15-13)1CT : 1CT

POLARITY

PER DOT CONVENTION

@100kHz, 100mV, 19mA DC BIAS

120uH MIN

HIPUT

2250VDC @5SEC

INSERTION LOSS

FREQ(MHz)	1	2-300	400	500
MAX (dB)	0.3	1.0	1.5	2.0

RETURN LOSS (100 DHM LINESIDE LOAD)

FREQ(MHz)	1	2-100	200	500	600
MIN (dB)	18	20	15	12	9

COMM. TO DIFF. MODE REJECTION

FREQ(MHz)	50	100	200	300	400	500
MAX (dB)	48	42	36	33	30	28

DIFF, TO COMM, MODE REJECTION

FREQ(MHz)	50	100	200	300	400	500
MAX (dB)	30	27	24	22	21	20

BALANCED DC LINE CURRENT: 600mA NOMINAL CONTINUOUS 720mA MAXIMUM CONTINUOUS 1.2A MAXIMUM FOR 200mS

TEMPERATURE RANGE: 0 TO 70C

W INTERNAL LICE ONLY

* INTERNA	L O2F DNLA		DESIGNED FOR 10G POE	APPLI	.CATION	2	
ORIGINATED BY	DATE	TITLE	PART NO. / DRAWING NO.	STAND	ARD DIM.	[] METRIC D	IM. AS REF.
Alice Pang	2016-12-07		S558-10GB-15	TOL.	IN INCH	UNIT : INCH	[mm]
DRAWN BY	DATE	ELECTRICAL SPECIFICATION	FILE NAME	.X	1 /	SCALE: N/	
ZC Guo	2016-12-07		S55810GB15A.DWG	.XX	/	⊕ □	SIZE : A4

POWER I PROTECT I CONNECT

REV. : A

PAGE: 2

