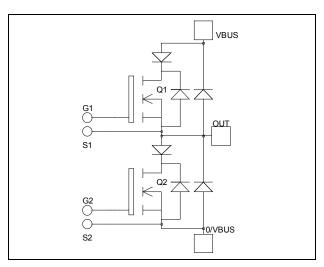
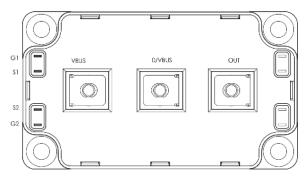


Phase leg Series & parallel diodes MOSFET Power Module





APTM50AM24SG

$V_{DSS} = 500V$ $R_{DSon} = 24m\Omega \text{ typ } @ \text{ Tj} = 25^{\circ}\text{C}$ $I_D = 150\text{A} @ \text{Tc} = 25^{\circ}\text{C}$

Application

- Motor control
- Switched Mode Power Supplies
- Uninterruptible Power Supplies

Features

- Power MOS 7[®] MOSFETs
 - Low R_{DSon}
 - Low input and Miller capacitance
 - Low gate charge
 - Fast intrinsic reverse diode
 - Avalanche energy rated
 - Very rugged
- Kelvin source for easy drive
 - Very low stray inductance
 - Symmetrical design
 - M5 power connectors
- High level of integration

Benefits

- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Low profile
- RoHS Compliant

All ratings (a) $T_i = 25^{\circ}C$ unless otherwise specified

Absolute maximum ratings

Symbol	Parameter		Max ratings	Unit
V _{DSS}	Drain - Source Breakdown Voltage		500	V
т	Cardina a Davia Carand	$T_c = 25^{\circ}C$	150	
I _D	Continuous Drain Current	$T_c = 80^{\circ}C$	110	А
I _{DM}	Pulsed Drain current		600	
V _{GS}	Gate - Source Voltage		±30	V
R _{DSon}	Drain - Source ON Resistance		28	mΩ
P _D	Maximum Power Dissipation $T_c = 25^{\circ}C$		1250	W
I _{AR}	Avalanche current (repetitive and non repetitive)		24	А
E _{AR}	Repetitive Avalanche Energy		30	mI
E _{AS}	Single Pulse Avalanche Energy		1300	mJ

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com



Electrical Characteristics

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
I _{DSS}	Zero Gate Voltage Drain Current	$V_{GS} = 0V, V_{DS} = 500V$			500	μA
R _{DS(on)}	Drain – Source on Resistance	$V_{GS} = 10V, I_D = 75A$		24	28	mΩ
V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 6mA$	3		5	V
I _{GSS}	Gate – Source Leakage Current	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$			± 600	nA

Dynamic Characteristics

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
C _{iss}	Input Capacitance	$V_{GS} = 0V$		19.6		
C _{oss}	Output Capacitance	$V_{\rm DS} = 25 V$		4.2		nF
C _{rss}	Reverse Transfer Capacitance	f = 1MHz		0.3		
Qg	Total gate Charge	$V_{GS} = 10V$		434		
Q _{gs}	Gate – Source Charge	$V_{Bus} = 250V$		120		nC
Q_{gd}	Gate – Drain Charge	$I_{\rm D} = 150 {\rm A}$		216		
T _{d(on)}	Turn-on Delay Time	Inductive switching @ 125°C		10		
Tr	Rise Time	$V_{GS} = 15V$		17		
T _{d(off)}	Turn-off Delay Time	$V_{Bus} = 333V$ $I_D = 150A$		50		ns
T_{f}	Fall Time	$R_{\rm G} = 0.8\Omega$		41		
Eon	Turn-on Switching Energy	Inductive switching @ $25^{\circ}C$		1.9		mI
E _{off}	Turn-off Switching Energy	$V_{GS} = 15V, V_{Bus} = 333V$ $I_D = 150A, R_G = 0.8\Omega$		1.5		mJ
Eon	Turn-on Switching Energy	Inductive switching @ 125°C		3.3		mĪ
E _{off}	Turn-off Switching Energy	$V_{GS} = 15V, V_{Bus} = 333V$ $I_D = 150A, R_G = 0.8\Omega$		1.7		mJ
R _{thJC}	Junction to Case Thermal Resistance				0.1	°C/W

Series diode ratings and characteristics

Symbol	Characteristic	cteristic Test Conditions		Min	Тур	Max	Unit
V _{RRM}	Maximum Peak Repetitive Reverse Volt	age		600			V
I _{RM}	Maximum Reverse Leakage Current	$V_{R} = 600V$				150	μA
I _F	DC Forward Current		$Tc = 80^{\circ}C$		200		Α
V	Diode Forward Voltage	$I_{\rm F} = 200 {\rm A}$	$T_i = 25^{\circ}C$		1.6	2	V
$V_{\rm F}$		$V_{GE} = 0V$	$T_{i} = 150^{\circ}C$		1.5		v
+	Pavaraa Paaavary Tima		$T_j = 25^{\circ}C$		125		
t _{rr}	Reverse Recovery Time		$T_{j} = 150^{\circ}C$		220		ns
0	Pawaraa Paaawary Charga	$V_{P} = 300V$	$T_j = 25^{\circ}C$		9.4		чC
Q _{rr}	Reverse Recovery Charge		$T_{j} = 150^{\circ}C$		19.8		μC
Б	Reverse Recovery Energy]	$T_j = 25^{\circ}C$		2.2		mI
Er			$T_{j} = 150^{\circ}C$		4.8		mJ
R _{thJC}	Junction to Case Thermal Resistance					0.39	°C/W



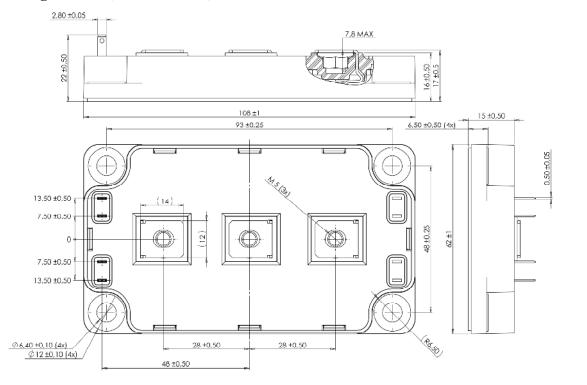
Parallel diode ratings and characteristics

	Characteristic	Test Conditions		Min	Тур	Max	Unit
V _{RRM}	Maximum Repetitive Reverse Voltage	e		600			V
I _{RM}	Maximum Reverse Leakage Current	$V_{R} = 600V$				350	μA
I _F	DC Forward Current		$T_c = 70^{\circ}C$		120		Α
	Diode Forward Voltage	$I_{\rm F} = 120 {\rm A}$			1.6	1.8	
V _F		$I_{\rm F} = 240 {\rm A}$			1.9		V
		$I_{\rm F} = 120 {\rm A}$	$T_{j} = 125^{\circ}C$		1.4		
+	t_{rr} Reverse Recovery Time $I_F = 120A$	1 120.4	$T_j = 25^{\circ}C$		130		20
ι _{rr}			$T_i = 125^{\circ}C$		170		ns
0		$V_{R} = 400V$ di/dt = 400A/µs	$T_i = 25^{\circ}C$		440		тC
Q _{rr}	Reverse Recovery Charge	$T_j = 125^{\circ}C$			1840		nC
R _{thJC}	Junction to Case Thermal Resistance					0.46	°C/W

Thermal and package characteristics

Symbol	Characteristic			Min	Max	Unit
V _{ISOL}	RMS Isolation Voltage, any terminal to case t =1 min, 50/60Hz			4000		V
T _J	Operating junction temperature range			-40	150	
T _{JOP}	Recommended junction temperature under s	witching condition	ıs	-40	T _J max -25	°C
T _{STG}	Storage Temperature Range		-40	125	C	
T _C	Operating Case Temperature	-40	100			
Torque	Mounting torque	To heatsink	M6	3	5	N.m
Torque	Mounting torque For terminals M5			2	3.5	19.111
Wt	Package Weight				300	g

SP6 Package outline (dimensions in mm)

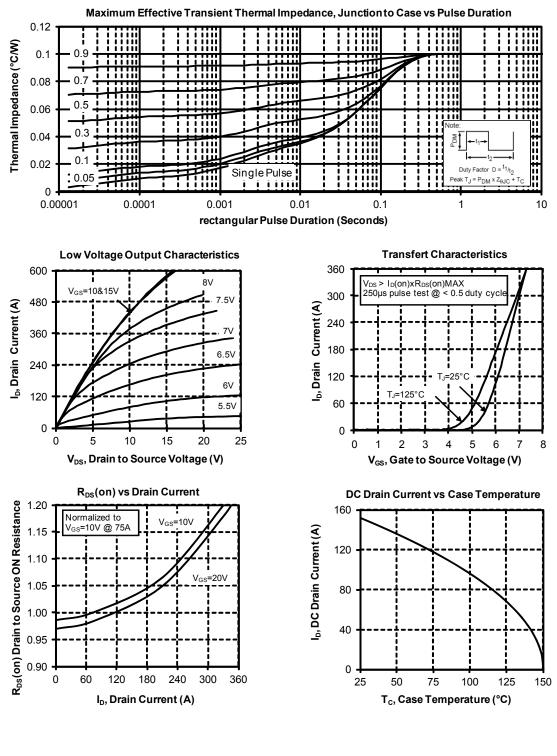


See application note APT0601 - Mounting Instructions for SP6 Power Modules on www.microsemi.com

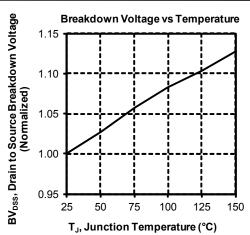
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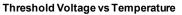


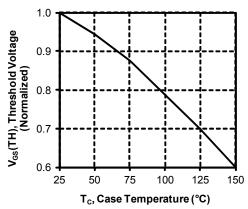
Typical Performance Curve

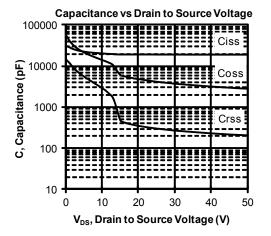


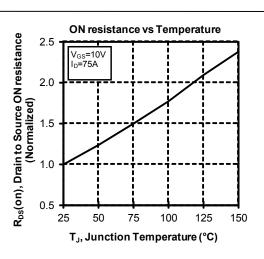




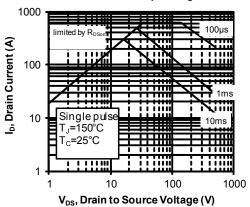


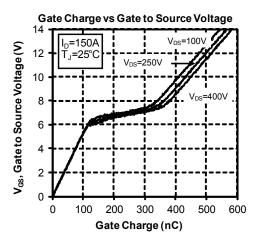






Maximum Safe Operating Area

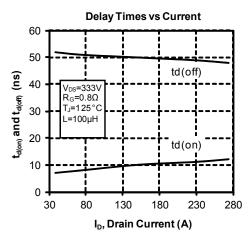




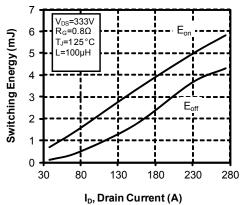
APTM50AM24SG-Rev 4 November, 2013

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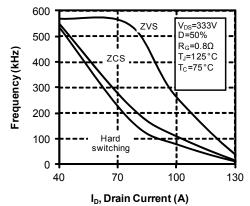


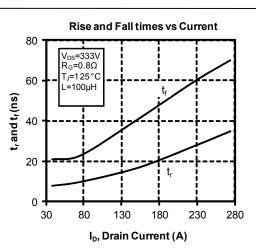




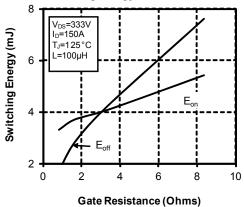








Switching Energy vs Gate Resistance



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