

TPS6282xEVM-005 Evaluation Module

The TPS6282xEVM-005 facilitates the evaluation of the TPS6282x 1-A, 2-A, and 3-A pin-to-pin compatible step-down converters with DCS-Control™ in a 2-mm × 1.5-mm QFN package. The BSR005-001 uses the 1-A TPS62821, the BSR005-002 uses the 2-A TPS62822, and the BSR005-003 uses the 3-A TPS62823. The EVMs output a 1.8-V output voltage with 1% accuracy from input voltages from 2.4 V to 5.5 V with a maximum solution height of 1 mm. The TPS6282x is a highly efficient and small solution for point-of-load (POL) converters in all types of equipment, such as solid-state drives (SSDs), computing, and factory automation.

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Introduction www.ti.com

1 Introduction

The TPS6282x are synchronous, step-down converters in a small $2- \times 1.5- \times 1$ -mm QFN package. Three different devices in this family support 1 A, 2 A, or 3 A of output current.

1.1 Performance Specification

Table 1 provides a summary of the TPS6282xEVM-005 performance specifications.

Table 1. Performance Specification Summary

Specification	Test Conditions	Min	Тур	Max	Unit
Input voltage		2.4	5	5.5	V
Output voltage setpoint			1.8		V
Output current	TPS62821EVM-005	0		1	Α
	TPS62822EVM-005	0		2	Α
	TPS62823EVM-005	0		3	Α

1.2 Modifications

Additional input and output capacitors can be added. Also, the loop response of the IC can be measured.

1.2.1 Input and Output Capacitors

C5 is provided for an additional input capacitor. This capacitor is not required for proper operation but can be used to reduce the input voltage ripple.

C7 and C8 are provided for additional output capacitors. These capacitors are not required for proper operation but can be used to reduce the output voltage ripple and to improve the load transient response. The total output capacitance must remain within the recommended range in the data sheet for proper operation.

1.2.2 Loop Response Measurement

The loop response of the TPS6282xEVM-005 can be measured by lifting both R1 and C3 and inserting a $50-\Omega$ resistor in series with these two components to inject the measurement signal across. The results of this test are shown in Figure 2.



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2 Setup

This section describes how to properly use the TPS6282xEVM-005.

2.1 Input/Output Connector Descriptions

J1, Pin 1 and 2 – VIN	Positive input connection from the input supply for the EVM.
J1, Pin 3 and $4 - S+/S-$	Input voltage sense connections. Measure the input voltage at this point.
J1, Pin 5 and 6 – GND	Input return connection from the input supply for the EVM.
J2, Pin 1 and 2 – VOUT	Output voltage connection
J2, Pin 3 and $4 - S+/S-$	Output voltage sense connections. Measure the output voltage at this point.
J2, Pin 5 and 6 – GND	Output return connection
J3 – PG/GND	The PG output appears on pin 1 of this header with ground on pin 2.
JP1 – EN	EN pin input jumper. Place the supplied jumper across ON and EN to turn on the IC. Place the jumper across OFF and EN to turn off the IC.
JP2 – PG Pullup Voltage	PG pin pullup voltage jumper. Place the supplied jumper on JP3 to connect the PG pin pullup resistor to V_{IN} . Alternatively, the jumper can be removed and a different voltage can be supplied on pin 2 to pull up the PG pin to a different level. This externally applied voltage must remain below 6 V.

2.2 Setup

To operate the EVM, set jumpers JP1 through JP2 to the desired position per Section 2.1. Connect the input supply to J1 and connect the load to J2.



3 TPS6282xEVM-005 Test Results

The TPS6282xEVM-005 was used to take all the data in *TPS6282x 2.4-V to 5.5-V, 1-, 2-, 3-A Step-Down Converter with 1% Accuracy.* See the device data sheet for the performance of this EVM.

Figure 1 shows the thermal performance of the EVM.

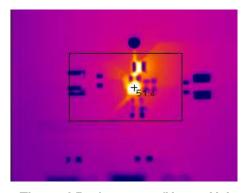


Figure 1. Thermal Performance ($V_{IN} = 5 \text{ V}, I_{OUT} = 3 \text{ A}$)

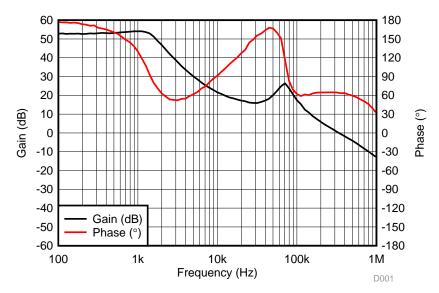


Figure 2. Loop Response Measurement (V_{IN} = 5 V, Load = 3 A)



www.ti.com Board Layout

4 Board Layout

This section provides the TPS6282xEVM-005 board layout and illustrations in Figure 3 through Figure 8. The Gerbers are available on the EVM product page: TPS62821EVM-005.

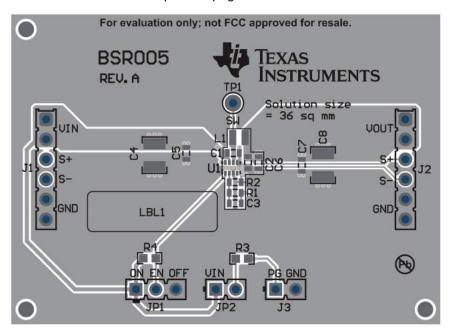


Figure 3. Top Assembly

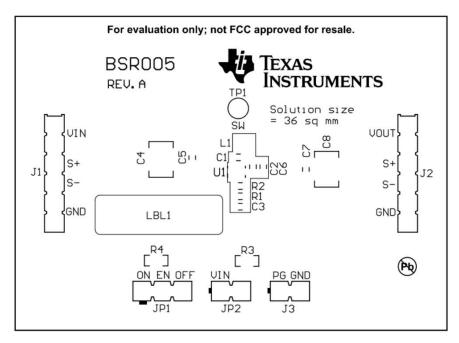


Figure 4. Top Overlay



Board Layout www.ti.com

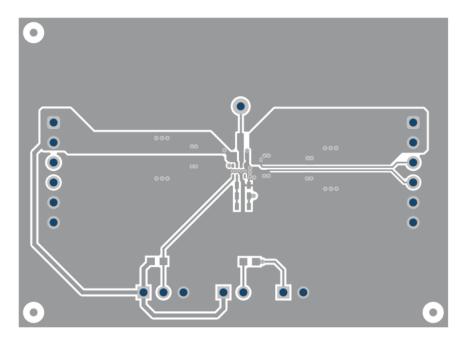


Figure 5. Top Layer

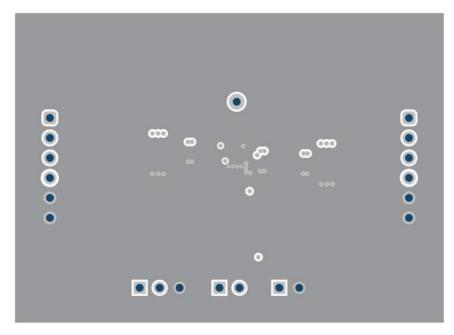


Figure 6. Signal Layer 1



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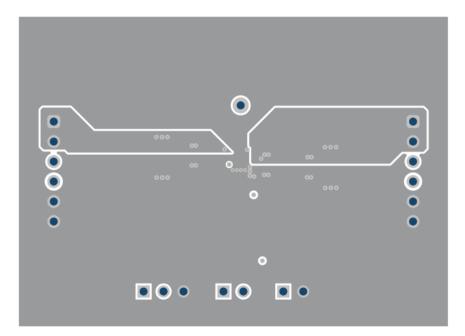


Figure 7. Signal Layer 2

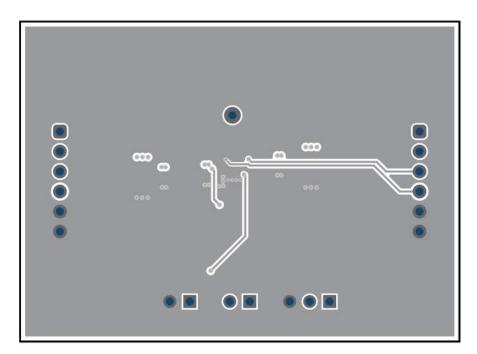


Figure 8. Bottom Layer



Schematic and Bill of Materials www.ti.com

5 Schematic and Bill of Materials

This section provides the TPS6282xEVM-005 schematic and bill of materials (BOM).

5.1 Schematic

Figure 9 illustrates the EVM schematic.

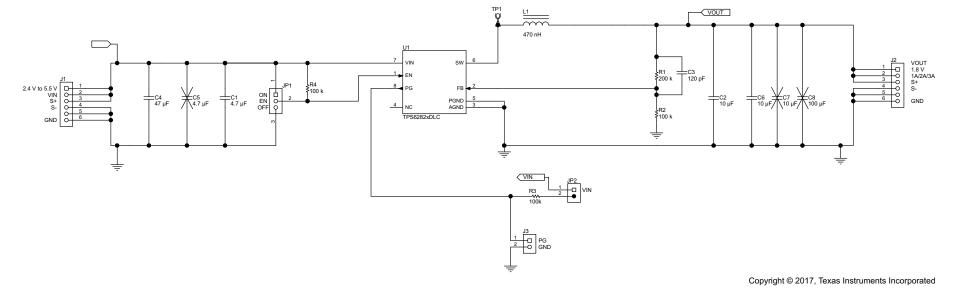


Figure 9. TPS6282xEVM-005 Schematic



www.ti.com Schematic and Bill of Materials

5.2 Bill of Materials

Table 2 lists the BOM for this EVM.

Table 2. TPS6282xEVM-005 Bill of Materials

	Count		Designator Value	Description	Package	Part Number	Manufacturer	
-001	-002	-003	Designator	value	Description	Reference	Part Number	Manufacturer
1	1	1	C1	4.7uF	CAP, CERM, 4.7 μF, 6.3 V, +/- 10%, X7R, 0603	0603	JMK107BB7475MA-T	Taiyo Yuden
2	2	2	C2, C6	10uF	CAP, CERM, 10 μF, 10 V, +/- 20%, X7R, 0603	0603	GRM188Z71A106MA73D	Murata
1	1	1	C3	120pF	CAP, CERM, 120 pF, 50 V, +/- 5%, C0G/NP0, 0603	0603	Std	Std
1	1	1	C4	47uF	CAP, CERM, 47 μF, 10 V, +/- 20%, X7R, 1210	1210	GRM32ER71A476ME15L	Murata
1	1	1	L1	470nH	Inductor, Shielded, 470 nH, 3.6 A, 0.032 ohm, SMD	2016	DFE201610E-R47M=P2	Murata
1	1	1	R1	200k	RES, 200 k, 1%, 0.1 W, 0603	0603	Std	Std
3	3	3	R2, R3, R4	100k	RES, 100 k, 1%, 0.1 W, 0603	0603	Std	Std
1	0	0	U1		2.4-V to 5.5-V, 1-, 2-, 3-A Step-Down Converter with 1% Accuracy	2x1.5mm	TPS62821DLC	Texas Instruments
0	1	0	U1		2.4-V to 5.5-V, 1-, 2-, 3-A Step-Down Converter with 1% Accuracy	2x1.5mm	TPS62822DLC	Texas Instruments
0	0	1	U1		2.4-V to 5.5-V, 1-, 2-, 3-A Step-Down Converter with 1% Accuracy	2x1.5mm	TPS62823DLC	Texas Instruments

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- · Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- · Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

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3.2.1 For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210 or RSS-247

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Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur

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