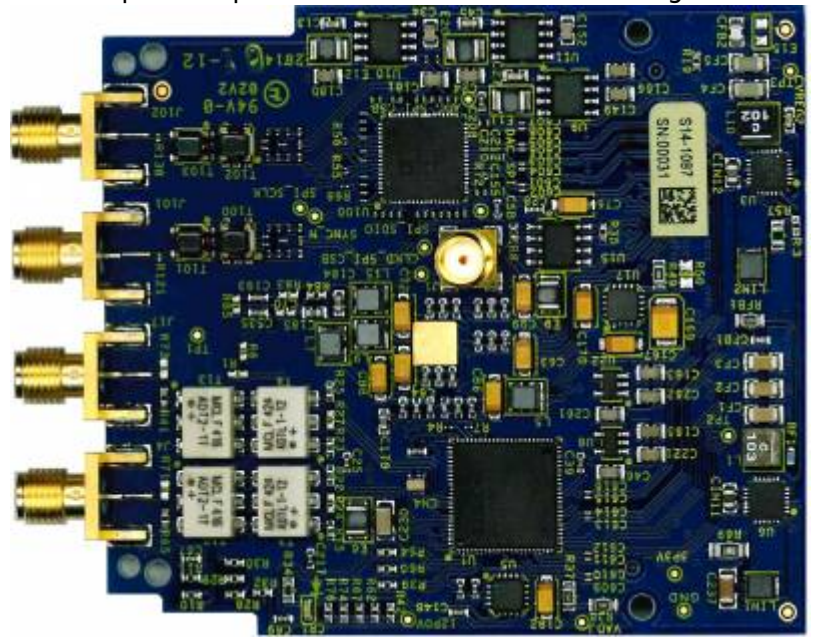


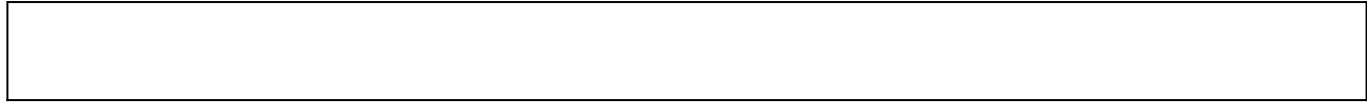
AD-FMCDQAQ2-EBZ User Guide

The AD-FMCDQAQ2-EBZ is an FMC board for the high speed DAC [AD9144](#) & ADC [AD9680](#). While the complete chip level design package can be found on the ADI product pages of these converters, information on the card, and how to use it, the design package that surrounds it, and the software which can make it work, can be found here.

The purpose of the AD-FMCDQAQ2-EBZ is a data acquisition platform that connects the analog world using FMC to the FPGA.



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All the boards described on this page include ESD (electrostatic discharge) sensitive devices. Electrostatic charges as high as *4000V* readily accumulate on the human body and test equipment and can discharge without detection.

Although the boards feature ESD protection circuitry, permanent damage may occur on devices subjected to high-energy electrostatic discharges. Therefore, proper ESD precautions are recommended to avoid performance degradation or loss of functionality.

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