

# 8A, 50V - 1000V High Efficient Rectifier

#### **FEATURES**

- AEC-Q101 qualified available
- · Glass passivated chip junction
- High efficiency, Low V<sub>F</sub>
- High current capability
- High reliability
- · High surge current capability
- RoHS Compliant
- Halogen-free according to IEC 61249-2-21

### **APPLICATIONS**

- DC to DC converter
- Switching mode converters and inverters
- Freewheeling application

#### **MECHANICAL DATA**

Case: TO-220AC

Molding compound meets UL 94V-0 flammability rating

• Terminal: Matte tin plated leads, solderable per J-STD-002

Mounting torque: 0.56 N·m maximum
Meet JESD 201 class 2 whisker test

Polarity: As marked

Weight: 1.80g (approximately)

KEY PARAMETERS				
PARAMETER	VALUE	UNIT		
l <sub>F</sub>	8	Α		
$V_{RRM}$	50 - 1000	V		
I <sub>FSM</sub>	150	Α		
$T_{JMAX}$	150	°C		
Package	TO-220AC			
Configuration	Single die			











DADAMETED	0)/11001	HERA								
PARAMETER	SYMBOL	801G	802G	803G	804G	805G	806G	807G	808G	UNIT
Marking code on the device		HERA 801G	HERA 802G	HERA 803G	HERA 804G	HERA 805G	HERA 806G	HERA 807G	HERA 808G	
Repetitive peak reverse voltage	$V_{RRM}$	50	100	200	300	400	600	800	1000	V
Reverse voltage, total rms value	$V_{R(RMS)}$	35	70	140	210	280	420	560	700	V
Forward current	$I_F$	8				Α				
Surge peak forward current 8.3ms single half sine wave superimposed on rated load	I <sub>FSM</sub>	150					А			
Junction temperature	TJ	-55 to +150					°C			
Storage temperature	T <sub>STG</sub>	-55 to +150					°C			

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THERMAL PERFORMANCE					
PARAMETER	SYMBOL	TYP	UNIT		
Junction-to-case resistance	R <sub>eJC</sub>	2	°C/W		

PARAMETER		CONDITIONS	SYMBOL	TYP	MAX	UNIT
<b>-</b> (1)	HERA801G HERA802G HERA803G HERA804G			-	1.0	V
Forward voltage <sup>(1)</sup>	HERA805G		V <sub>F</sub>	-	1.3	V
	HERA806G HERA807G HERA808G			-	1.7	V
- (2)		T <sub>J</sub> = 25°C		-	10	μΑ
Reverse current @ rated \	<b>/</b> R` ′	T <sub>J</sub> = 125°C	I <sub>R</sub>	-	400	μΑ
Junction capacitance	HERA801G HERA802G HERA803G HERA804G HERA805G	1MHz, V <sub>R</sub> = 4.0V	CJ	65	-	pF
	HERA806G HERA807G HERA808G			55	-	pF
Reverse recovery time	HERA801G HERA802G HERA803G HERA804G HERA805G	IF = 0.5A, IR = 1.0A Irr = 0.25A	t <sub>rr</sub>	-	50	ns
	HERA806G HERA807G HERA808G			-	80	ns

## Notes:

- 1. Pulse test with PW = 0.3ms
- 2. Pulse test with PW = 30ms

ORDERING INFORMATION					
ORDERING CODE <sup>(1)(2)</sup>	PACKAGE	PACKING			
HERA8xG	TO-220AC	50 / Tube			
HERA8xGH	TO-220AC	50 / Tube			

### Notes:

- 1. "x" defines voltage from 50V(HERA801G) to 1000V(HERA808G)
- 2. "H" means AEC-Q101 qualified



### **CHARACTERISTICS CURVES**

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$ 

Fig.1 Forward Current Derating Curve

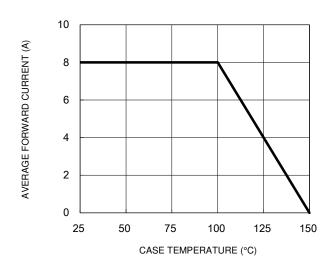


Fig.3 Typical Reverse Characteristics

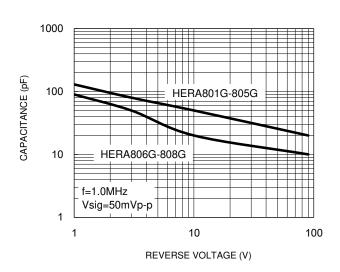
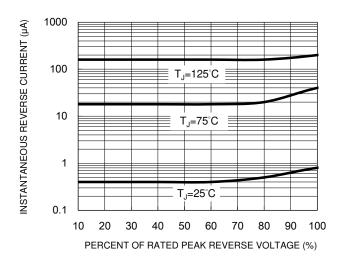


Fig.2 Typical Junction Capacitance

**Fig.4 Typical Forward Characteristics** 



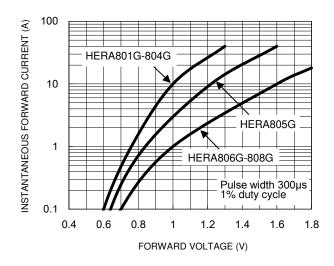
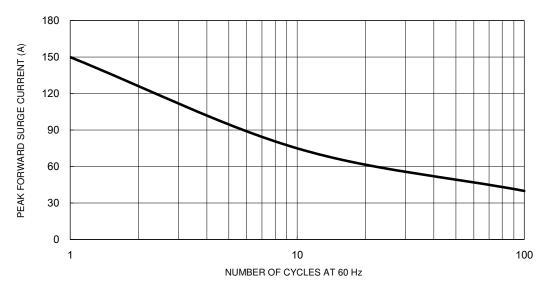


Fig.5 Maximum Non-Repetitive Forward Surge Current

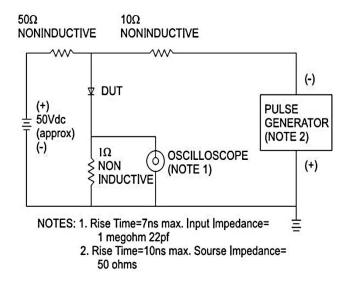


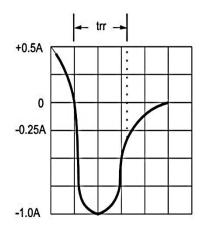
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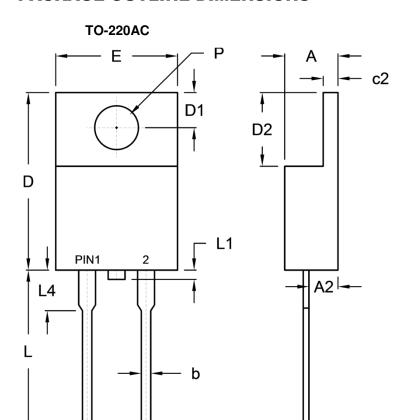
## Fig.6 Reverse Recovery Time Characteristic and Test Circuit Diagram







## **PACKAGE OUTLINE DIMENSIONS**



DIM.	Unit	(mm)	Unit (	(inch)
DIWI.	Min.	Max.	Min.	Max.
Α	4.42	4.76	0.174	0.187
A2	2.20	2.80	0.087	0.110
b	0.68	0.94	0.027	0.037
С	0.35	0.64	0.014	0.025
c2	1.14	1.40	0.045	0.055
D	14.60	16.00	0.575	0.630
D1	2.62	3.44	0.103	0.135
D2	5.84	6.86	0.230	0.270
E	-	10.50	-	0.413
e1	4.95	5.20	0.195	0.205
L	13.19	14.79	0.519	0.582
L1	0.00	1.60	0.000	0.063
L4	2.80	4.20	0.110	0.165
Р	3.54	4.00	0.139	0.157

## **MARKING DIAGRAM**



e1

P/N = Marking Code

С

G = Green Compound

YWW = Date Code F = Factory Code



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