

Features

- Shielded construction low radiation
- Bifilar wound
- Meets Open Alliance IEEE 100Base-T1 requirements
- AEC-Q200 compliant
- RoHS compliant* and halogen free**

2.5 ± 0.2 (.098 ± .008)

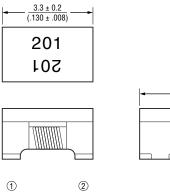
2.5 (.098) MAX.

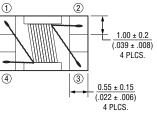
SRF3225TABM Series – Common Mode Chip Inductor

Electrical Specifications @ 25 °C

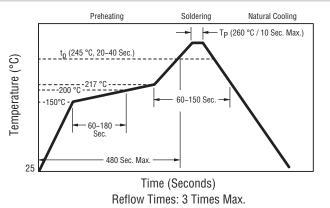
Bourns Part Number	Inductance @ 100 KHz / 0.1 V		Leakage Inductance (µH) Typ. @	DCR (Ω)	IDC (mA)	Cp 1-4 (pF) Max.	Common Mode Impedance (Ω) @ 10 MHz	
Fart Number	L (µH)	Tol. (%)	100 kHz / 0.1 V	Max.	Тур.	100 kHz / 0.1 V	Тур.	Min.
SRF3225TABM-201Y	200	+30/-10	0.40	5.5	70	20	10,000	7,000

Product Dimensions





Soldering Profile





* RoHS Directive 2015/863, Mar 31, 2015 and Annex.

** Bourns considers a product to be "halogen free" if (a) the Bromine (Br) content is 900 ppm or less; (b) the Chlorine (Cl) content is 900 ppm or less; and (c) the total Bromine (Br) and Chlorine (Cl) content is 1500 ppm or less.

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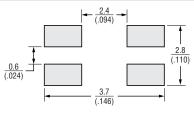
General Specifications

Rated Voltage
Insulation Resistance
10 MΩ (min.) @ 50 VDC
Operating Temperature
-40 °C to +125 °C
(Temperature rise included)
Storage Temperature
-40 °C to +125 °C
Temperature Rise 40 °C at rated IDC

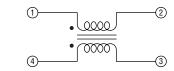
Materials

Core	Ferrite
Wire	Enameled copper
Terminal Finish	Sn
Packaging	2000 pcs. per 7-inch reel

Recommended Layout



Electrical Schematic



How to Order

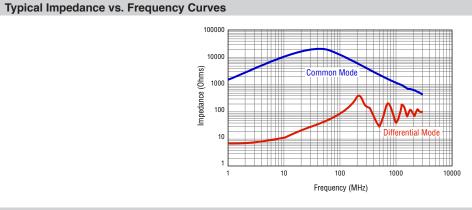


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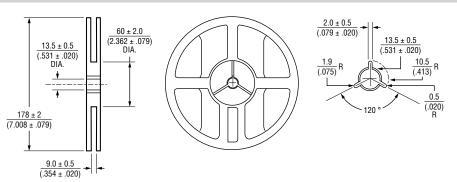
Asia-Pacific: Tel: +886-2 2562-4117 Email: asiacus@bourns.com EMEA: Tel: +36 88 885 877 Email: eurocus@bourns.com The Americas: Tel: +1-951 781-5500 Email: americus@bourns.com www.bourns.com

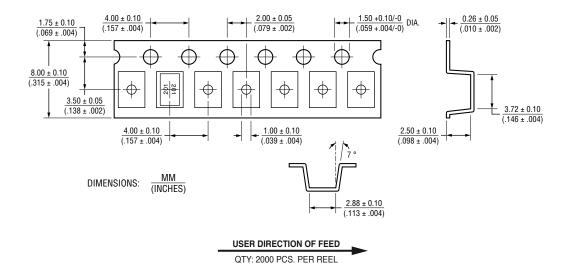
SRF3225TABM Series – Common Mode Chip Inductor

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Packaging Specifications





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