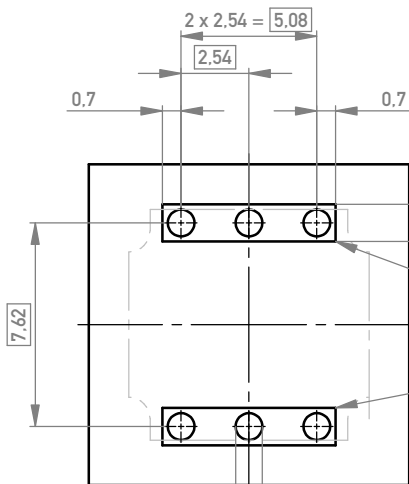


M 2:1

Leiterplattenbohrbild
PCB drillhole pattern



Dieser Bereich muß gleiches Potential auf der LP-Oberfläche haben

This area must have same electrical potential on surface of PCB

Schichtaufbau im metallisiertem Loch siehe Zeichnung 164062 Nr. 6 / 114124

diameter of drilled hole see drawing 164062 No 6 / 114124

1) $\phi 1,0^{+0,09}_{-0,04}$ Durchmesser des metallisierten Loches

$\phi 1,0^{+0,09}_{-0,06}$ Diameter of finished plated-through hole



alle Löcher/
all holes

214787	M4
214796	M3
214797	6-32UNC
214798	8-32UNC
Id.-Nr. Part No.	Gewinde Thread

Information:	Tolerances	Scale 5:1	Designation SVA 6-polig EE Power Bug 6 pin EE
	All Dimensions in mm		
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		d	23.11.2020
Index	Date	Class	EPSVA

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