Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOS VII)

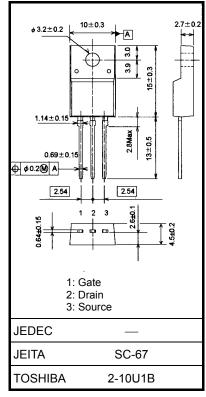
# TK6A65D

#### Switching Regulator Applications

- Low drain-source ON resistance:  $RDS(ON) = 0.95 \Omega$  (typ.)
- High forward transfer admittance:  $|Y_{fs}| = 4.0 \text{ S}$  (typ.)
- Low leakage current:  $I_{DSS} = 10 \ \mu A \ (max) \ (V_{DS} = 650 \ V)$
- Enhancement-mode:  $V_{th} = 2.0$  to 4.0 V ( $V_{DS} = 10 \text{ V}$ ,  $I_D = 1 \text{ mA}$ )

Characteristics			Symbol	Rating	Unit	
Drain-source voltage			V <sub>DSS</sub>	650	V	
Gate-source voltage			V <sub>GSS</sub>	±30	V	
Drain current	DC (N	ote 1)	ID	6	А	
	Pulse (N	ote 1)	I <sub>DP</sub>	24	A	
Drain power dissipation (Tc = $25^{\circ}$ C)			PD	45	W	
Single pulse avalanche energy (Note 2)			E <sub>AS</sub>	281	mJ	
Avalanche current			I <sub>AR</sub>	6	А	
Repetitive avalanche energy (Note 3)			E <sub>AR</sub>	4.5	mJ	
Channel temperature			T <sub>ch</sub>	150	°C	
Storage temperature range			T <sub>stg</sub>	-55 to 150	°C	

#### Absolute Maximum Ratings (Ta = 25°C)



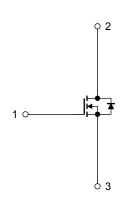
Weight : 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	2.78	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	62.5	°C/W

Internal Connection



Start of commercial production 2012-01

Note 1: Please use devices on conditions that the channel temperature is below 150°C.

Note 2: V<sub>DD</sub> = 90 V, T<sub>ch</sub> = 25°C (initial), L = 13.8 mH, R<sub>G</sub> = 25  $\Omega$ , I<sub>AR</sub> = 6 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic sensitive device. Please handle with caution.

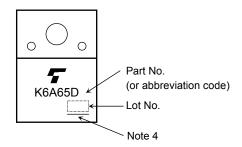
Electrical Characteristics (Ta = 25°C)

Char	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I <sub>GSS</sub>	$V_{GS}=\pm 30~V,~V_{DS}=0~V$			±1	μA
Drain cut-off current		I <sub>DSS</sub>	$V_{DS} = 650 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			10	μA
Drain-source breakdown voltage		V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	650			V
Gate threshold voltage		V <sub>th</sub>	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$	2.0		4.0	V
Drain-source ON	resistance	R <sub>DS (ON)</sub>	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 3 \text{ A}$		0.95	1.11	Ω
Forward transfer	admittance	Y <sub>fs</sub>	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 3 \text{ A}$	1.0	4.0		S
Input capacitance		C <sub>iss</sub>			1050		
Reverse transfer capacitance		C <sub>rss</sub>	$V_{DS} = 25 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}$		5		pF
Output capacitance		C <sub>oss</sub>		_	100		
Switching time	Rise time	tr	$V_{GS} \\ 0 \\ V \\ 50 \\ \Omega \\ 0 \\ V \\ V$		25	_	- ns
	Turn-on time	t <sub>on</sub>			60		
	Fall time	t <sub>f</sub>			10	_	
	Turn-off time	t <sub>off</sub>	$V_{DD} \approx 200 \text{ V}$ Duty $\leq$ 1%, $t_W = 10 \ \mu s$		75	_	
Total gate charge		Qg			20		
Gate-source charge		Q <sub>gs</sub>	V <sub>DD</sub> ≈ 400 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 6 A		13		nC
Gate-drain charge		Q <sub>gd</sub>	]	_	7	—	

## Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	—	_	_	6	А
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	—	_		24	А
Forward voltage (diode)	V <sub>DSF</sub>	$I_{DR} = 6 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	$I_{DR} = 6 \text{ A}, V_{GS} = 0 \text{ V},$	_	1300	_	ns
Reverse recovery charge	Qrr	dl <sub>DR</sub> /dt = 100 A/μs		10		μC

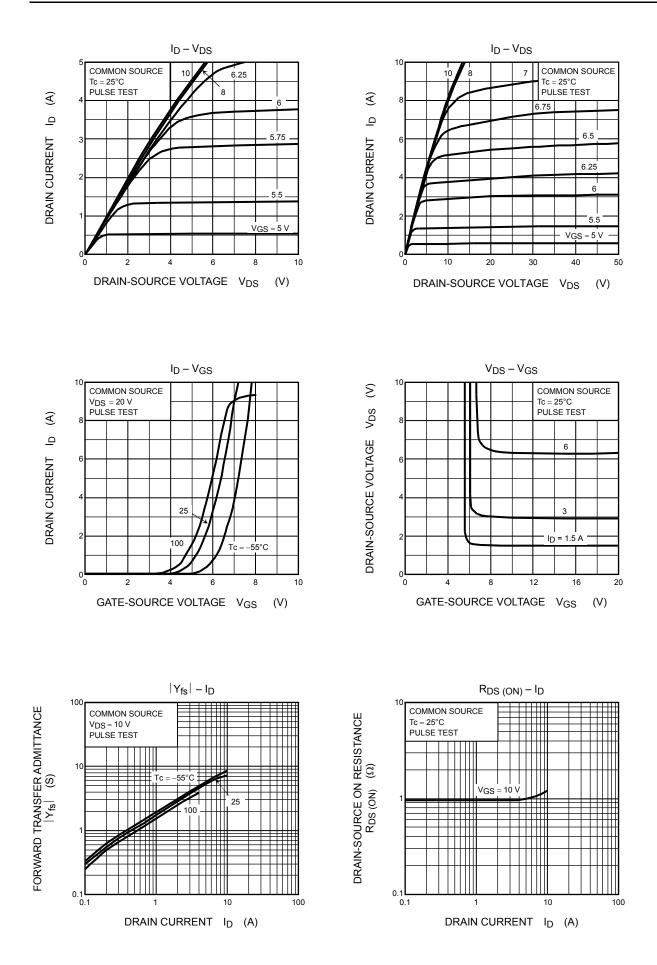
## Marking



Note 4 : A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

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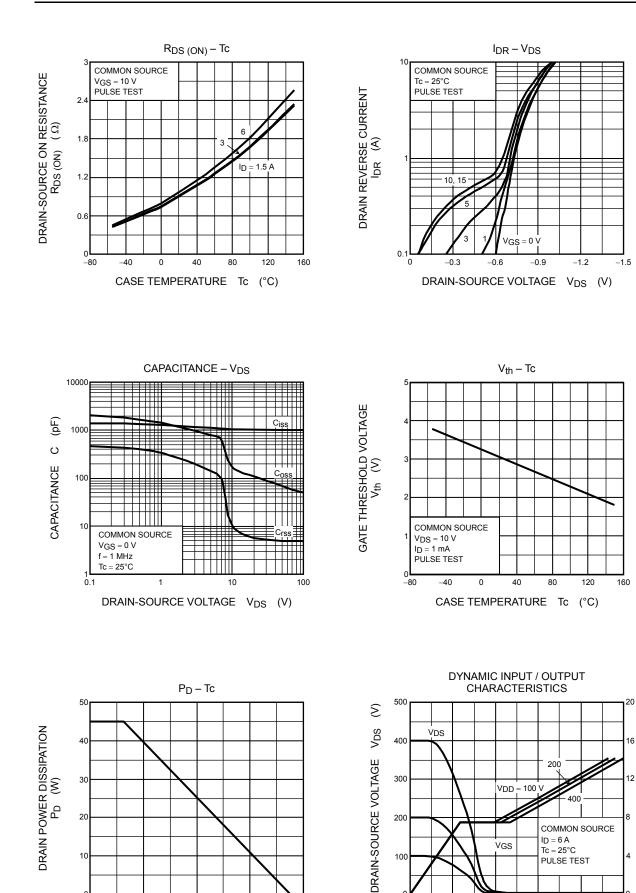
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80

CASE TEMPERATURE Tc (°C)

120

160



n

30

S

V<sub>GS</sub>

GATE-SOURCE VOLTAGE

PULSE TEST

24

(nC)

18

100

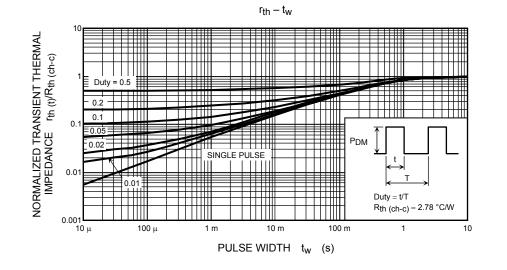
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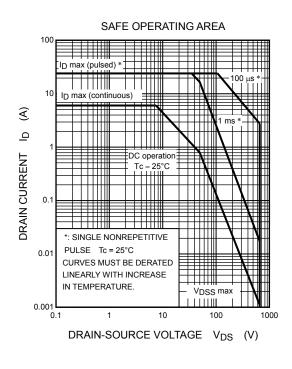
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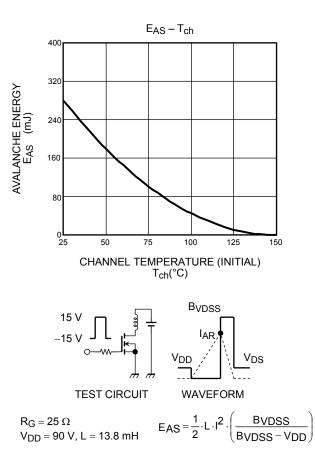
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12

TOTAL GATE CHARGE Qg







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