



20V P-Channel Enhancement Mode MOSFET

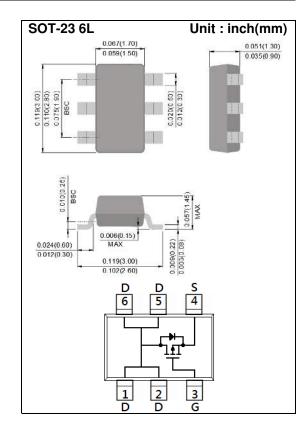
Voltage -20 V Current -5.2A

Features

- RDS(ON), VGS@-4.5V, ID@-5.2A<56mΩ
- RDS(ON) , VGS@-2.5V, ID@-3.6A<70mΩ
- RDS(ON), VGS@-1.8V, ID@-1.9A<94mΩ
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc.
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

- Case: SOT-23 6L Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0005 ounces, 0.014 grams
- Marking: S15



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V _{DS}	-20	V
Gate-Source Voltage		V _{GS}	<u>+</u> 12	V
Continuous Drain Current		I _D	-5.2	Α
Pulsed Drain Current		I _{DM}	-20.8	Α
Power Dissipation	T _a =25°C	P _D	2	W
	Derate above 25°C		16	mW/°C
Operating Junction and Storage Temperature Range		T _J ,T _{STG}	-55~150	°C
Typical Thermal Resistance				
- Junction to Ambient ^(Note 3)		$R_{\theta JA}$	62.5	°C/W





Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =-250uA	-20	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V _{DS} =V _{GS} , I _D =-250uA	-0.4	-0.63	-1.2	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =-4.5V, I _D =-5.2A	-	49	56	mΩ
		V _{GS} =-2.5V, I _D =-3.6A	-	58	70	
		V _{GS} =-1.8V, I _D =-1.9A	-	73	94	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-20V, V _{GS} =0V	-	-0.01	-1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 12V, V _{DS} =0V	-	<u>+</u> 10	<u>+</u> 100	nA
Dynamic						
Total Gate Charge	Q_g		-	18	-	nC
Gate-Source Charge	Q_gs	V _{DS} =-10V, I _D =-5.2A, V _{GS} =-4.5V ^(Note 1,2)	-	2.2	-	
Gate-Drain Charge	Q_gd		-	7.5	-	
Input Capacitance	Ciss	V _{DS} =-10V, V _{GS} =0V, f=1.0MHZ	-	765	-	pF
Output Capacitance	Coss		-	75	-	
Reverse Transfer Capacitance	Crss	I=I.UIVIHZ	-	58	-	
Switching						
Turn-On Delay Time	td _(on)	V 40V I 50A	-	4.8	-	ns
Turn-On Rise Time	tr	$\begin{array}{c} V_{DD}\text{=-}10\text{V},\ I_{D}\text{=-}5.2\text{A},\\ V_{GS}\text{=-}4.5\text{V},\\ R_{G}\text{=-}6\Omega^{(Note\ 1,2)} \end{array}$	-	63	-	
Turn-Off Delay Time	td _(off)		-	72	-	
Turn-Off Fall Time	tf		-	140	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	Is		-	-	-2.0	А
Diode Forward Voltage	V _{SD}	I _S =-1.0A, V _{GS} =0V	-	-0.74	-1.2	V

NOTES:

- 1. Pulse width<a>300us, Duty cycle<a>2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Rejah is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
- 4. The maximum current rating is package limited





TYPICAL CHARACTERISTIC CURVES

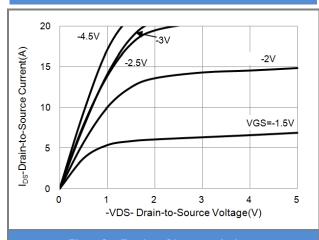


Fig.1 On-Region Characteristics

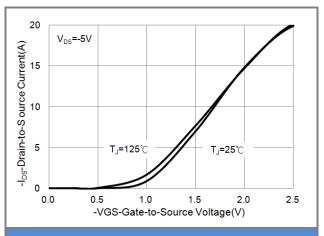


Fig.2 Transfer Characteristics

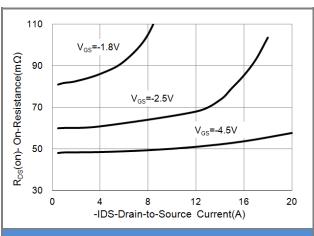


Fig.3 On-Resistance vs. Drain Current

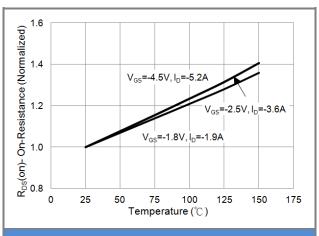
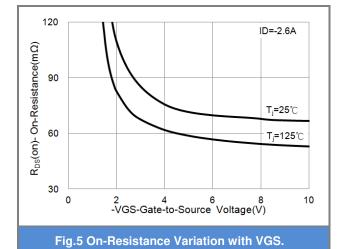
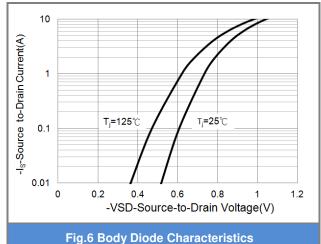


Fig.4 On-Resistance vs. Junction temperature









TYPICAL CHARACTERISTIC CURVES

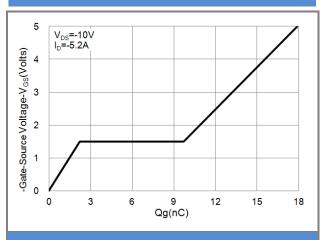


Fig.7 Gate-Charge Characteristics

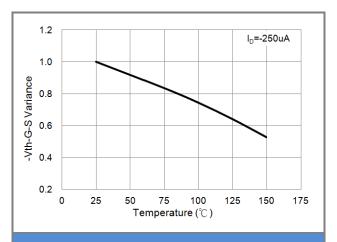


Fig.8 Threshold Voltage Variation with Temperature.

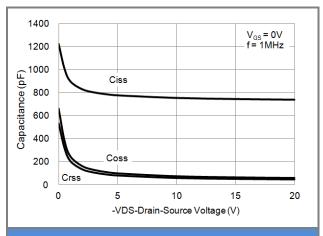


Fig.9 Capacitance vs. Drain-Source Voltage.

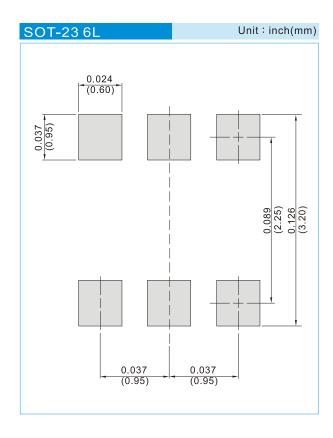




PART NO. PACKING CODE VERSION

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJS6415_S1_00001	SOT-23 6L	3K pcs / 7" reel	S15	Halogen free RoHS compliant
PJS6415_S2_00001	SOT-23 6L	10K pcs / 13" reel	S15	Halogen free RoHS compliant

MOUNTING PAD LAYOUT







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