

COMPLIANT

HALOGEN FREE

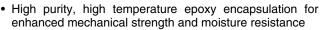
High Performance Schottky Rectifier, 2 x 20 A



PRIMARY CHARACTERISTICS								
I _{F(AV)} 2 x 20 A								
V_{R}	15 V							
V _F at I _F	See Electrical table							
I _{RM} max.	600 mA at 100 °C							
T _J max.	125 °C							
E _{AS}	10 mJ							
Package	TO-220AB 3L							
Circuit configuration	Common cathode							

FEATURES

- 125 °C T_J operation (V_R < 5 V)
- Optimized for OR-ing applications
- Ultra low forward voltage drop
- · High frequency operation
- Guard ring for enhanced ruggedness and long term reliability



- Designed and qualified according to JEDEC®-JESD47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The center tap Schottky rectifier module has been optimized for ultra low forward voltage drop specifically for the OR-ing of parallel power supplies. The proprietary barrier technology allows for reliable operation up to 125 °C junction temperature. Typical applications are in parallel switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MAJOR RATINGS AND CHARACTERISTICS								
SYMBOL CHARACTERISTICS VALUES U								
I _{F(AV)}	Rectangular waveform	40	Α					
V_{RRM}		15	V					
I _{FSM}	t _p = 5 μs sine	700	Α					
V _F	19 A _{pk} , T _J = 125 °C (per leg, typical)	0.25	V					
T _J		-55 to +125	°C					

VOLTAGE RATINGS							
PARAMETER	SYMBOL	VS-STPS40L15CT-M3	UNITS				
Maximum DC reverse voltage	V_R	15	V				
Maximum working peak reverse voltage	V_{RWM}	15	V				

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST COND	VALUES	UNITS				
Maximum average forward per leg			20					
current, see fig. 5 per device	I _{F(AV)}	50 % duty cycle at T _C = 85 °C, rectangular waveform		40				
Maximum peak one cycle non-repetitive	1	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	700	А			
surge current per leg, see fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	330				
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		2				
Non-repetitive avalanche energy per leg	Non-repetitive avalanche energy per leg E_{AS} $T_{J} = 25$ °C, $I_{AS} = 2$ A, L = 6 mH				mJ			

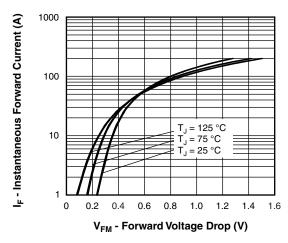


ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	TYP.	MAX.	UNITS			
Forward voltage drop per leg See fig. 1		19 A	T _{.1} = 25 °C	-	0.41	V		
	V _{FM} ⁽¹⁾	40 A	1J=25 C	-	0.52			
	V _{FM} (1)	19 A	T _ 105 °C	0.25	0.33			
		40 A	- T _J = 125 °C	0.37	0.50			
Reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V - Potod V	-	10	mA		
See fig. 2	IRM (")	T _J = 100 °C	V _R = Rated V _R	-	600	IIIA		
Threshold voltage	V _{F(TO)}	T T massimum		0.1	82	V		
Forward slope resistance	r _t	$T_J = T_J$ maximum	7.6		mΩ			
Maximum junction capacitance per leg	C _T	V _R = 5 V _{DC} (test signal rang	-	2000	pF			
Typical series inductance per leg	L _S	Measured lead to lead 5 m	8	-	nH			
Maximum voltage rate of change	dV/dt	Rated V _R	10	000	V/μs			

Note

 $^{(1)}\,$ Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction temperature	e range T _J		-55 to +125	°C			
Maximum storage temperature	range T _{Stg}		-55 to +150				
Maximum thermal resistance, junction to case per leg	R _{thJC}	DC operation See fig. 4	1.5				
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth, and greased (only for TO-220)	0.50	°C/W			
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation (for D ² PAK and TO-262)	40				
Annyayimata wajaht			2	g			
Approximate weight			0.07	OZ.			
	ninimum	Non-lubricated threads	6 (5)	kgf · cm			
Mounting torque m	aximum	1 Non-lubricated tiffeads	12 (10)	(lbf \cdot in)			
Marking device		Case style TO-220AB 3L	STPS40	DL15CT			



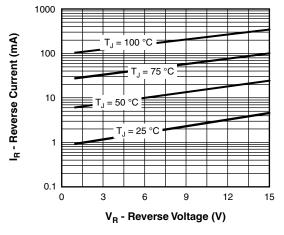


Fig. 1 - Maximum Forward Voltage Drop Characteristics

Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

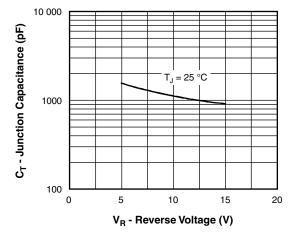


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

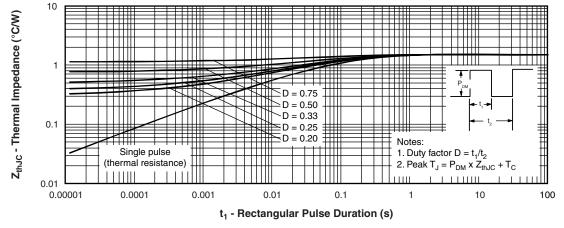


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

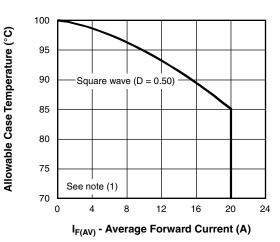


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

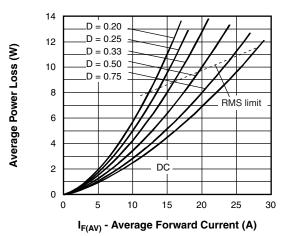


Fig. 6 - Forward Power Loss Characteristics

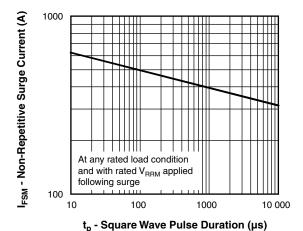


Fig. 7 - Maximum Non-Repetitive Surge Current

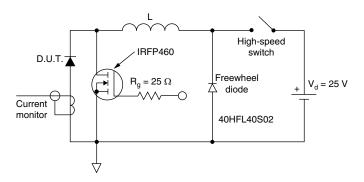


Fig. 8 - Unclamped Inductive Test Circuit

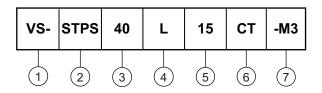
Note

 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{th,JC}; Pd = forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R



ORDERING INFORMATION TABLE

Device code



Vishay Semiconductors product

2 - Schottky STPS series

3 - Current rating (40 = 40 A)

4 - L = Low voltage drop

5 - Voltage rating (15 = 15 V)

6 - CT = essential part number

7 - Environmental digit

-M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

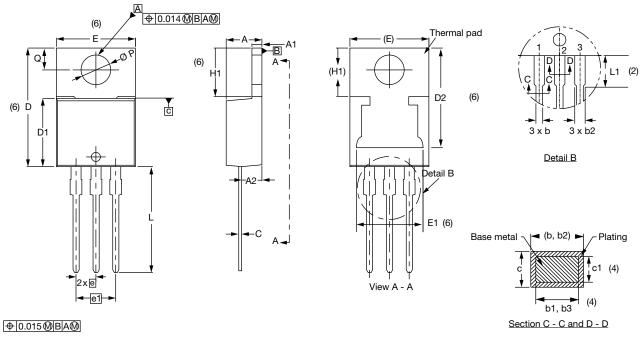
ORDERING INFORMATION (Example)						
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION				
VS-STPS40L15CT-M3	50	Antistatic plastic tubes				

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?96154				
Part marking information	www.vishay.com/doc?95028				



TO-220AB 3L

DIMENSIONS in millimeters and inches



Lead	tip \		

Conforms to JEDEC® outline TO-220AB

SYMBOL	MILLIN	IETERS	INCHES		NOTES		NOTES		SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STWIBUL	MIN.	MAX.	MIN.	MAX.	NOTES	NOTES	STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES		
Α	4.25	4.65	0.167	0.183			D2	11.68	13.30	0.460	0.524	6, 7		
A1	1.14	1.40	0.045	0.055			E	10.11	10.51	0.398	0.414	3, 6		
A2	2.50	2.92	0.098	0.115			E1	6.86	8.89	0.270	0.350	6		
b	0.69	1.01	0.027	0.040			е	2.41	2.67	0.095	0.105			
b1	0.38	0.97	0.015	0.038	4		e1	4.88	5.28	0.192	0.208			
b2	1.20	1.73	0.047	0.068			H1	6.09	6.48	0.240	0.255	6		
b3	1.14	1.73	0.045	0.068	4		L	13.52	14.02	0.532	0.552			
С	0.36	0.61	0.014	0.024			L1	3.32	3.82	0.131	0.150	2		
с1	0.36	0.56	0.014	0.022	4		ØΡ	3.54	3.91	0.139	0.154			
D	14.85	15.35	0.585	0.604	3		Q	2.60	3.00	0.102	0.118			
D1	8.38	9.02	0.330	0.355										

Notes

- $^{(1)}$ Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3, and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2, and E1
- (7) Outline conforms to JEDEC® TO-220, except D2



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Vishay

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