TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSV)

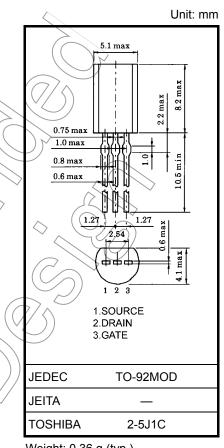
2SK3670

Chopper Regulator and DC-DC Converter Applications

- 2.5V-Gate Drive
- Low drain-source ON-resistance: R_{DS} (ON) = 1.0 Ω (typ.)
- High forward transfer admittance: |Y_{fs}| = 2.1 S (typ.)
- Low leakage current: I_{DSS} = 100 μA (max) (V_{DS} = 150 V)
- Enhancement mode: V_{th} = 0.5 to 1.3 V (V_{DS} = 10 V, I_D =200 μ A)

Absolute Maximum Ratings (Ta = 25°C)

| | | | | | < ' | |
|--|-------------------|------------------|-------------------|------------------------|-------------------|--------|
| Characteristics | | Symbol | Rating | Unit | $\langle \rangle$ | |
| Drain-source voltage | | V _{DSS} | 150 | $\langle \psi \rangle$ | | |
| Drain-gate voltage (R _{GS} = 20 kΩ) | | | V _{DGR} | 150 | × | |
| Gate-source voltage | | | V _{GSS} | ±12 | × | |
| Drain current | DC | (Note 1) | ۱ _D | 0.67 | \sim | |
| | Pulse (t \leq s | ōs) (Note 1) | I _{DP} | | A | |
| | Pulse | (Note 1) | I _{DP} | 3 | | |
| Drain power dissipation | | | Pp | 0.9 | W | |
| Single pulse avalanche energy (Note 2) | | | EAS | 41 | m J | \sim |
| Avalanche current | | | | 0.67 | A | |
| Repetitive avalanche energy (Note 3) | | | EAR | 0.09 | C m | |
| Channel temperature | | | / J _{ch} | 150 | ⊃°c | |
| Storage temperature range | | | T _{stg} | -55 to 150 | °C | |

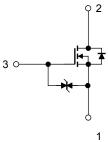


Weight: 0.36 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

| Characteristics | Symbol | Max | Unit |
|--|------------------------|-----|--------|
| Thermal resistance, channel to ambient | R _{th (ch−a)} | 138 | °C / W |



Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DS} = 50V, T_{ch} = 25°C(initial), L = 135mH, I_{AR} = 0.67A, R_G = 25 Ω

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.

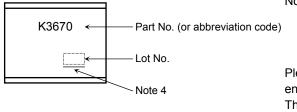
Electrical Characteristics (Ta = 25°C)

| Charac | cteristics | Symbol | Test Condition | Min | Тур. | Max | Unit |
|--------------------------------------|-----------------|----------------------|---|--------------------------|--------|-------|------|
| Gate leakage cu | irrent | I _{GSS} | V _{GS} = ±9.6 V, V _{DS} = 0 V | _ | — | ±10 | μA |
| Drain cut-off cu | rrent | I _{DSS} | V _{DS} = 150 V, V _{GS} = 0 V | | — | 100 | μA |
| Drain-source br | eakdown voltage | V (BR) DSS | I _D = 10 mA, V _{GS} = 0 V | 150 | — | _ | V |
| Gate threshold v | voltage | V _{th} | V _{DS} = 10 V, I _D =200 μ A | 0.5 | - | 1.3 | V |
| Drain-source ON-resistance | | R _{DS (ON)} | V _{GS} = 2.5 V, I _D = 0.5 A | X |)))1.1 | 2 | Ω |
| | | | V _{GS} = 4 V, I _D = 0.5 A | | 1.0 | 1.7 | |
| Forward transfer | r admittance | Y _{fs} | V _{DS} = 10 V, I _D = 0.5 A | 0. | 2.1 | | S |
| Input capacitance | | C _{iss} | | | 230 | _ | |
| Reverse transfer capacitance | | C _{rss} | V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz | | 14 | _ | pF |
| Output capacitance | | C _{oss} | | - | 50 | 1 | |
| Switching time | Rise time | tr | $5 V$ $I_D = 0.5 A$ OUT V_{GS} | - (| 16 | > $ $ | |
| | Turn-on time | t _{on} | | \mathbb{Z} | 40 |) _ | - ns |
| | Fall time | t _f | | $\overline{\mathbb{O}}'$ | 23 | _ | |
| | Turn-off time | t _{off} | Duty $\leq 1\%$, t _W = 10 µs |) – | 95 | _ | |
| Total gate charg plus gate−drain) | | Qg | | _ | 4.6 | _ | |
| Gate-source charge | | Q _{gs} | $V_{DD} \approx 120 \text{ V}, \text{ V}_{GS} = 5 \text{ V}, \text{ I}_{D} = 1 \text{ A}$ | _ | 2.9 | _ | nC |
| Gate-drain ("miller") Charge | | Qgd | | _ | 1.7 | _ | |

Source–Drain Ratings and Characteristics (Ta = 25°C)

| Characteristics | Symbol | Test Condition | Min | Тур. | Max | Unit |
|--|------------------|--|-----|------|------|------|
| Continuous drain reverse current (Note 1) | | | _ | | 0.67 | А |
| Pulse drain reverse current (t=5s) (Note 1) | I _{DRP} | - | | l | 1 | А |
| Pulse drain reverse current (Note 1) | | - | - | | 3 | А |
| Forward voltage (diode) | VDSF | I _{DR} = 0.5 A, V _{GS} = 0 V | | | -1.5 | V |
| Reverse recovery time | trr | 1 _{DR} = 1A, V _{GS} = 0V | | 95 | | ns |
| Reverse recovery charge | | dl _{DR} / dt = 50A / µs | _ | 110 | _ | nC |

Marking



Note 4: A line under a Lot No. identifies the indication of product Labels.

[[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

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