High Performance Schottky Rectifier, 2 x 7.5 A **FEATURES**

- 150 °C T_J operation
- Center tap TO-220 package
- · Low forward voltage drop
- · High frequency operation
- enhanced mechanical strength and moisture resistance
- reliability
- of 245 °C
- please see www.vishay.com/doc?99912

DESCRIPTION

The VS-MBR(B)15... center tap Schottky rectifier has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	15	A		
V _{RRM}		35/45	V		
I _{FSM}	t _p = 5 μs sine	690	A		
V _F	7.5 A _{pk} , T _J = 125 °C	0.57	V		
TJ		-65 to +150	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-MBRB1535CT-M3 VS-MBR1535CT-1-M3	VS-MBRB1545CT-M3 VS-MBR1545CT-1-M3	UNITS
Maximum DC reverse voltage	V _R	35	45	V
Maximum working peak reverse voltage	V _{RWM}		40	v



Anode

FRIMARI ONARAOTERISTIOS				
I _{F(AV)}	2 x 7.5 A			
V _R	35 V, 45 V			
V _F at I _F	0.57 V			
I _{RM} max.	15 mA at 125 °C			
T _J max.	150 °C			
E _{AS}	7 mJ			
Package	D ² PAK (TO-263AB), TO-262AA			
Circuit configuration	Common cathode			

- High purity, high temperature epoxy encapsulation for
- · Guard ring for enhanced ruggedness and long term
- Meets MSL level 1, per J-STD-020, LF maximum peak
- Designed and qualified according to JEDEC[®]-JESD 47
- Material categorization: for definitions of compliance

VS-MBRB15..CT-M3, VS-MBR15..CT-1-M3 **Vishay Semiconductors**

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2

3

TO-262AA

Base

common

cathode

02

2 1 Common 0 3

VS-MBR15..CT-1-M3

cathode Anode

SHAY

D²PAK (TO-263AB)

Base

common

cathode

02

<u>d</u> 2

VS-MBRB15..CT-M3

Common 🖒 3 Anode cathode Anode

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HALOGEN

FREE



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ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	OL TEST CONDITIONS		VALUES	UNITS
Maximum average per leg		T ₂ = 121 °C rote	od V-	7.5	
forward current per device	I _{F(AV)}	$T_{\rm C} = 131 {}^{\circ}{\rm C}$, rated $V_{\rm R}$		15	
Maximum peak one cycle	l	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	690	А
non-repetitive surge	IFSM	Surge applied at single phase, 60	rated load conditions halfwave, Hz	150	
Non-repetitive avalanche energy per leg	E _{AS}	T_J = 25 °C, I_{AS} =	2 A, L = 3.5 mH	7	mJ
Repetitive avalanche current per leg	I _{AR}		g linearly to zero in 1 μs d by T _J maximum V _A = 1.5 x V _R typical	2	А

ELECTRICAL SPECIFICATIO	NS				
PARAMETER	SYMBOL	TEST CO	ONDITIONS	VALUES	UNITS
		15 A	T _J = 25 °C	0.84	
Maximum forward voltage drop	V _{FM} ⁽¹⁾	7.5 A	− T,ı = 125 °C	0.57	V
		15 A	$1_{\rm J} = 125$ C	0.72	
	I (1)	T _J = 25 °C	Dated DC valtage	0.1	m۸
Maximum instantaneous reverse current	I _{RM} ⁽¹⁾	T _J = 125 °C	Rated DC voltage	15	mA
Maximum junction capacitance	CT	V _R = 5 V _{DC} (test signal rar	nge 100 kHz to 1 MHz), 25 °C	400	pF
Typical series inductance	L _S	Measured from top of terr	minal to mounting plane	8.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

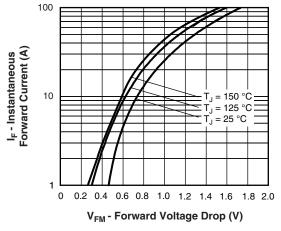
THERMAL - MECHA	NICAL SP	PECIFICA	TIONS		
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction tempera	ture range	TJ		-65 to +150	°C
Maximum storage temperat	ure range	T _{Stg}		-65 to +175	C
Maximum thermal resistanc	æ,	R _{thJC}	DC operation	3.0	
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50	°C/W
Maximum thermal resistanc	Maximum thermal resistance, junction to ambient		DC operation	60	
A				2	g
Approximate weight				0.07	oz.
Mounting torque	minimum			6 (5)	kgf ⋅ cm
Mounting torque	maximum			12 (10)	(lbf ⋅ in)
Marking davias			Case style D ² PAK (TO-263AB) MBRB1545CT		545CT
Marking device			Case style TO-262AA	MBR154	45CT-1

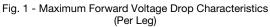
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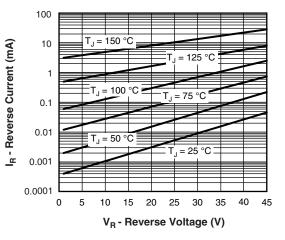


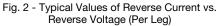
VS-MBRB15..CT-M3, VS-MBR15..CT-1-M3

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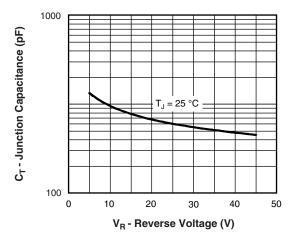
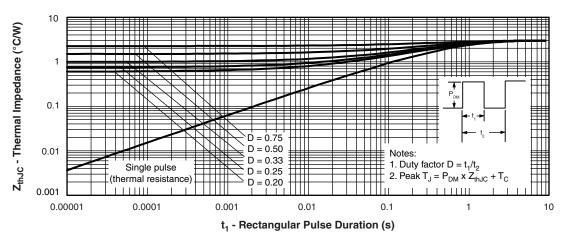


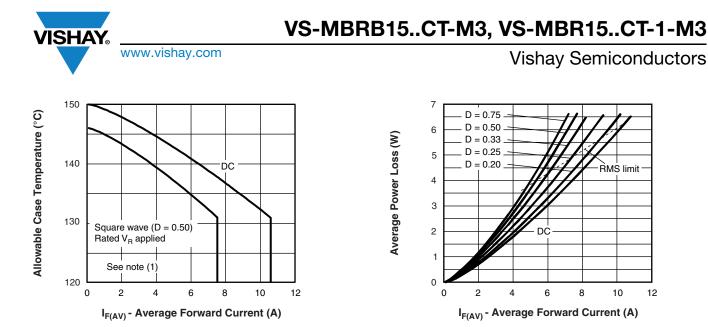
Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

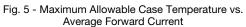




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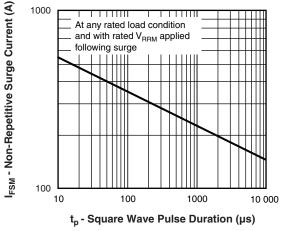


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; $Pd = forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = inverse power loss = V_{R1} \times I_R (1 - D)$; $I_R at V_{R1} = rated V_R$

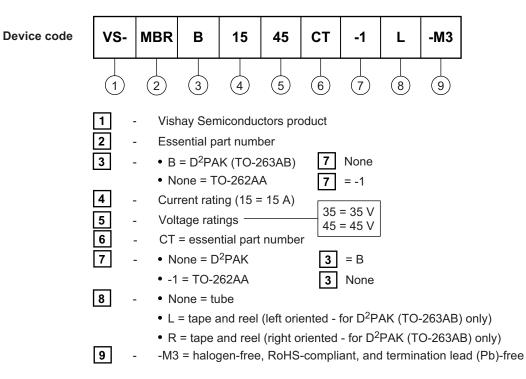


Vishay Semiconductors

ORDERING INFORMATION TABLE

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SHAY



ORDERING INFORMATION					
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION			
VS-MBRB1535CTL-M3	800	13" diameter plastic tape and reel			
VS-MBRB1535CT-M3	50	Antistatic plastic tubes			
VS-MBRB1535CTR-M3	800	13" diameter plastic tape and reel			
VS-MBRB1545CTL-M3	800	13" diameter plastic tape and reel			
VS-MBRB1545CT-M3	50	Antistatic plastic tubes			
VS-MBRB1545CTR-M3	800	13" diameter plastic tape and reel			
VS-MBR1535CT-1-M3	50	Antistatic plastic tubes			
VS-MBR1545CT-1-M3	50	Antistatic plastic tubes			

LINKS TO RELATED DOCUMENTS				
Dimensions	D ² PAK (TO-263AB)	www.vishay.com/doc?96164		
Dimensions	TO-262AA	www.vishay.com/doc?96165		
Part marking information	D ² PAK (TO-263AB)	www.vishay.com/doc?95444		
Part marking information	TO-262AA	www.vishay.com/doc?95443		
Packaging information		www.vishay.com/doc?96424		
SPICE model		www.vishay.com/doc?95294		

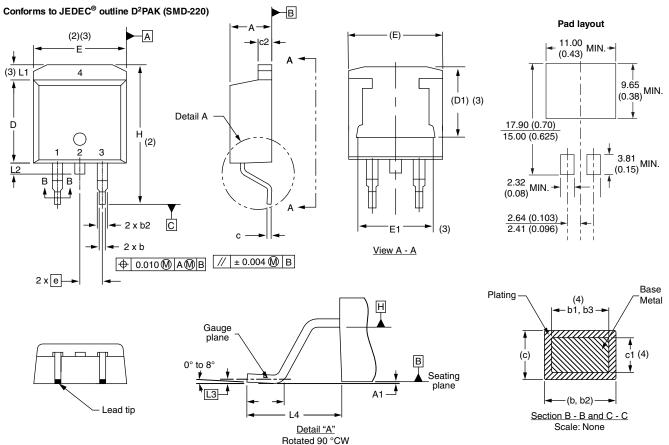
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D²PAK

DIMENSIONS in millimeters and inches

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SYMBOL	MILLIM	IETERS	INCHES		NOTES	
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	
А	4.06	4.83	0.160	0.190		
A1	0.00	0.254	0.000	0.010		
b	0.51	0.99	0.020	0.039		
b1	0.51	0.89	0.020	0.035	4	
b2	1.14	1.78	0.045	0.070		
b3	1.14	1.73	0.045	0.068	4	
С	0.38	0.74	0.015	0.029		
c1	0.38	0.58	0.015	0.023	4	
c2	1.14	1.65	0.045	0.065		
D	8.51	9.65	0.335	0.380	2	

SYMBOL	MILLIMETERS		INCHES		NOTES
STNIDUL	MIN.	MAX.	MIN.	MAX.	
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54	BSC	0.100) BSC	
Н	14.61	15.88	0.575	0.625	
L	1.78	2.79	0.070	0.110	
L1	-	1.65	-	0.066	3
L2	1.27	1.78	0.050	0.070	
L3	0.25	BSC	0.010	BSC	
L4	4.78	5.28	0.188	0.208	

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5 M-1994

(2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

⁽⁵⁾ Datum A and B to be determined at datum plane H

⁽⁶⁾ Controlling dimension: inches

⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-263AB

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Document Number: 96164

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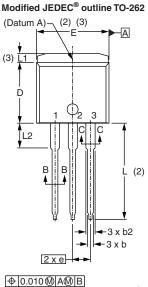
Outline Dimensions

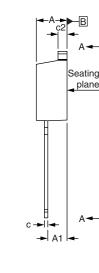


Vishay Semiconductors

TO-262AA

DIMENSIONS in millimeters and inches



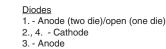


D1 (3) (3) Section A - A Base (4) Plating b1. b3 metal ≰ c1 (4) -(b, b2)-Section B - B and C - C Scale: None

E

010	(M) A	.@/E	3		
_				_	
	math	math.	mark		





Lead assignments

SYMBOL	MILLIN	METERS	INC	INCHES		
SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	
А	4.06	4.83	0.160	0.190		
A1	2.03	3.02	0.080	0.119		
b	0.51	0.99	0.020	0.039		
b1	0.51	0.89	0.020	0.035	4	
b2	1.14	1.78	0.045	0.070		
b3	1.14	1.73	0.045	0.068	4	
С	0.38	0.74	0.015	0.029		
c1	0.38	0.58	0.015	0.023	4	
c2	1.14	1.65	0.045	0.065		
D	8.51	9.65	0.335	0.380	2	
D1	6.86	8.00	0.270	0.315	3	
E	9.65	10.67	0.380	0.420	2, 3	
E1	7.90	8.80	0.311	0.346	3	
е	2.54 BSC		0.100) BSC		
L	13.46	14.10	0.530	0.555		
L1	-	1.65	-	0.065	3	
L2	3.56	3.71	0.140	0.146		

Notes

 ⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994
 ⁽²⁾ Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the second flash include mold flash. the outmost extremes of the plastic body

(3) Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

(5) Controlling dimension: inches

(6) Outline conform to JEDEC® TO-262 except A1 (max.), b (min., max.), b1 (min.), b2 (max.), c (min.), c1(min.), c2 (max.), D (min.), E (max.), L1 (max.), L2 (min., max.)

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