UT6KB5

40V Nch+Nch Power MOSFET

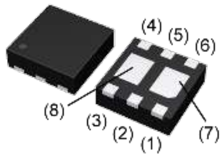
Datasheet

V_{DSS}	40V
$R_{DS(on)}$(Max.)	48mΩ
I_D	±5.0A
P_D	2W

●Outline

DFN2020-8D

HUML2020L8



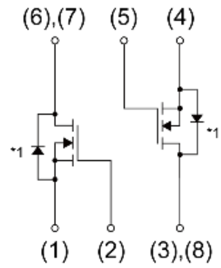
●Features

1) Low on - resistance
2) Small Surface Mount Package
3) Pb-free plating ; RoHS compliant
4) Halogen Free

●Inner circuit

(1) Tr1 Source
(2) Tr1 Gate
(3) Tr2 Drain
(4) Tr2 Source
(5) Tr2 Gate
(6) Tr1 Drain
(7) Tr1 Drain
(8) Tr2 Drain

*1 Body Diode



●Packaging specifications

Type	Packing	Embossed Tape
	Reel size (mm)	180
	Tape width (mm)	8
	Quantity (pcs)	3000
	Taping code	TCR
	Marking	KB5

●Application

Switching

DC/DC Converter

●Absolute maximum ratings (T_a = 25°C ,unless otherwise specified) <Tr1 and Tr2>

Parameter	Symbol	Value	Unit
Drain - Source voltage	V_{DSS}	40	V
Continuous drain current	I_D	±5.0	A
Pulsed drain current	I_{DP} *1	±20	A
Gate - Source voltage	V_{GSS}	±20	V
Avalanche current, single pulse	I_{AS} *2	5.0	A
Avalanche energy, single pulse	E_{AS} *2	2.0	mJ
Power dissipation	P_D *3	2	W
Junction temperature	T_j	150	℃
Operating junction and storage temperature range	T_{stg}	-55 to +150	℃

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●Thermal resistance

Parameter	Symbol	Values			Unit
		Min.	Typ.	Max.	
Thermal resistance, junction - ambient	R_{thJA} *3	-	-	62.5	°C/W

●Electrical characteristics (T_a = 25°C) <Tr1 and Tr2>

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Drain - Source breakdown voltage	$V_{(BR)DSS}$	V_{GS} = 0V, I_D = 1mA	40	-	-	V
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_j}$	I_D = 1mA referenced to 25°C	-	28.9	-	mV/°C
Zero gate voltage drain current	I_{DSS}	V_{DS} = 40V, V_{GS} = 0V	-	-	1	μA
Gate - Source leakage current	I_{GSS}	V_{DS} = 0V, V_{GS} = ±20V	-	-	±100	nA
Gate threshold voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 1mA	1.0	-	2.5	V
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_j}$	I_D = 1mA referenced to 25°C	-	-4.6	-	mV/°C
Static drain - source on - state resistance	$R_{DS(on)}$ *4	V_{GS} = 10V, I_D = 5.0A	-	37	48	mΩ
		V_{GS} = 4.5V, I_D = 5.0A	-	48	80	
Gate resistance	R_G	-	-	2.7	-	Ω
Forward Transfer Admittance	$\lvert Y_{fs} \rvert$ *4	V_{DS} = 5V, I_D = 3.0A	1.3	-	-	S

*1 Pw ≤ 10μs, Duty cycle ≤ 1%

*2 L ≃ 0.1mH, V_{DD} = 20V, R_G = 25Ω, Starting T_j = 25°C Fig.3-1,3-2

*3 Mounted on a Cu board (40×40×0.8mm)

*4 Pulsed

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●Electrical characteristics (T_a = 25°C) <Tr1 and Tr2>

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Input capacitance	C_{iss}	V_{GS} = 0V	-	150	-	pF
Output capacitance	C_{oss}	V_{DS} = 20V	-	85	-	pF
Reverse transfer capacitance	C_{rss}	f = 1MHz	-	10	-	pF
Turn - on delay time	$t_{d(on)}$[*4]	V_{DD} ≃ 20V, V_{GS} = 10V	-	5.0	-	ns
Rise time	t_r[*4]	I_D = 2.5A	-	4.7	-	ns
Turn - off delay time	$t_{d(off)}$[*4]	R_L = 8.0Ω	-	11.0	-	ns
Fall time	t_f[*4]	R_G = 10Ω	-	2.7	-	ns

●Gate charge characteristics (T_a = 25°C) <Tr1 and Tr2>

Parameter	Symbol	Conditions		Values			Unit
				Min.	Typ.	Max.	
Total gate charge	Q_g[*4]	V_{DD} ≃ 20V, I_D = 5.0A	V_{GS} = 10V	-	3.5	-	nC
			V_{GS} = 4.5V	-	1.8	-	nC
Gate - Source charge	Q_{gs}[*4]		V_{GS} = 4.5V	-	1.1	-	nC
Gate - Drain charge	Q_{gd}[*4]		V_{GS} = 4.5V	-	0.3	-	nC

●Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

<Tr1 and Tr2>

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Continuous forward current	I_S	T_a = 25°C	-	-	1.67	A
Pulse forward current	I_{SP}[*1]		-	-	20	A
Forward voltage	V_{SD}[*4]	V_{GS} = 0V, I_S = 1.67A	-	-	1.2	V
Reverse recovery time	t_{rr}[*4]	I_S = 5.0A, V_{GS} = 0V, di/dt = 100A/μs	-	18	-	ns
Reverse recovery charge	Q_{rr}[*4]		-	11	-	nC

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●Electrical characteristic curves

Fig.1 Power Dissipation Derating Curve

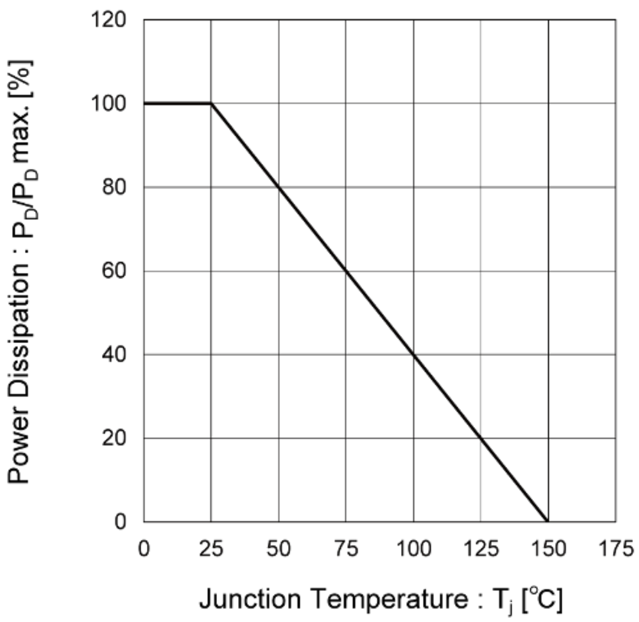


Fig.2 Maximum Safe Operating Area

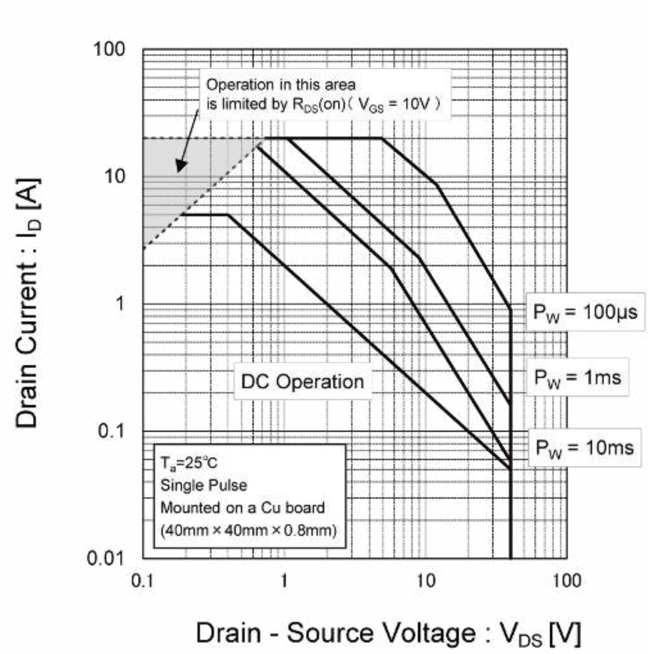


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

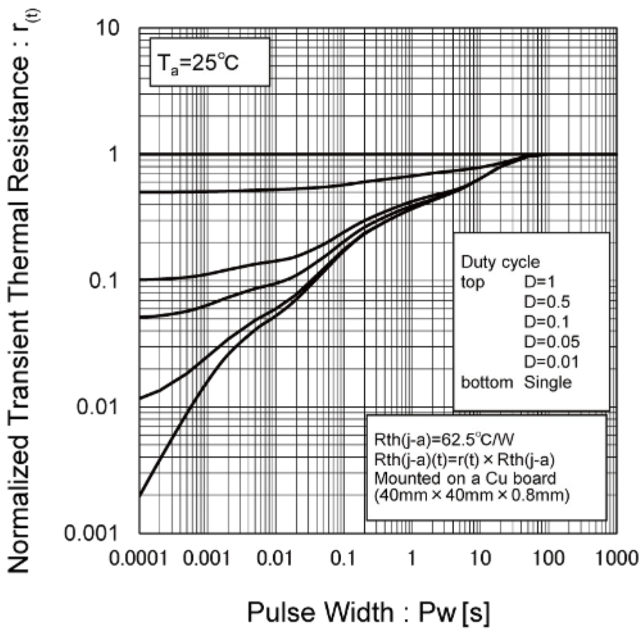
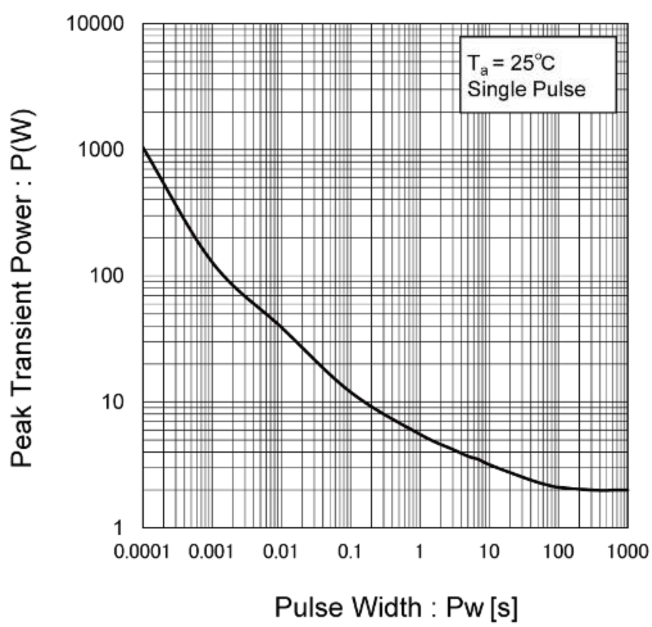


Fig.4 Single Pulse Maximum Power Dissipation



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●Electrical characteristic curves

Fig.5 Typical Output Characteristics(I)

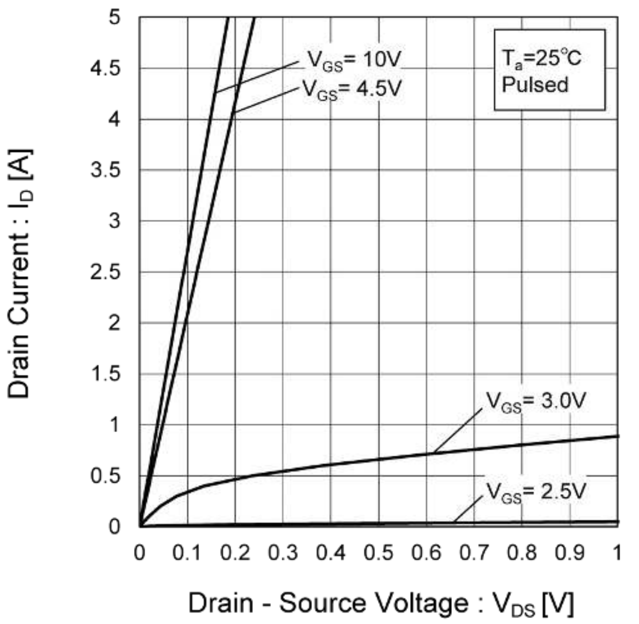


Fig.6 Typical Output Characteristics(II)

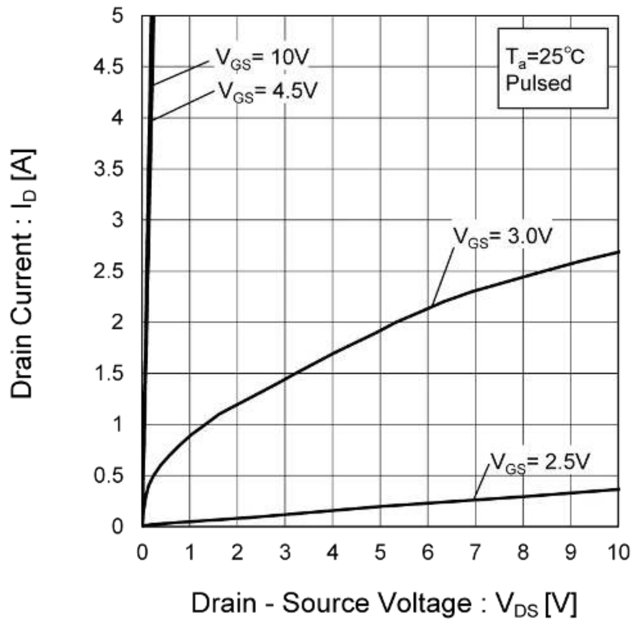
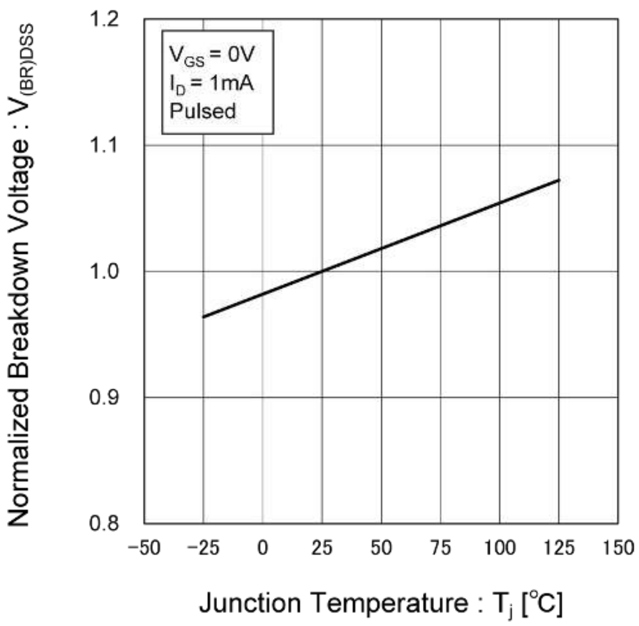


Fig.7 Breakdown Voltage vs. Junction Temperature



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●Electrical characteristic curves

Fig.8 Typical Transfer Characteristics

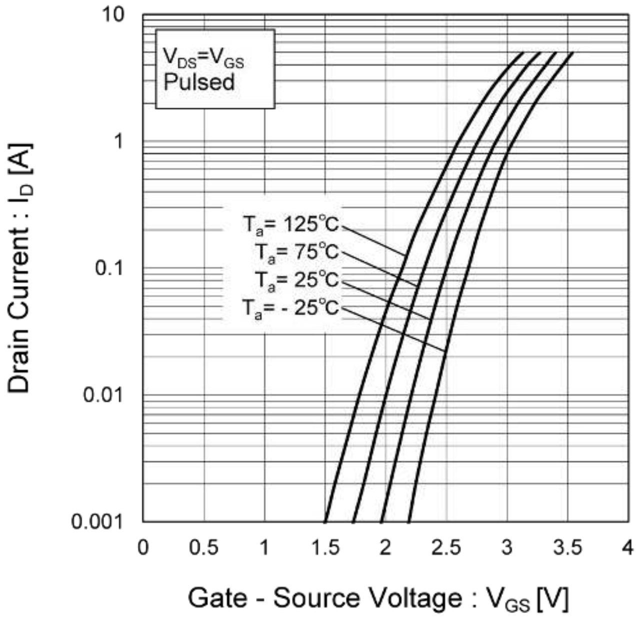


Fig.9 Gate Threshold Voltage vs. Junction Temperature

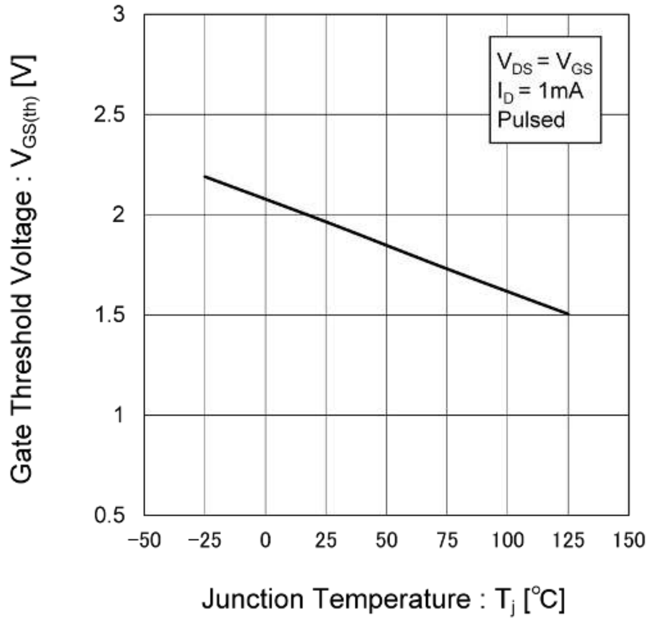
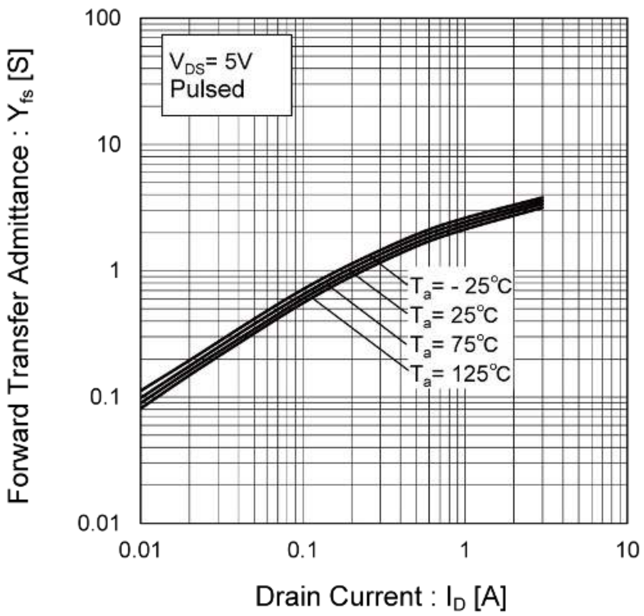


Fig.10 Forward Transfer Admittance vs. Drain Current



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●Electrical characteristic curves

Fig.11 Drain Current Derating Curve

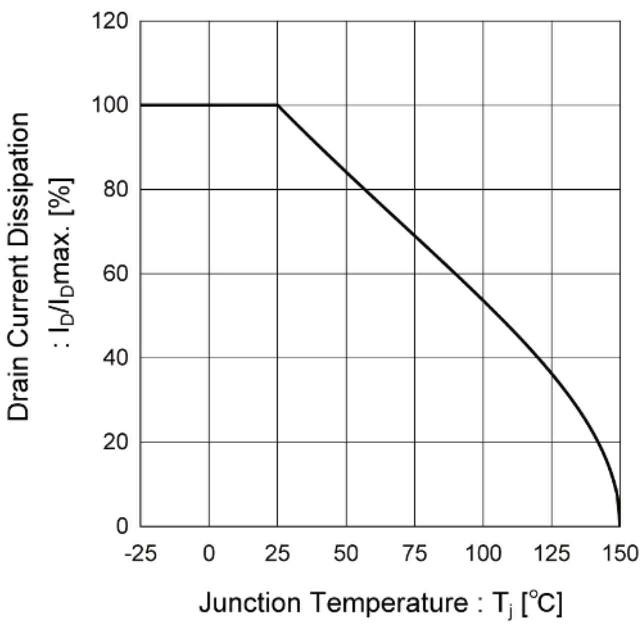


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

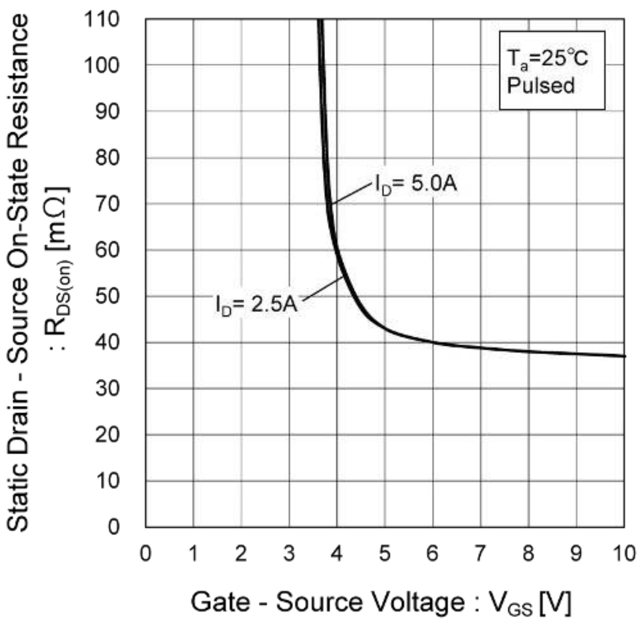
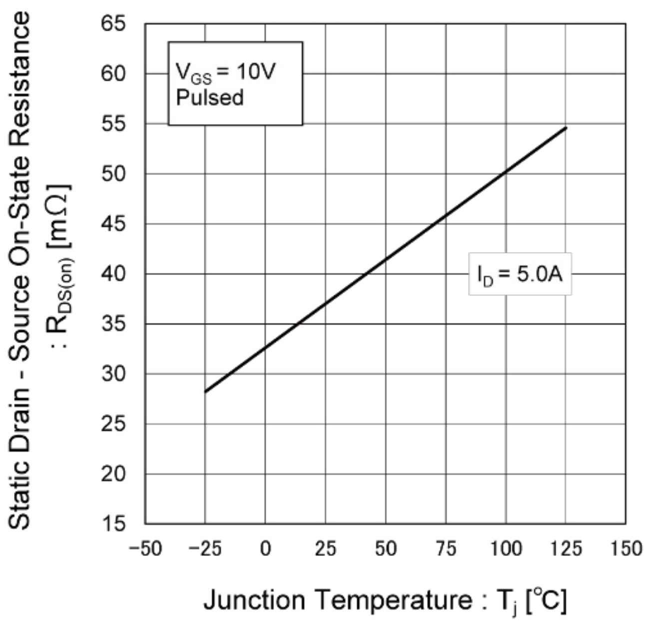


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature



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●Electrical characteristic curves

Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (I)

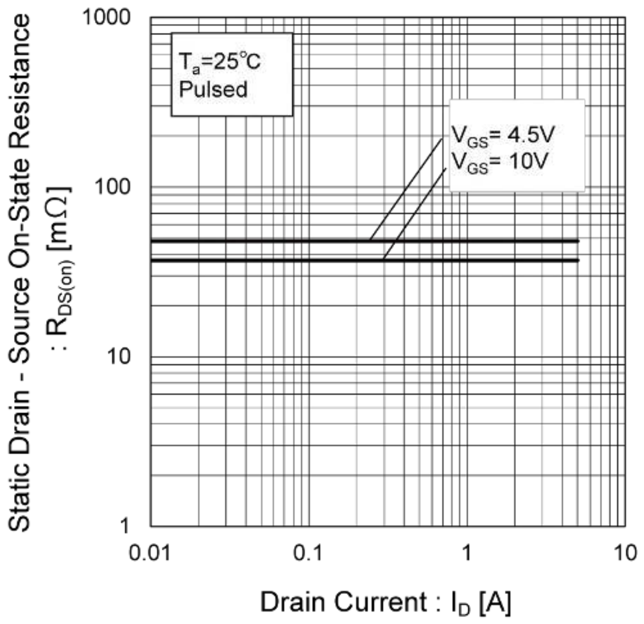


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

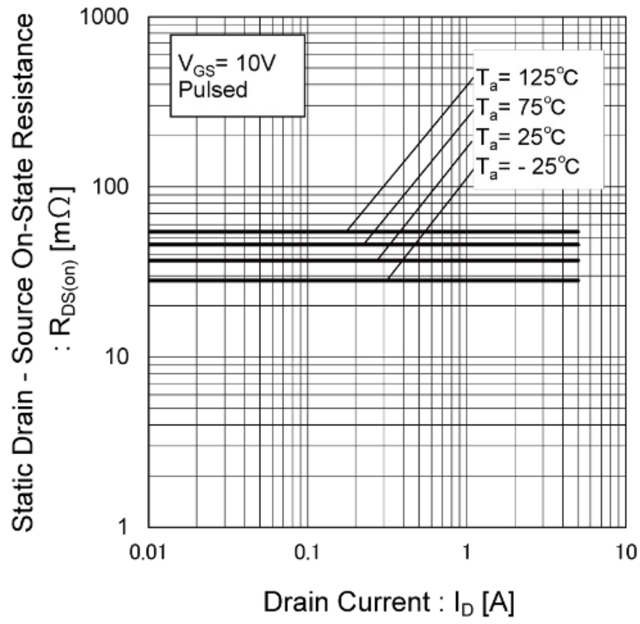
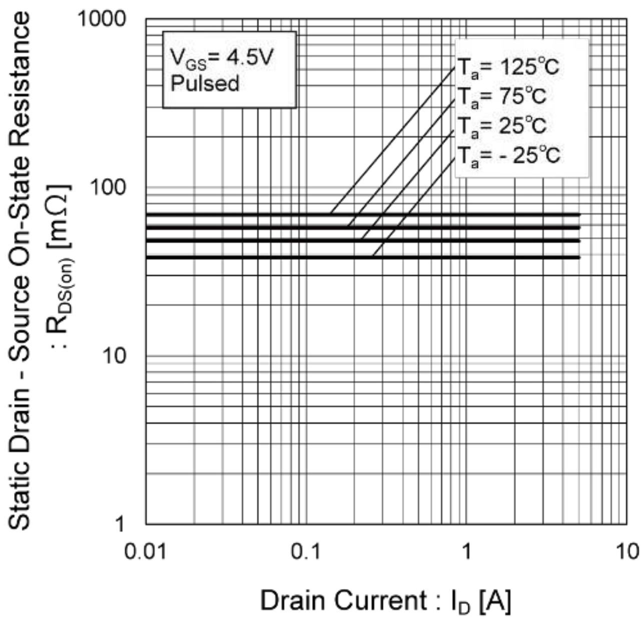


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)



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●Electrical characteristic curves

Fig.17 Typical Capacitances vs. Drain - Source Voltage

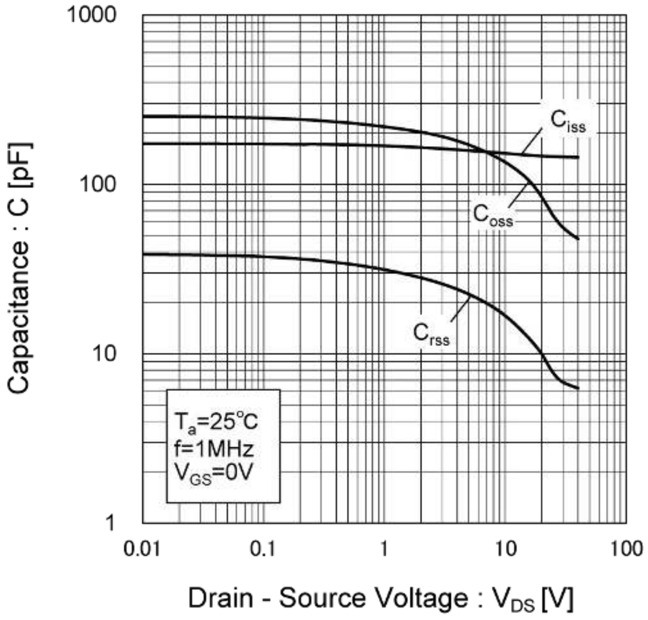


Fig.18 Switching Characteristics

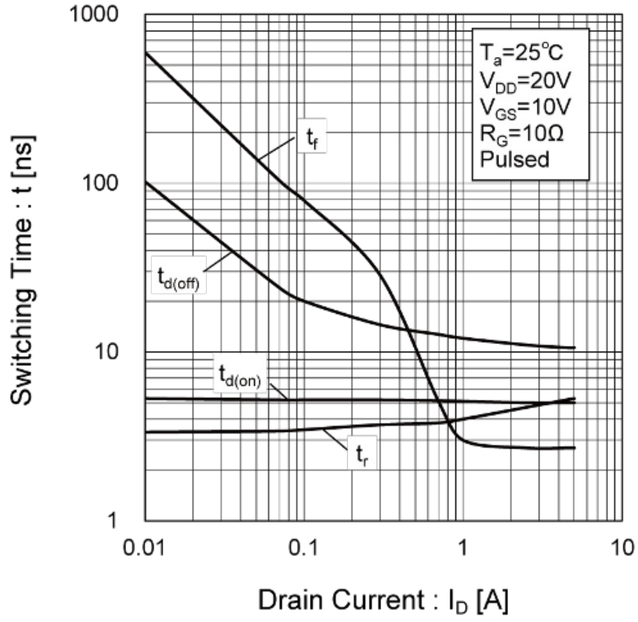


Fig.19 Typical Gate Charge

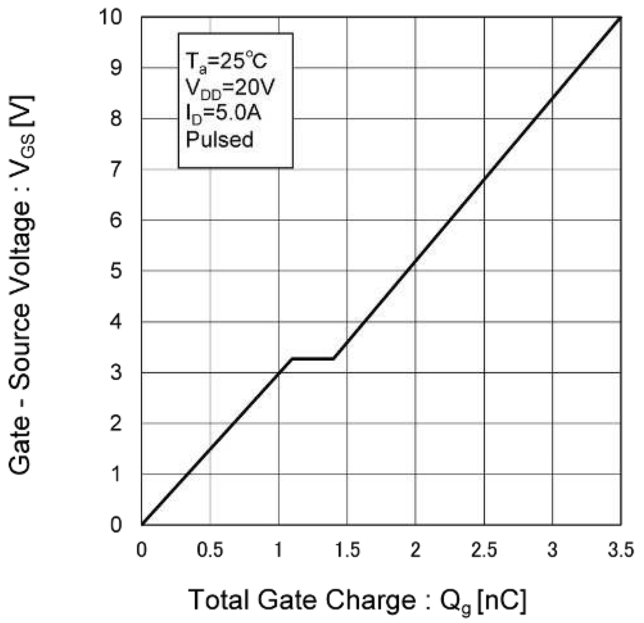
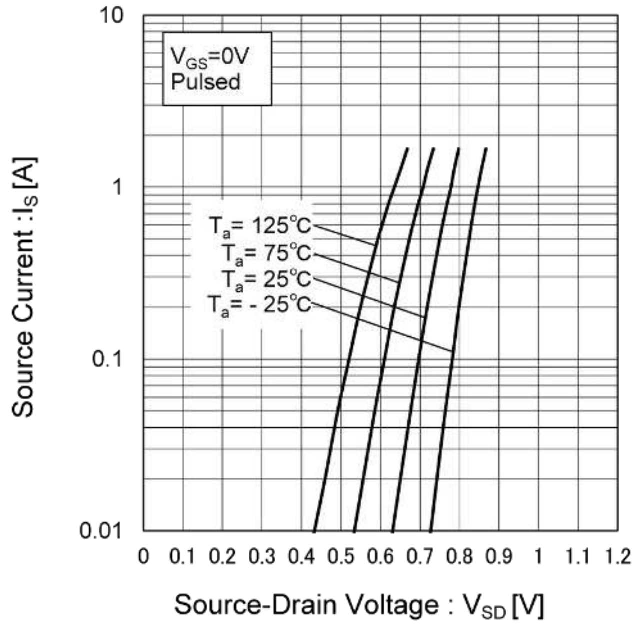


Fig.20 Source Current vs. Source Drain Voltage



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●**Measurement circuits** <It is the same for the Tr1 and Tr2>

Fig.1-1 Switching Time Measurement Circuit

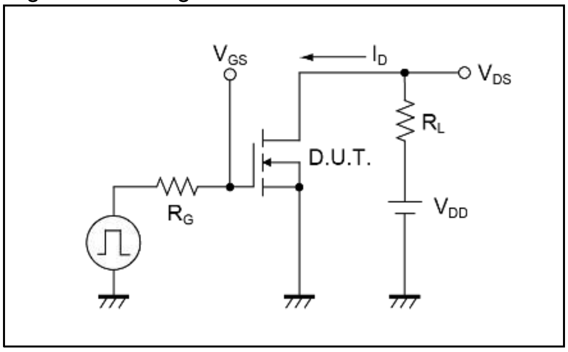


Fig.1-2 Switching Waveforms

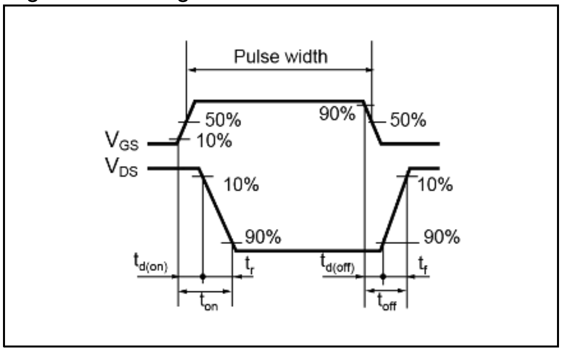


Fig.2-1 Gate Charge Measurement Circuit

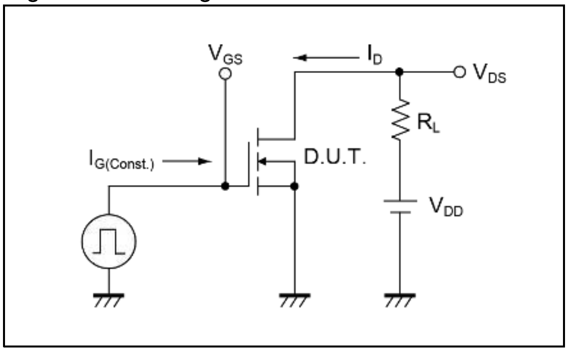


Fig.2-2 Gate Charge Waveform

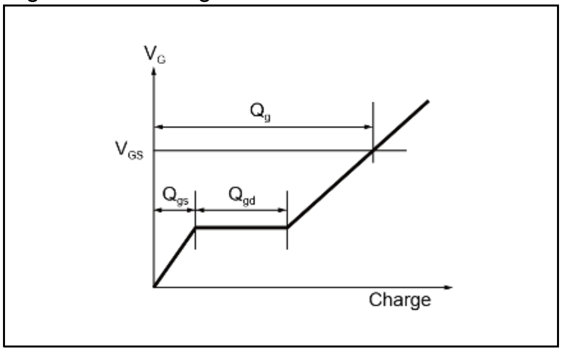


Fig.3-1 Avalanche Measurement Circuit

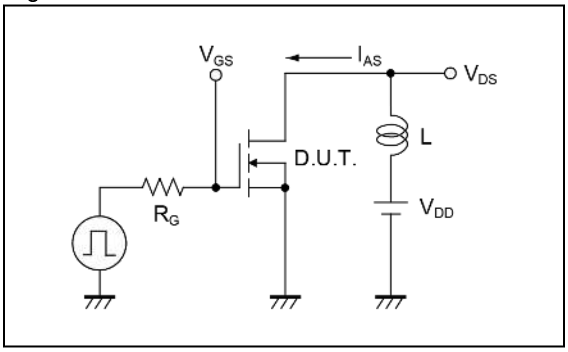
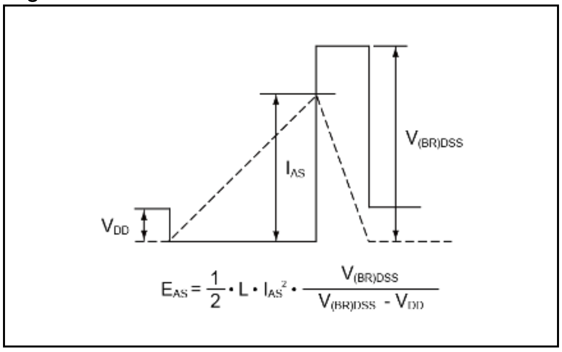


Fig.3-2 Avalanche Waveform



●**Notice**

This product might cause chip aging and breakdown under the large electrified environment.
Please consider to design ESD protection circuit.

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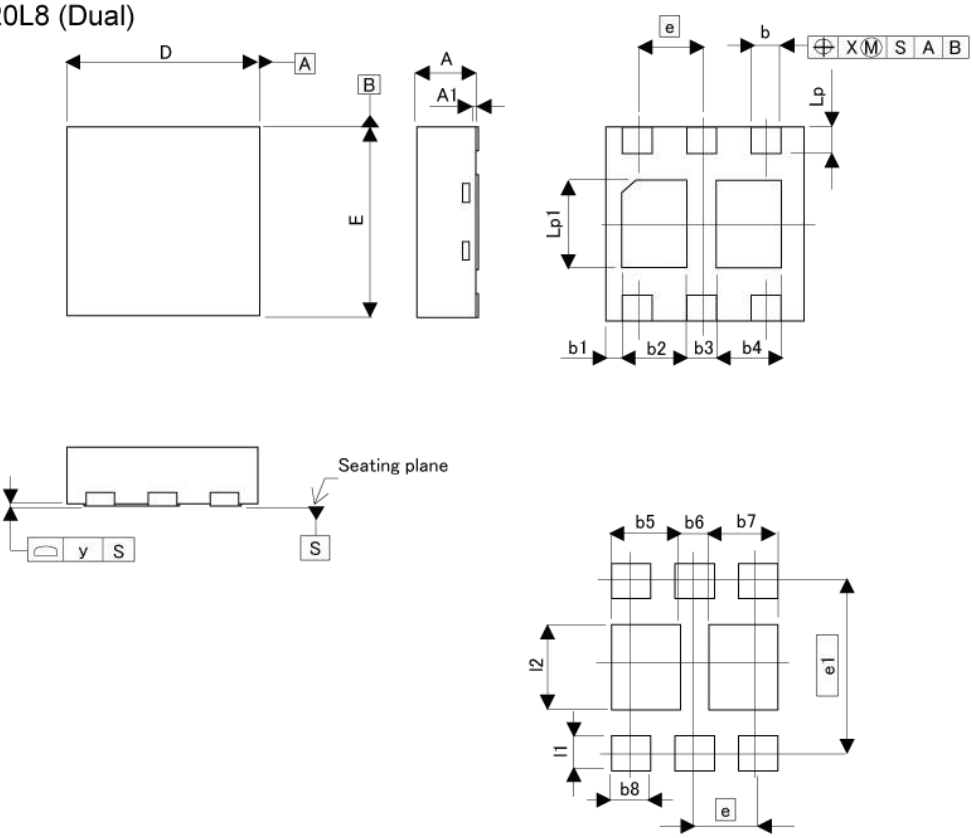
●Dimensions

DFN2020-8D

HUML2020L8 (Dual)



Pattern of terminal position areas
[Not a pattern of soldering pads]

DIM	MILIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	0.55	0.65	0.022	0.026
A1	0.00	0.05	0.000	0.002
b	0.25	0.35	0.010	0.014
b1	0.25		0.010	
b2	0.60	0.70	0.024	0.028
b3	0.3		0.012	
b4	0.60	0.70	0.024	0.028
D	1.90	2.10	0.075	0.083
E	1.90	2.10	0.075	0.083
e	0.65		0.026	
Lp	0.225	0.325	0.009	0.013
Lp1	0.80	1.00	0.031	0.039
x	-	0.10	-	0.004
y	-	0.10	-	0.004

DIM	MILIMETERS		INCHES	
	MIN	MAX	MIN	MAX
b5	-	0.70	-	0.028
b6	0.20	0.30	0.008	0.012
b7	-	0.70	-	0.028
b8	-	0.45	-	0.018
e1	1.725		0.068	
l1	-	0.425	-	0.017
l2	-	1.00	-	0.039

Dimension in mm/inches

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(Note1) Medical Equipment Classification of the Specific Applications

JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSⅢ	CLASSⅡb	CLASSⅢ
CLASSⅣ		CLASSⅢ	

2. ROHM designs and manufactures its Products subject to strict quality control system. However, semiconductor products can fail or malfunction at a certain rate. Please be sure to implement, at your own responsibilities, adequate safety measures including but not limited to fail-safe design against the physical injury, damage to any property, which a failure or malfunction of our Products may cause. The following are examples of safety measures:
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   [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
   [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl_2, H_2S, NH_3, SO_2, and NO_2
   [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
   [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
   [f] Sealing or coating our Products with resin or other coating materials
   [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.) ; or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
   [h] Use of the Products in places subject to dew condensation

4. The Products are not subject to radiation-proof design.

5. Please verify and confirm characteristics of the final or mounted products in using the Products.

6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.

7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.

8. Confirm that operation temperature is within the specified range described in the product specification.

9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.

2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.

2. You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of Ionizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
   [a] the Products are exposed to sea winds or corrosive gases, including Cl_2, H_2S, NH_3, SO_2, and NO_2
   [b] the temperature or humidity exceeds those recommended by ROHM
   [c] the Products are exposed to direct sunshine or condensation
   [d] the Products are exposed to high Electrostatic

2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.

3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.

4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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A two-dimensional barcode printed on ROHM Products label is for ROHM's internal use only.

Precaution for Disposition

When disposing Products please dispose them properly using an authorized industry waste company.

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