

N-Channel RF Amplifier

This device is designed for HF/VHF mixer/amplifier and applications where Process 50 is not adequate. Sufficient gain and low noise for sensitive receivers. Sourced from Process 90.

Absolute Maximum Ratings* TA = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
V_{DG}	Drain-Gate Voltage	25	V
V_{GS}	Gate-Source Voltage	- 25	V
I _{GF}	Forward Gate Current	10	mA
T _J ,T _{stg}	Operating and Storage Junction Temperature Range	-55 to +150	°C

*These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

NOTES:

1) These ratings are based on a maximum junction temperature of 150 degrees C.
2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

Thermal Characteristics

Thermal Characteristics TA = 25°C unless otherwise noted				
Symbol	Characteristic		Мах	Units
		J210-212	*MMBFJ210-212	
PD	Total Device Dissipation	350	225	mW
	Derate above 25°C	2.8	1.8	mW/°C
$R_{\theta JC}$	Thermal Resistance, Junction to Case	125		°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	357	556	°C/W

*Device mounted on FR-4 PCB 1.6" X 1.6" X 0.06."

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J210/J211/J212/MMBFJ210/J211/J212, Rev A

N-Channel RF Amplifier

-2.5V

2.0

3.0

4.0

5.0

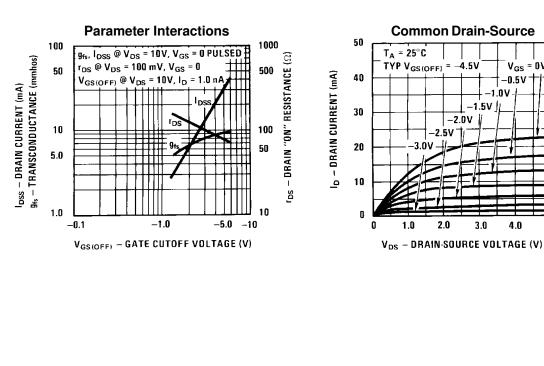
3.0V

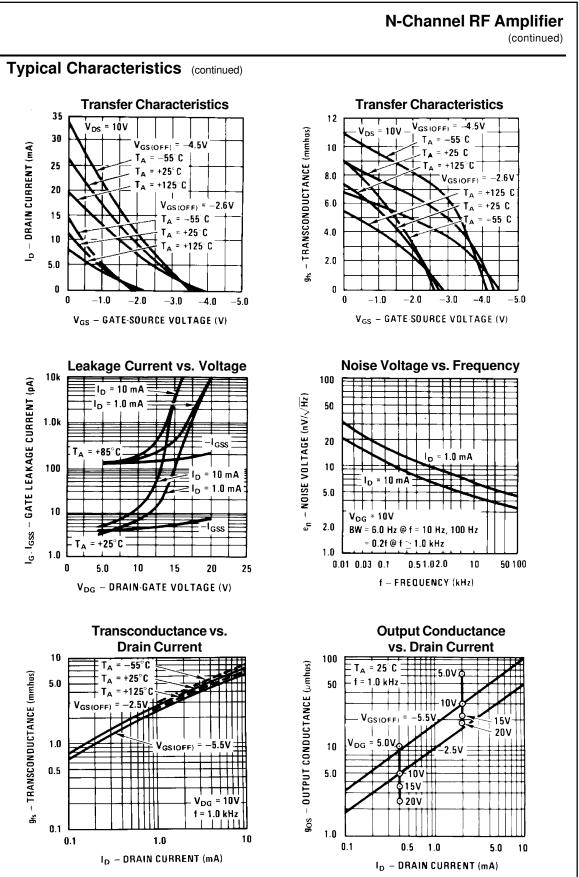
1.0

(continued)

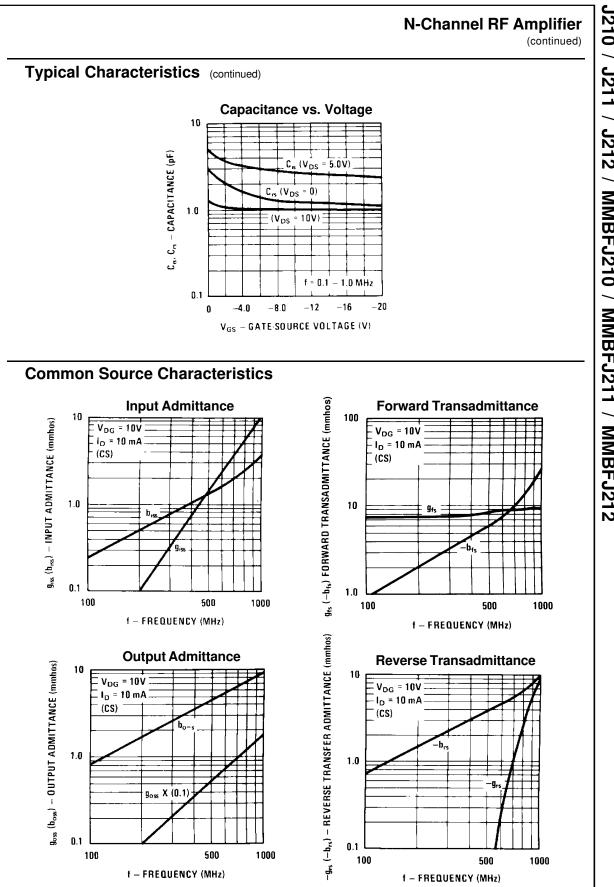
Symbol	Parameter	Test Conditions		Min	Max	Units
	RACTERISTICS				-	
V _{(BR)GSS}	Gate-Source Breakdown Voltage	$I_{G} = 1.0 \ \mu A, \ V_{DS} = 0$		- 25		V
l _{GSS}	Gate Reverse Current	$V_{GS} = 15 V, V_{DS} = 0$			- 100	pA
$V_{GS(off)}$	Gate-Source Cutoff Voltage		210 211	-1.0 - 2.5	-3.0 - 4.5	V V
			212	- 4.0	- 6.0	v
ON CHAR	ACTERISTICS					
IDSS	Zero-Gate Voltage Drain Current*	$V_{DS} = 15 V, V_{GS} = 0$	210	2.0	15	m A
			211 212	7.0 15	20 40	m A m A
goss	Transconductance Common Source Output Conductance		210 211 212 Hz	4000 6000 7000	12,000 12,000 12,000 200	µmhos µmhos µmhos µmhos
Puise rest. Puis	se Width ≤300 μS					
Туріса	al Characteristics					
	Parameter Interactions	1000 50 C c	ommo	n Drai	n-Sourc	:e

J210 / J211 / J212 / MMBFJ210 / MMBFJ211 / MMBFJ212

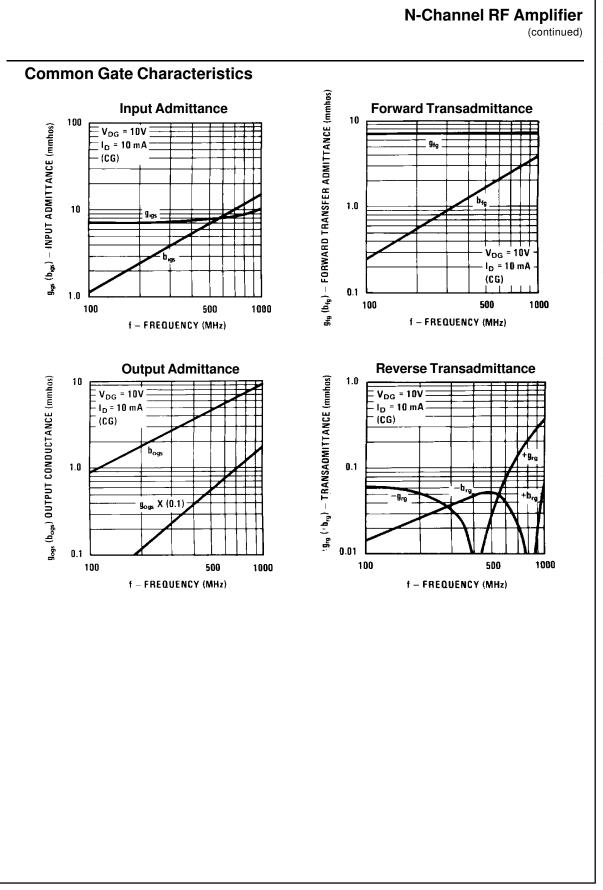












J210 / J211 / J212 / MMBFJ210 / MMBFJ211 / MMBFJ212

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