# PQ15RF15/PQ15RF16

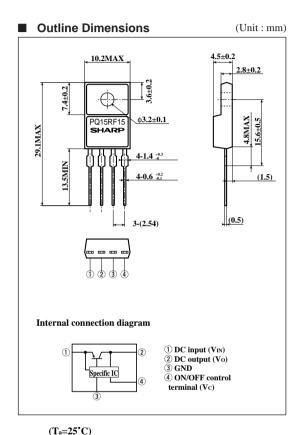
1A Output, Low Power-Loss Voltage Regulators Considering Power Line Voltage Drop

### Features

- Low power-loss (Dropout voltage : MAX. 0.5V)
- Compact resin full-mold package
- Conforming to the unified standard for BS converter
- Output voltage value (15.7V) with an allowance for voltage loss caused by reverse flow preventing diode
- Built-in ON/OFF control terminal corresponding to BS antenna power supply selecting switch
- High-precision output type **(PQ15RF16**) (Output voltage precision : ±2.5%)



- TVs and VCRs with built-in BS tuners
- BS tuners



# Absolute Maximum Ratings

Parameter	Symbol	Rating	Unit	
*1 Input voltage	VIN	35	V	
*1 ON/OFF control terminal voltage	Vc	35	V	
Output current	Io	1	Α	
Power dissipation (No heat sink)	PD1	1.5	w	
Power dissipation (With infinite heat sink)	PD2	15	vv	
*2 Junction temperature	Tj	150	•С	
Operating temperature	Topr	-20 to +80	•C	
Storage temperature	Tstg	-40 to +150	•C	
Soldering temperature	Tsol	260 (For 10s)	•C	

\*1 All are open except GND and applicable terminals.

\*2 Overheat protection may opetate at 125<=Tj<=150°C



# Electrical Characteristics

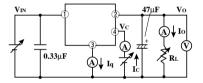
(Unless otherwise specified, condition shall be VIN=18V, I0=0.5A, Ta=25°C)

Parameter		Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Output voltage	PQ15RF15	- Vo		14.92	15.7	16.48	v
	PQ15RF16			15.31	15.7	16.09	v
Load regulation		RegL	Io=5mA to 1.0A	-	0.2	2.0	%
Line regulation		RegI	V <sub>N</sub> =17 to 27V	-	0.2	2.5	%
Temperature coefficient of output voltage		TcVo	T <sub>j</sub> =0 to 125°C	-	±0.01	-	%/ <b>°</b> C
Ripple rejection		RR	Refer to Fig. 2	45	65	-	dB
Dropout voltage		Vi-0	*3 Io=0.5A	-	0.2	0.5	V
ON-state voltage for control		Vc (on)	*4	2.0	-	-	V
ON-state current for control		Ic (on)	Vc=2.7V	-	-	20	μA
OFF-state voltage for control		Vc (off)		-	-	0.8	V
OFF-state current for control		IC (OFF)	Vc=0.4V	-	-	-0.4	mA
Output OFF-state consumption current		Iqs	Io=0A	-	6	10	mA

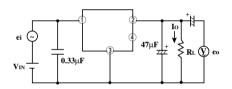
\*3 Input voltage shall be the value when output voltage is 95% in comparison with the initial value.

\*4 In case of opening control terminal (4), output voltage turns on.

#### Fig.1 Test Circuit



#### Fig.2 Test Circuit of Ripple Rejection



f=120Hz (sine wave) ei=0.5Vrms RR=20 log (ei/eo)

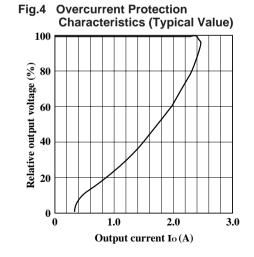
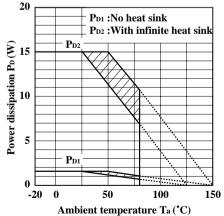
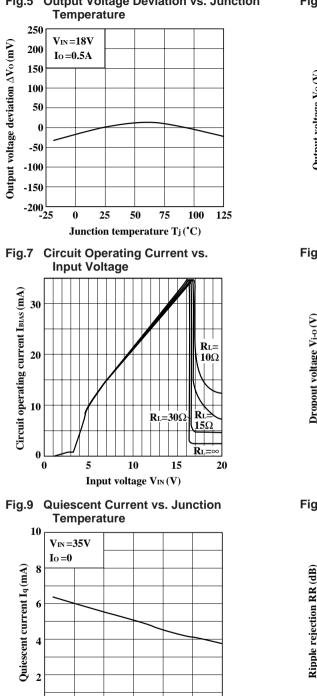


Fig.3 Power Dissipation vs. Ambient Temperature







0

-25

0

25

50

Junction temperature T<sub>j</sub> (C)

75

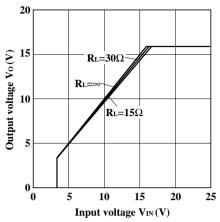
100

125

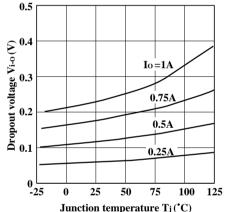
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Fig.5 Output Voltage Deviation vs. Junction

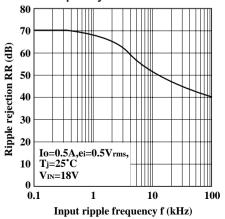
Fig.6 Output Voltage vs. Input Voltage



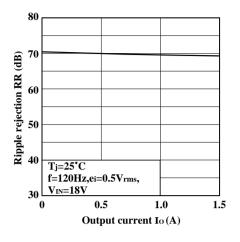
**Dropout Voltage vs. Junction** Fig.8 Temperature

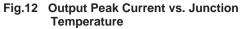


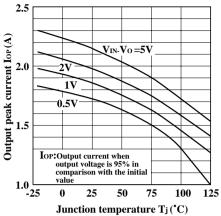
**Ripple Rejection vs. Input Ripple** Fig.10 Frequency



### Fig.11 Ripple Rejection vs. Output Current



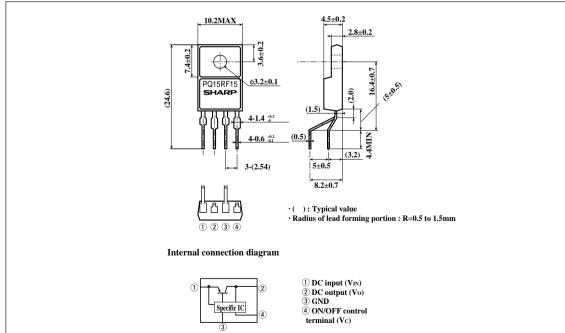




#### Model Line-ups for Lead Forming Type

Output voltage	15.7V output		
Output voltage precision:±5%	PQ15RF1F		
Output voltage precision:±2.5%	PQ15RF1G		

# Outline Dimensions (PQ15RF1F/PQ15RF1G)



Note) The value of absolute maximum ratings and electrical characteristics is same as ones of PQ15RF15/16 series.

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(Unit:mm)

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