



N-CHANNEL ENHANCEMENT MODE MOSFET POWERDI

Small Form Factor Thermally Efficient Package Enables Higher

100% Unclamped Inductive Switching - Ensures More Reliability

please contact us or your local Diodes representative.

Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
Halogen and Antimony Free. "Green" Device (Note 3)
For automotive applications requiring specific change

Product Summary

BVDSS	Rds(on) Max	I _D Max T _C = +25°C
	1.35mΩ @V _{GS} = 10V	150A
30V	2.4mΩ @V _{GS} = 4.5V	100A

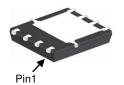
Description and Applications

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Backlighting
- Power management functions
- DC-DC converters

PowerDI5060-8 (Type K)





Bottom View

Top View

control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities),

Features and Benefits

Density End Products

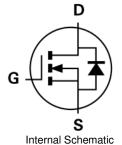
Low R_{DS(ON)} – Minimizes On-State Losses Excellent Q_{gd} x R_{DS(ON)} Product (FOM) Advanced Technology for DC-DC Converters

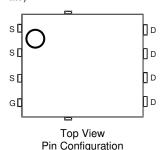
- Mechanical Data

 Package: PowerDI®5060-8 (Type K)
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0

https://www.diodes.com/quality/product-definitions/

- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208
- Weight: 0.097 grams (Approximate)





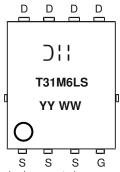
Ordering Information (Note 4)

Part Number	Pankaga	Packing		
Fait Number	Package	Qty.	Carrier	
DMT31M6LPS-13	PowerDI5060-8 (Type K)	2,500	Tape & Reel	

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



⊃¦¦ = Manufacturer's Marking T31M6LS = Product Type Marking Code YYWW = Date Code Marking YY = Year (ex: 22 = 2022) WW = Week (01 to 53)

PowerDI is a registered trademark of Diodes Incorporated.



Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V_{DSS}	30	V	
Gate-Source Voltage	V _{GSS}	±20	V	
Continuous Drain Current, $V_{GS} = 10V$ (Note 6) $T_{A} = +25^{\circ}C$ $T_{A} = +70^{\circ}C$		I _D	35.8 28.6	Α
Continuous Drain Current, V _{GS} = 10V (Note 7) $ T_C = +25^{\circ}C $ $ T_C = +70^{\circ}C $		lo	150 128	Α
Maximum Continuous Body Diode Forward Current (Note 7	Is	83	Α	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	IDM	160	Α	
Avalanche Current, L = 1mH	las	25.5	Α	
Avalanche Energy, L = 1mH	Eas	325	mJ	

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T _A = +25°C	PD	1.3	W
Thermal Resistance, Junction to Ambient (Note 5)		Reja	99	°C/W
Total Power Dissipation (Note 6)	T _A = +25°C	PD	2.5	W
Thermal Resistance, Junction to Ambient (Note 6)		Reja	50	°C/W
Total Power Dissipation (Note 7)	Tc = +25°C	PD	100	W
Thermal Resistance, Junction to Case (Note 7)		Rejc	1.2	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

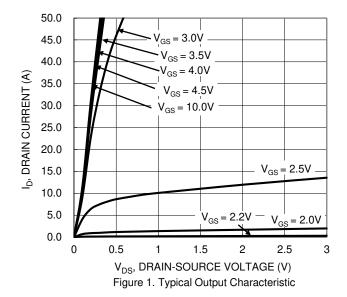
Electrical Characteristics (TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV _{DSS}	30	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	IDSS		-	1	μA	V _{DS} = 24V, V _{GS} = 0V	
Gate-Source Leakage	Igss	_	_	±100	nA	V _{GS} = 20V, V _{DS} = 0V V _{GS} = -16V, V _{DS} = 0V	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(TH)}	1	-	3	٧	$V_{DS}=V_{GS},\ I_D=250\mu A$	
Static Drain-Source On-Resistance			1.1	1.35	mΩ	V _{GS} = 10V, I _D = 20A	
Static Drain-Source On-Nesistance	RDS(ON)	_	1.6	2.4		$V_{GS} = 4.5V, I_D = 20A$	
Diode Forward Voltage	V_{SD}	_	_	1.2	V	$V_{GS} = 0V$, $I_S = 2A$	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	Ciss	_	7019	_		V _{DS} = 15V, V _{GS} = 0V, f = 1.0MHz	
Output Capacitance	Coss	_	3372	_	pF		
Reverse Transfer Capacitance	Crss	_	554	_			
Gate Resistance	Rg	_	0.94	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1.0MHz$	
Total Gate Charge (VGS = 10V)	Qg	_	123	_			
Total Gate Charge (Vgs = 4.5V)	Qg	_	59.1	_		V _{DD} = 15V, I _D = 20A	
Gate-Source Charge	Qgs	_	14.9	_	nC		
Gate-Drain Charge	Q _{gd}	_	24.5	_			
Turn-On Delay Time	tD(ON)	_	8.6	_		$V_{DD} = 15V$, $V_{GS} = 10V$, $R_g = 3\Omega$, $I_D = 20A$	
Turn-On Rise Time	tr	_	20.2	_			
Turn-Off Delay Time	t _{D(OFF)}		71.5	_	ns		
Turn-Off Fall Time	tF		42.3	_			
Body Diode Reverse Recovery Time	trr		37.2	_	ns		
Body Diode Reverse Recovery Charge	Qrr		73.6	_	$\frac{1}{1}$ IF = 20A, dI/dt = 500A/µs		

Notes:

Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
 Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
 Thermal resistance from junction to soldering point (on the exposed drain pad).
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.





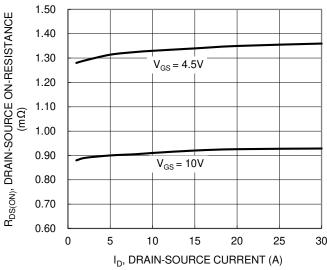


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

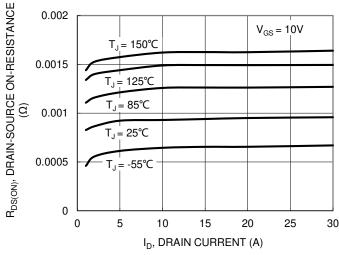


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

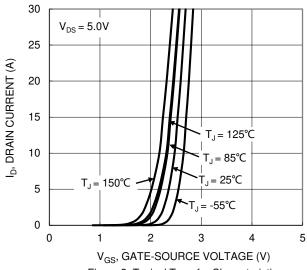


Figure 2. Typical Transfer Characteristic

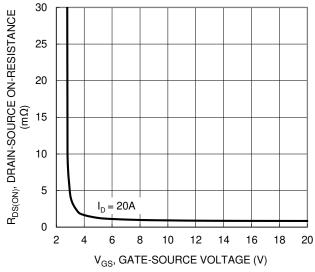


Figure 4. Typical Transfer Characteristic

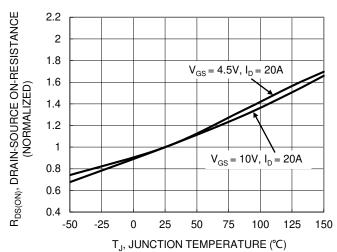


Figure 6. On-Resistance Variation with Temperature



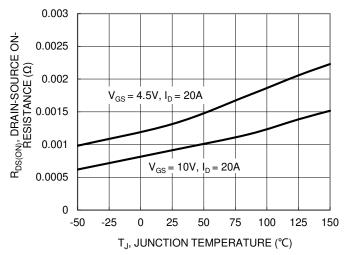


Figure 7. On-Resistance Variation with Temperature

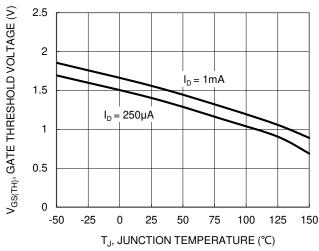


Figure 8. Gate Threshold Variation vs. Junction Temperature

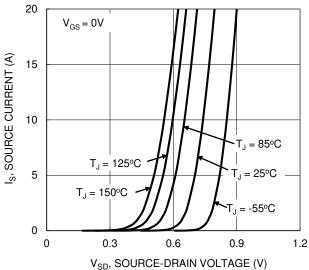
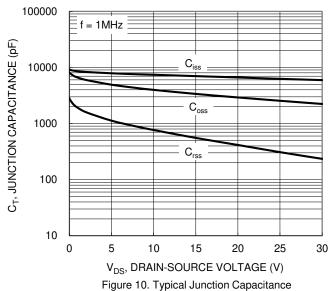


Figure 9. Diode Forward Voltage vs. Current



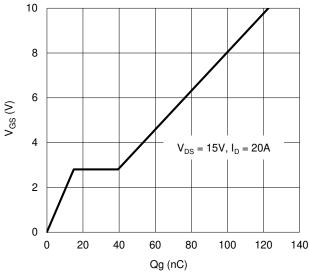


Figure 11. Gate Charge

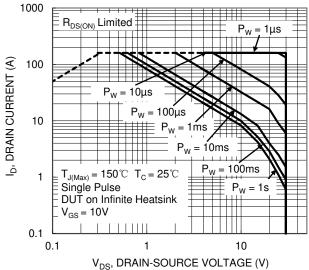


Figure 12. SOA, Safe Operation Area



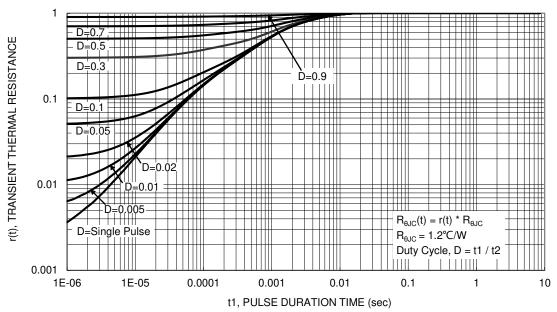


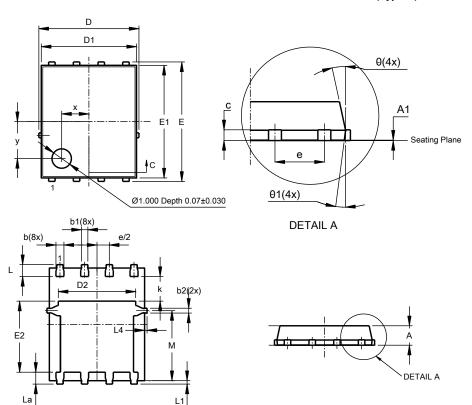
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8 (Type K)

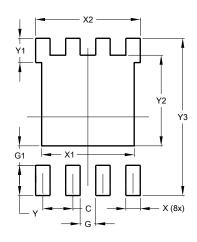


PowerDI5060-8						
(Type K)						
Dim	Min	Max	Тур			
Α	0.90	1.10	1.00			
A 1	0	0.05	0.02			
b	0.33	0.51	0.41			
b1	0.300	0.366	0.333			
b2	0.20	0.35	0.25			
С	0.23	0.33	0.277			
D	5	.15 BS0	\circ			
D1	4.85	4.95	4.90			
D2	-	-	3.98			
Е	6	.15 BS0	\sim			
E1	5.75	5.85	5.80			
E2	3.56	3.725	3.66			
е	1	.27BS0				
k	-	-	1.27			
L	0.51	0.71	0.61			
La	0.51	0.675	0.61			
L1	0.05	0.20	0.175			
L4	-	-	0.125			
М	3.50	3.71	3.605			
X	-	-	1.400			
у	-	-	1.900			
θ	10°	12°	11°			
θ1	6°	8°	7°			
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8 (Type K)



Dimensions	Value (in mm)		
С	1.270		
G	0.660		
G1	0.820		
Х	0.610		
X1	3.910		
X2	4.420		
Υ	1.270		
Y1	1.020		
Y2	3.810		
Y3	6.610		



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