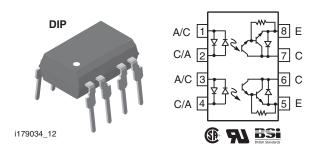


Vishay Semiconductors

Optocoupler, Photodarlington Output, AC Input, Internal RBE



DESCRIPTION

The ILD766 are bidirectional input optically coupled isolators. They consist of two gallium arsenide infrared emitting diodes coupled to a silicon NPN photodarlington per channel.

The ILD766 has two isolated channels in a single DIP package.

FEATURES

- Internal R_{BE} for better stability
- BV_{CEO} > 60 V
- · AC or polarity insensitive inputs
- Built-in reverse polarity input protection
- Industry standard DIP package
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC





ROHS

APPLICATIONS

 Designed for applications requiring detection or monitoring of AC signals

AGENCY APPROVALS

- UL1577, file no. E52744 system code H or J, double protection
- CSA 93751
- BSI IEC 60950; IEC 60065

ORDERING INFORMATION			
I L D	7 6 6 -	# DIP-8	
PART NUMBER		CTR BIN	
AGENCY CERTIFIED/PACKAGE	CTR (%)		
AGENCY CENTIFIED/PACKAGE	2 mA	1 mA	
UL, CSA, BSI	≥ 500	≥ 500	
DIP-8	ILD766-1	ILD766-2	

Note

• Additional options may be possible, please contact sales office.

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
INPUT							
Forward current		I _F	60	mA			
Power dissipation		P _{diss}	90	mW			
Derate linearly	from 25 °C		1.2	mW/°C			
OUTPUT							
Collector emitter breakdown voltage		BV _{CEO}	60	V			
Collector base breakdown voltage		BV _{CBO}	70	V			
Power dissipation		P _{diss}	100	mW			
Derate linearly	from 25 °C		1.33	mW/°C			



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ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
COUPLER						
Total dissipation		P _{tot}	400	mW		
Derate linearly	from 25 °C		5.3	mW/°C		
Isolation test voltage	t = 1 s	V _{ISO}	5300	V _{RMS}		
Isolation resistance	T _{amb} = 25 °C	R _{IO}	≥ 10 ¹²	Ω		
	T _{amb} = 100 °C	R _{IO}	≥ 10 ¹¹	Ω		
Creepage distance			≥ 7.0	mm		
Clearance distance			≥ 7.0	mm		
Comparative tracking index per DIN IEC 112/VDE 0303, part 1		СТІ	175			
Storage temperature		T _{stg}	- 55 to + 150	°C		
Operating temperature		T _{amb}	- 55 to + 100	°C		
Lead soldering time	at 260 °C		10	S		

Note

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT						
Forward voltage	I _F = ± 10 mA	V _F		1.2	1.5	V
OUTPUT						
Collector emitter breakdown voltage	I _C = 1.0 mA	BV _{CEO}	60	75		V
Collector base breakdown voltage	I _C = 10 μA	BV _{CBO}	60	90		V
Collector emitter leakage current	V _{CE} = 10 V	I _{CEO}		10	100	nA
COUPLER						
Collector emitter saturation voltage	$I_F = \pm 10 \text{ mA}, I_C = 10 \text{ mA}$	V _{CEsat}			1.0	V

Note

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering
evaluation. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
DC current transfer ratio	$V_{CE} = 5.0 \text{ V}, I_F = \pm 2 \text{ mA}$	CTR _{DC}	500			%

SWITCHING CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Rise time	$V_{CC} = 10 \text{ V}, I_F = \pm 2.0 \text{ mA},$	t _r		100		
Fall time	$R_L = 100 \Omega$	t _f		100		μs



TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

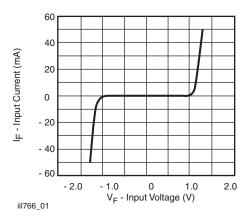


Fig. 1 - Input Characteristics

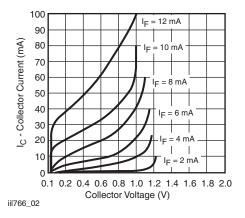


Fig. 2 - Transistor Current vs. Voltage

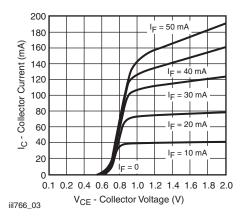


Fig. 3 - Transistor Output Current vs. Voltage

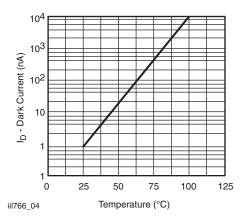


Fig. 4 - I_{CEO} at V_{CE} = 10 V vs. Temperature

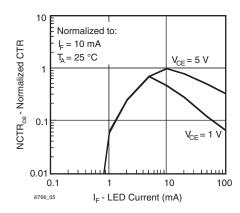


Fig. 5 - Normalized CTR vs. Forward Current

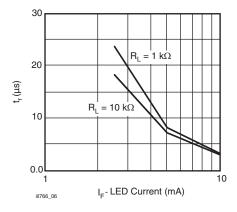
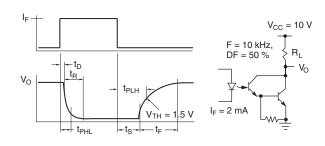


Fig. 6 - t_r vs. Forward Current



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iil766_07

Fig. 7 - Saturated Switching Characteristics Measurements-Schematic and Waveform

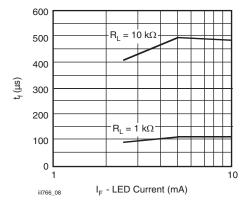


Fig. 8 - t_{fall} vs. Forward Current

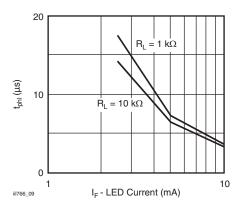


Fig. 9 - t_{phl} vs. Forward Current

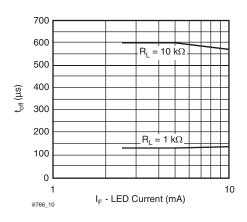


Fig. 10 - t_{off} vs. Forward Current

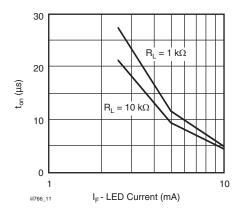


Fig. 11 - ton vs. Forward Current

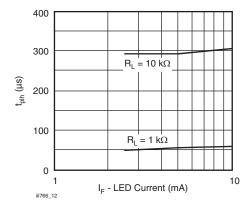
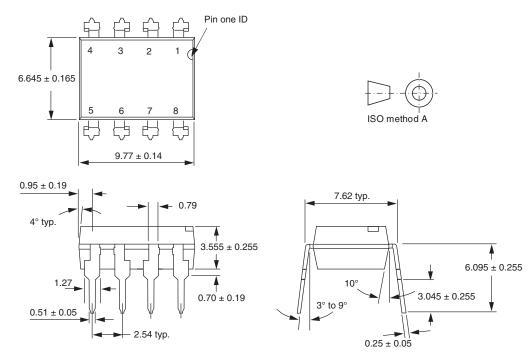


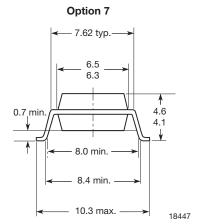
Fig. 12 - t_{plh} vs. Forward Current

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PACKAGE DIMENSIONS in millimeters



i178006





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