



#### N-CHANNEL ENHANCEMENT MODE MOSFET

#### **Product Summary**

BV <sub>DSS</sub>		RDS(ON) MAX	I⊳ Tc = +25°C	
	700V	1.4Ω @ V <sub>GS</sub> = 10V	6.1A	

#### **Features and Benefits**

- Low On-Resistance
- High BV<sub>DSS</sub> Rating for Power Application
- Low Input Capacitance
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. <u>https://www.diodes.com/quality/product-definitions/</u>

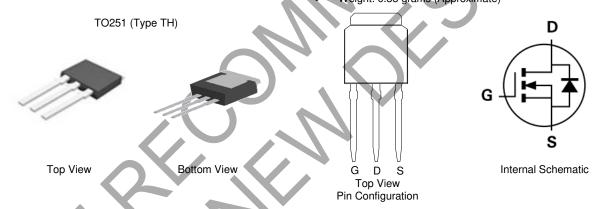
## **Description and Applications**

This MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

- Adaptors
- LCD & PDP TVs
- Lighting

### **Mechanical Data**

- Package: TO251
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 3
  - Weight: 0.33 grams (Approximate)



## Ordering Information (Note 4)

Part Number	Deskare	Packing		
Part Number	Package	Qty.	Carrier	
DMJ70H1D4SJ3	TO251 (Type TH)	75 Pieces	Tube	

Notes: 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied. 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

## **Marking Information**



)::= Manufacturer's Marking70H1D4 = Product Type Marking CodeYY<u>WW</u> = Date Code MarkingYY = Last Two Digits of Year (ex: 22 = 2022)<u>WW</u> = Week Code (01 to 53)



## Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit V	
Drain-Source Voltage	VDSS	700		
Gate-Source Voltage		Vgss	±30	V
Continuous Drain Current (Notes 5, 9) $V_{GS} = 10V$	$T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$	ID	6.1 3.9	А
Maximum Body Diode Forward Current (Note 6)		ls	6.1	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		Ідм	24	А
Pulsed Source Current (10µs Pulse, Duty Cycle = 1%)		lsм	24	А
Avalanche Current	L = 60mH	IAS	0.6	А
Avalanche Energy	L = 60mH	Eas	10	mJ
Peak Diode Recovery dV/dt (Note 7)		dV/dt	7.6	V/ns

## Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	$T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$	- P <sub>D</sub>	78 31	w
Thermal Resistance, Junction to Ambient (Note 6)		Reja	72	9C M
Thermal Resistance, Junction to Case (Note 5)		Rejc	1.6	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

## Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

					I	I	
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BVDSS	700	1		V	$V_{GS} = 0V, I_D = 250 \mu A$	
Zero Gate Voltage Drain Current	IDSS			1	μA	$V_{DS} = 700V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>		—	100	nA	$V_{GS}=\pm 30V,V_{DS}=0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	VGS(TH)	2	3.8	5	V	$V_{DS} = V_{GS}$ , $I_D = 250 \mu A$	
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>		1.26	1.4	Ω	$V_{GS} = 10V, I_D = 1A$	
Diode Forward Voltage	V <sub>SD</sub>	—	0.88	1.3	V	$V_{GS} = 0V, I_S = 5A$	
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance	Ciss	_	273	-			
Output Capacitance	Coss		16		pF	$V_{DS} = 100V$ , f = 1MHz $V_{GS} = 0V$	
Reverse Transfer Capacitance	Crss		1.5			VGS – UV	
Gate Resistance	Rg		3.9		Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	
Total Gate Charge	Qg		9				
Gate-Source Charge	Q <sub>gs</sub>		1.9		nC	$V_{DD} = 480V, I_D = 1.5A$ $V_{GS} = 10V$	
Gate-Drain Charge	Q <sub>gd</sub>		3.6			VG3 - 10 V	
Turn-On Delay Time	td(on)		7				
Turn-On Rise Time	t <sub>R</sub>		7	_	ns	$V_{DD} = 400V, V_{GS} = 13V$	
Turn-Off Delay Time	tD(OFF)	_	27	_	115	$R_g = 10.2\Omega, I_D = 1.5A$	
Turn-Off Fall Time	tF	_	15	_			
Body Diode Reverse Recovery Time	t <sub>RR</sub>	_	134		ns	$l_{0} = 1.5$ dl/dt = 100 Å/up	
Body Diode Reverse Recovery Charge	Q <sub>RR</sub>	_	0.7	_	μC	I <sub>S</sub> = 1.5A, dl/dt = 100A/μs	

Notes:

Device mounted on an infinite heatsink.
Device mounted on FR-4 substrate PC board, 2oz. copper, with minimum recommended pad layout.

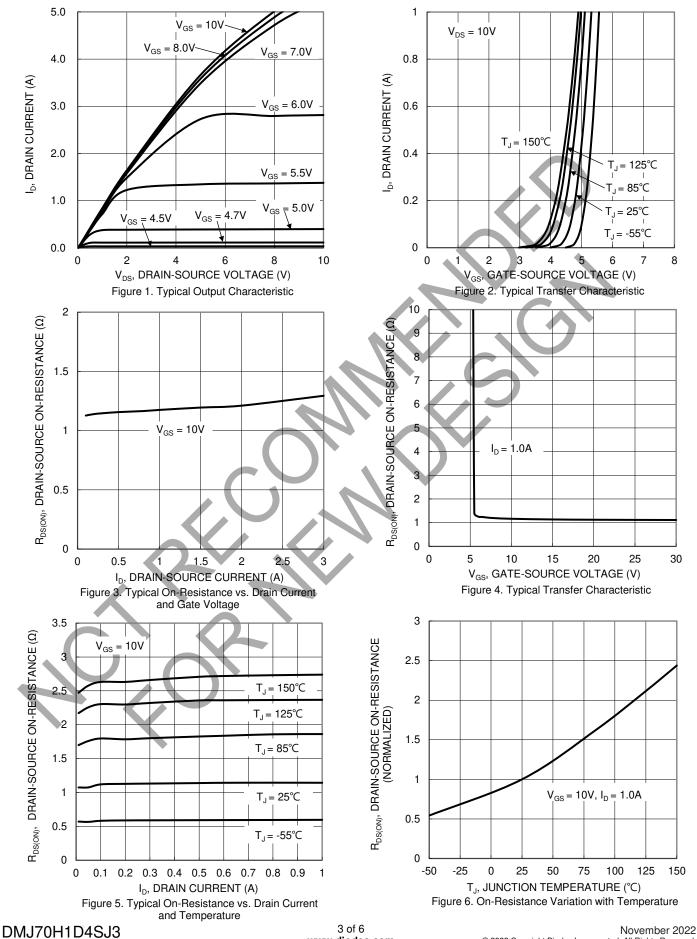
7. Guaranteed by design. Not subject to production testing.

8. Short duration pulse test used to minimize self-heating effect.

9. Drain current limited by maximum junction temperature.



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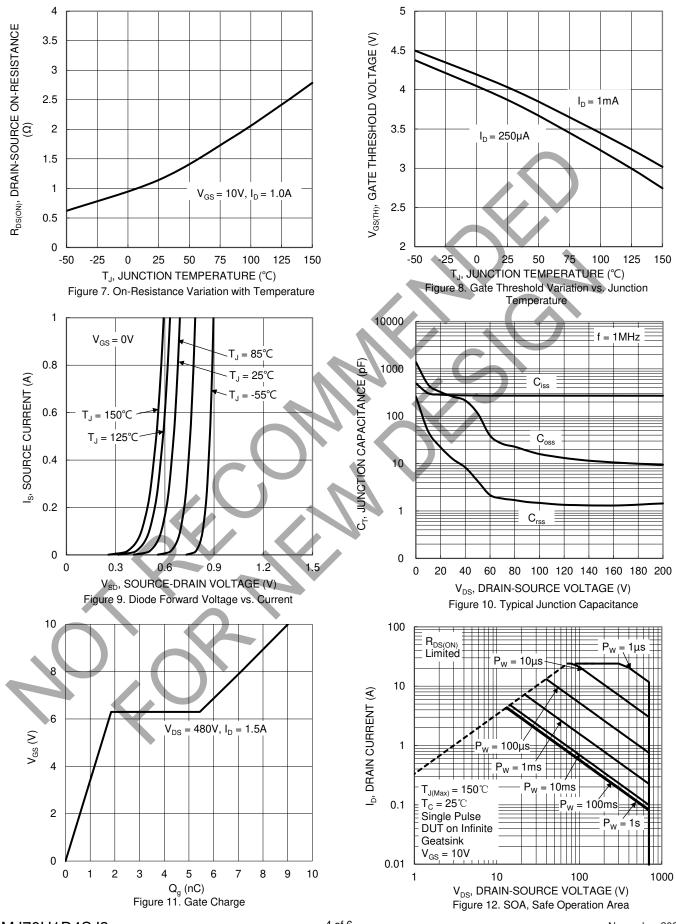


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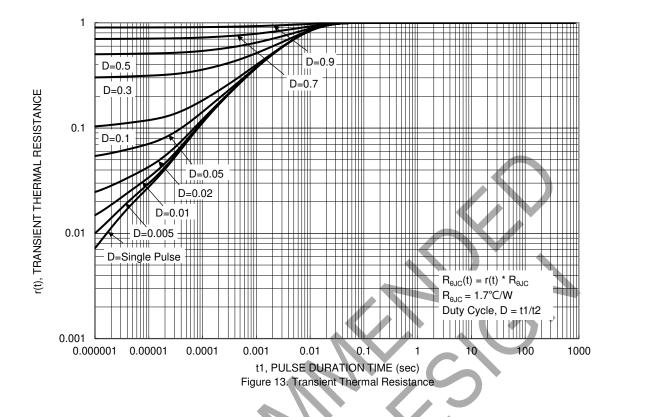
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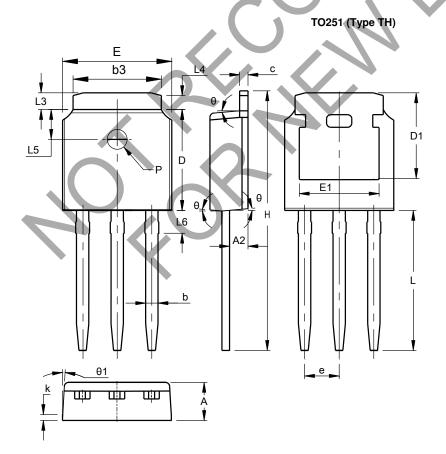
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# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.



TO251 (Type TH)						
Dim	Min	Max	Тур			
Α	2.20	2.40	2.30			
A2	0.97	1.17	1.07			
b	0.68	0.90	0.78			
b3	5.20	5.50	5.33			
c	0.43	0.63	0.53			
D	5.98	6.22	6.10			
D1	5	.30 RE	F			
e	2.	286 BS	C			
E	6.40	6.80	6.60			
E1	4.63	5.03	4.83			
Η	16.22	16.82	16.52			
k	C	0.40REF				
L	9.15	9.65	9.40			
L3	0.88	1.28	1.02			
L4	0	.75 RE	-			
L5	1.65	1.95	1.80			
L6	0.85	1.25	1.05			
PØ	1.20					
θ	5°	9°	7°			
θ1	5°	9°	7°			
All Dimensions in mm						

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