# B0520LW/B0530W/B0540W

Taiwan Semiconductor

# 500mA, 20V - 40V Schottky Barrier Diode

### FEATURES

- Low power loss, high current capability, low  $\ensuremath{\mathsf{V}_{\mathsf{F}}}$
- Surface mount device type
- Moisture sensitivity level: level 1, per J-STD-020
- RoHS Compliant
- Halogen-free according to IEC 61249-2-21

### APPLICATIONS

- Switching mode power supply (SMPS)
- Adapters
- Lighting application

### MECHANICAL DATA

- Case: SOD-123
- Molding compound meets UL 94 V-0 flammability rating
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Polarity: Indicated by cathode band
- Weight: 10.0mg (approximately)

KEY PARAMETERS			
PARAMETER	VALUE	UNIT	
I <sub>F</sub>	500	mA	
V <sub>RRM</sub>	20 - 40	V	
I <sub>FSM</sub>	5.5	А	
T <sub>J MAX</sub>	125	°C	
Package	SOD-123		
Configuration	Single Die		









ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25°C unless otherwise noted)					
PARAMETER	SYMBOL	B0520LW	B0530W	B0540W	UNIT
Marking code on the device		SD	SE	SF	
Power Dissipation	PD		410		mW
Repetitive peak reverse voltage	V <sub>RRM</sub>	20	30	40	V
Reverse voltage, total rms value	V <sub>R(RMS)</sub>	14	21	28	V
Forward current	I <sub>F</sub>	500		mA	
Surge peak forward current, 8.3ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	5.5		A	
Junction temperature	TJ	- 55 to +125		°C	
Storage temperature	T <sub>STG</sub>	- 55 to +125		°C	





THERMAL PERFORMANCE			
PARAMETER	SYMBOL	ТҮР	UNIT
Junction-to-ambient thermal resistance	R <sub>eJA</sub>	244	°C/W

ELECTRICAL SPECIFICATIONS (T <sub>A</sub> = 25°C unless otherwise noted)					
PARAMETER		CONDITIONS	SYMBOL	VALUE	UNIT
Reverse Breakdown Voltage (Minimum Value)	B0520LW	Ι <sub>R</sub> = 250μΑ		20	V
	B0530W	I <sub>R</sub> = 130μΑ	$V_{BR}$	30	V
	B0540W	I <sub>R</sub> = 20μΑ		40	V
	B0520LW			0.300	V
	B0530W	$I_F = 100 \text{mA}$		0.375	V
	B0540W			-	V
(1)	B0520LW			0.385	V
Forward voltage <sup>(1)</sup> (Maximum Value)	B0530W	$I_F = 500 \text{mA}$	V <sub>F</sub>	0.430	V
	B0540W			0.510	V
	B0520LW			-	V
	B0530W	I <sub>F</sub> = 1000mA		-	V
	B0540W			0.620	V
	B0520LW	V <sub>R</sub> = 10V	I <sub>R</sub>	75	μA
	B0530W			-	μA
	B0540W			-	μA
	B0520LW	V <sub>R</sub> = 15V		-	μA
	B0530W			20	μA
	B0540W			-	μA
- (2)	B0520LW			250	μA
Reverse current @ rated V <sub>R</sub> <sup>(2)</sup> (Maximum Value)	B0530W	V <sub>R</sub> = 20V		-	μA
	B0540W			10	μA
	B0520LW			-	μA
	B0530W	$V_{R} = 30V$		130	μA
	B0540W			-	μA
	B0520LW			-	μA
	B0530W	$V_{R} = 40V$		-	μA
	B0540W			20	μA
Junction capacitance		1MHz, V <sub>R</sub> = 0V	CJ	170	pF

ORDERING INFORMATION			
ORDERING CODE <sup>(1)</sup>	PACKAGE	PACKING	
B05x RHG	SOD-123	3,000 / 7" Tape & Reel	
1.1			

#### Notes:

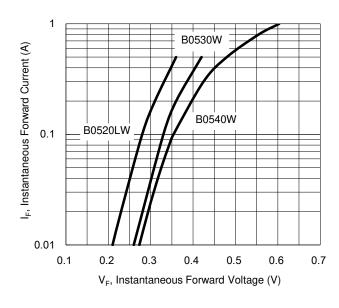
1. "x" defines voltage from 20V(B0520LW) to 40V(B0540W)



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### **CHARACTERISTICS CURVES**

(T<sub>A</sub> = 25°C unless otherwise noted)



#### **Fig.1 Forward Characteristics**

0.75 () 0.5 0.25 0 25 50 75 100 125 T<sub>L</sub>, Lead Temperature (C)

#### Fig.2 Forward Current Derating Curve

Fig.3 Power Derating Curve

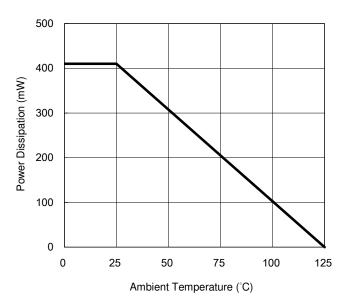
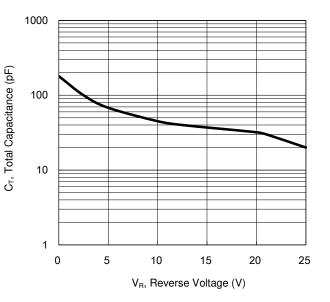
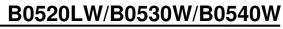


Fig.4 Typical Capacitance vs. Reverse Voltage





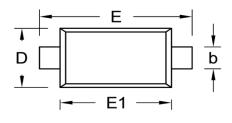
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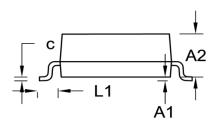
#### **PACKAGE OUTLINE DIMENSIONS**

SOD-123

TAIWAN SEMICONDUCTOR

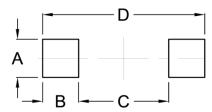
**9**5





DIM.	Unit (mm)		Unit	(inch)
	Min.	Max.	Min.	Max.
A1	-	0.10	-	0.004
A2	0.95	1.30	0.037	0.051
b	0.45	0.70	0.018	0.028
С	0.05	0.15	0.002	0.006
D	1.40	1.70	0.055	0.067
E	3.55	3.85	0.140	0.152
E1	2.55	2.85	0.100	0.112
L1	0.50 (REF)		0.020	(REF)

#### SUGGESTED PAD LAYOUT



Symbol	Unit (mm)	Unit (inch)
A	0.95	0.037
В	0.90	0.035
С	2.25	0.089
D	4.05	0.159

#### Notes:

This recommended land pattern is for reference purposes only. Please consult your manufacturing group to ensure your PCB design guidelines are met.



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