

Product Summary

BV _{DSS}	R _{DS(ON)} MAX	I _D MAX T _C = +25°C (Note 9)
60V	3.1mΩ @ V _{GS} = 10V	100A

Description and Applications

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

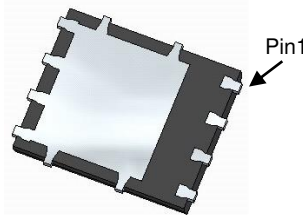
- Switching
- Synchronous rectifications
- DC-DC converters

Site 1:

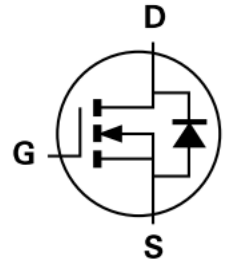
PowerDI5060-8



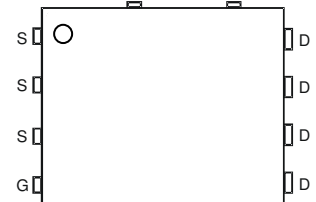
Top View



Bottom View



Internal Schematic



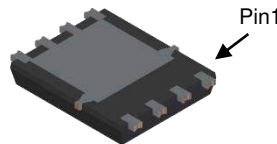
Top View
Pin Configuration

Site 2:

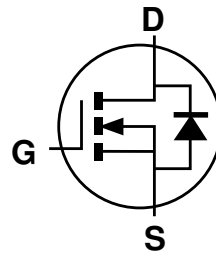
PowerDI5060-8/SWP (Type UX)



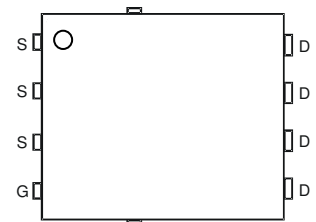
Top View



Bottom View



Internal Schematic



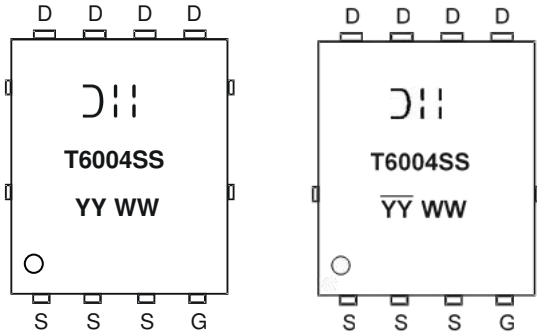
Top View
Pin Configuration

Ordering Information (Note 4)

Part Number	Package	Packing	
		Qty.	Carrier
DMT6004SPS-13	PowerDI5060-8	2,500	Tape & Reel
DMT6004SPS-13	PowerDI5060-8/SWP (Type UX)	2,500	Tape & Reel

- Notes:
1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



D = Manufacturer's Marking
 T6004SS = Product Type Marking Code
 YYWW = Date Code Marking
 YY or \overline{YY} = Year (ex: 23 = 2023)
 WW = Week (01 to 53)

Maximum Ratings (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	60	V
Gate-Source Voltage			V_{GSS}	± 20	V
Continuous Drain Current (Note 5)	Steady State	$T_A = +25^\circ\text{C}$	I_D	23	A
		$T_A = +70^\circ\text{C}$		18	
Continuous Drain Current (Notes 6 & 9)		$T_C = +25^\circ\text{C}$	I_D	100	A
		$T_C = +70^\circ\text{C}$		100	
Maximum Continuous Body Diode Forward Current (Notes 6 & 9)			I_S	100	A
Pulsed Drain Current (10 μs Pulse, Duty Cycle = 1%)			I_{DM}	400	A
Pulsed Body Diode Forward Current (10 μs Pulse, Duty Cycle = 1%)			I_{SM}	400	A
Avalanche Current, $L = 0.2\text{mH}$			I_{AS}	45	A
Avalanche Energy, $L = 0.2\text{mH}$			E_{AS}	200	mJ

Thermal Characteristics

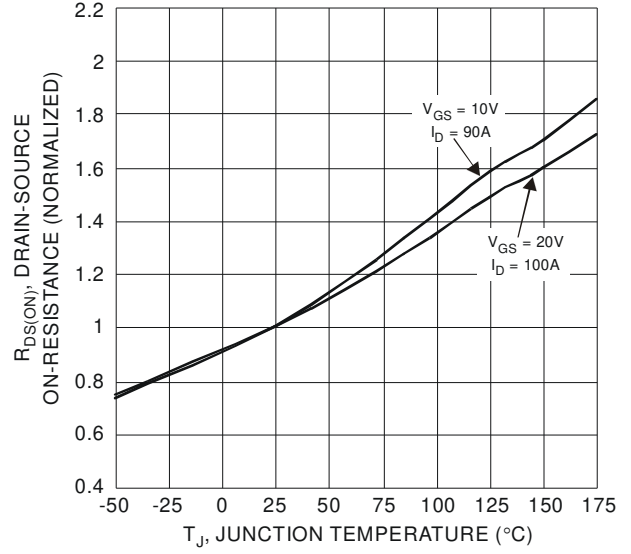
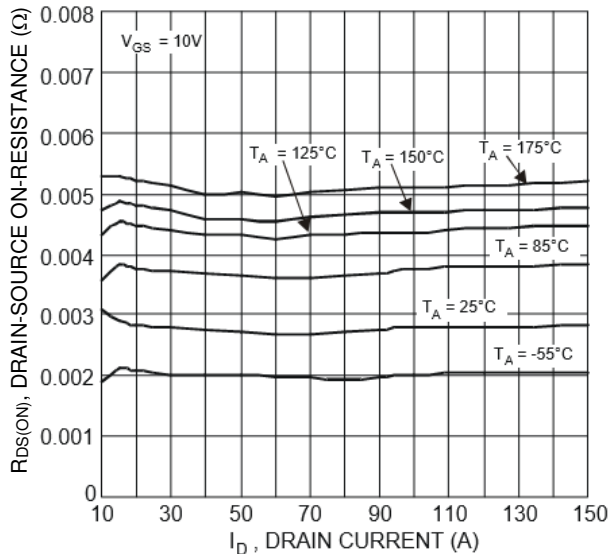
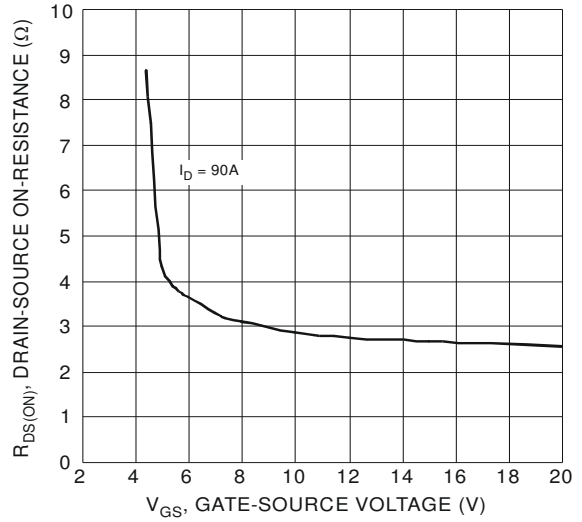
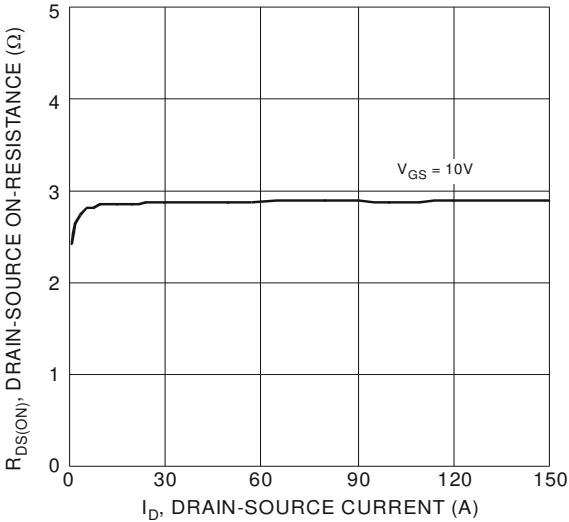
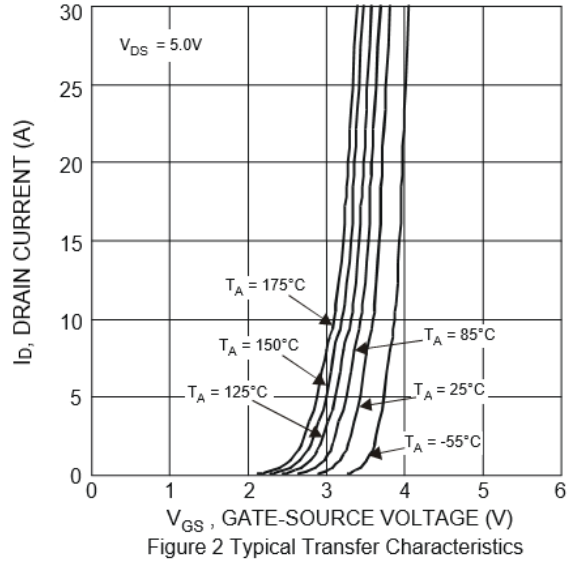
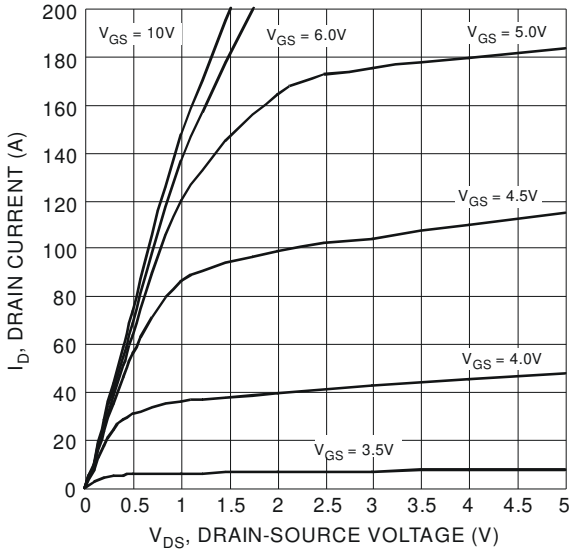
Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	$T_A = +25^\circ\text{C}$	P_D	2.6	W
Thermal Resistance, Junction to Ambient	Steady State	$R_{\theta JA}$	47	$^\circ\text{C}/\text{W}$
Total Power Dissipation (Note 6)	$T_C = +25^\circ\text{C}$	P_D	139	W
Thermal Resistance, Junction to Case		$R_{\theta JC}$	0.9	$^\circ\text{C}/\text{W}$
Operating and Storage Temperature Range		T_J, T_{STG}	-55 to +150	$^\circ\text{C}$

- Notes:
5. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.
 6. Thermal resistance from junction to soldering point (on the exposed drain pad).
 7. Short duration pulse test used to minimize self-heating effect.
 8. Guaranteed by design. Not subject to production testing.
 9. Package limited.

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	60	—	—	V	V _{GS} = 0V, I _D = 1mA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	1	μA	V _{DS} = 48V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±100	nA	V _{GS} = ±20V, V _{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	2	2.5	4	V	V _{DS} = V _{GS} , I _D = 250μA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	—	3.1	mΩ	V _{GS} = 10V, I _D = 50A
Diode Forward Voltage	V _{SD}	—	0.9	1.2	V	V _{GS} = 0V, I _S = 20A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	—	4,556	—	pF	V _{DS} = 30V, V _{GS} = 0V, f = 1MHz
Output Capacitance	C _{oss}	—	1,383	—		
Reverse Transfer Capacitance	C _{rss}	—	105.2	—		
Gate Resistance	R _g	—	0.7	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Total Gate Charge	Q _g	—	95.4	—	nC	V _{DD} = 30V, I _D = 90A, V _{GS} = 10V
Gate-Source Charge	Q _{gs}	—	21.6	—		
Gate-Drain Charge	Q _{gd}	—	20.4	—		
Turn-On Delay Time	t _{D(ON)}	—	13.2	—	ns	V _{DD} = 30V, V _{GS} = 10V, I _D = 90A, R _g = 3.5Ω
Turn-On Rise Time	t _R	—	11.7	—		
Turn-Off Delay Time	t _{D(OFF)}	—	31	—		
Turn-Off Fall Time	t _F	—	12	—		
Body Diode Reverse Recovery Time	t _{RR}	—	50.5	—	ns	I _F = 50A, di/dt = 100A/μs
Body Diode Reverse Recovery Charge	Q _{RR}	—	80.8	—	nC	

Notes: 7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to production testing.



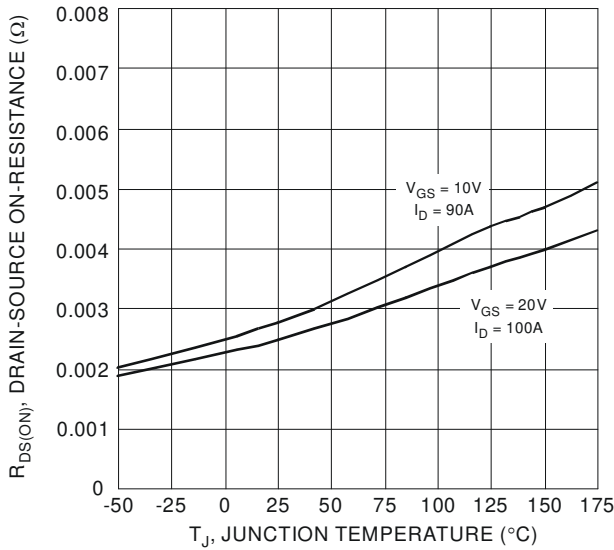


Figure 7 On-Resistance Variation with Temperature

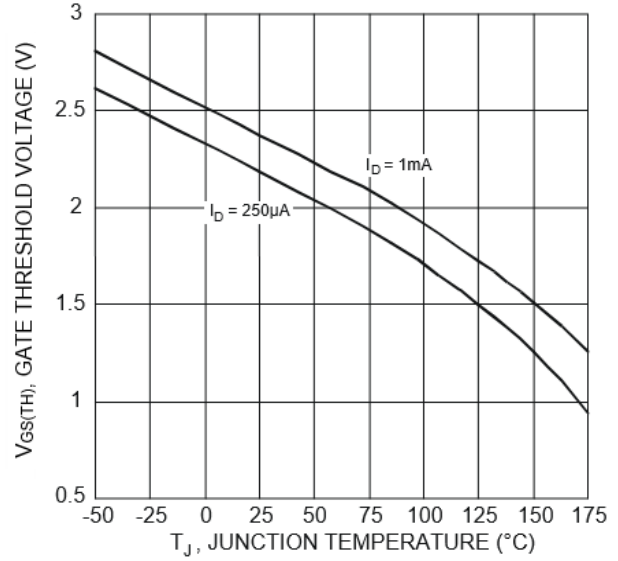


Figure 8 Gate Threshold Variation vs. Junction Temperature

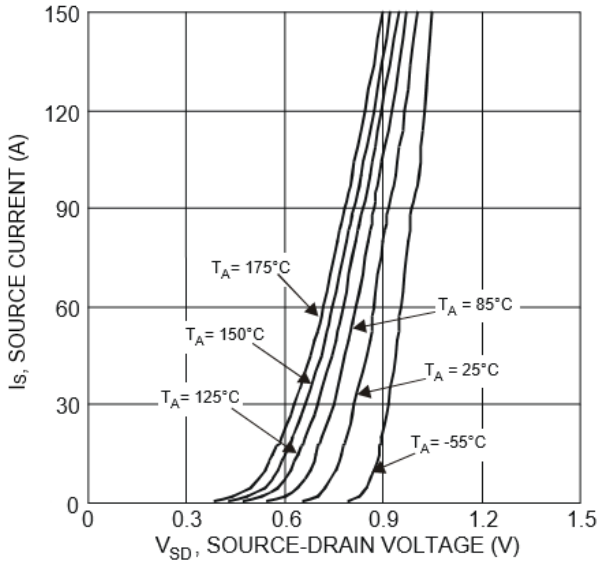


Figure 9 Diode Forward Voltage vs. Current

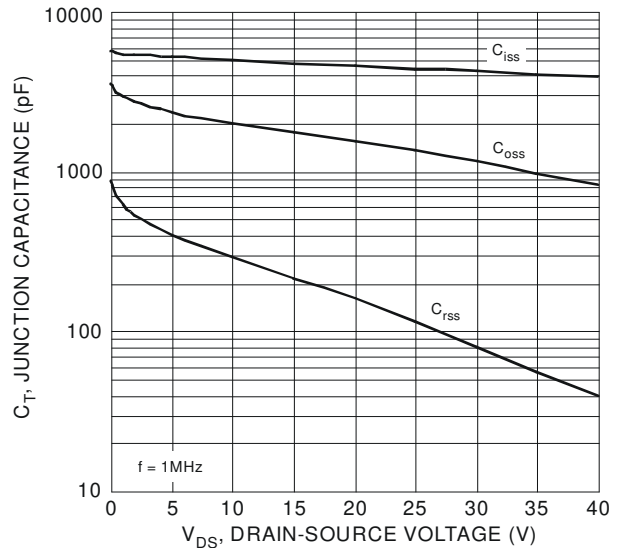


Figure 10 Typical Junction Capacitance

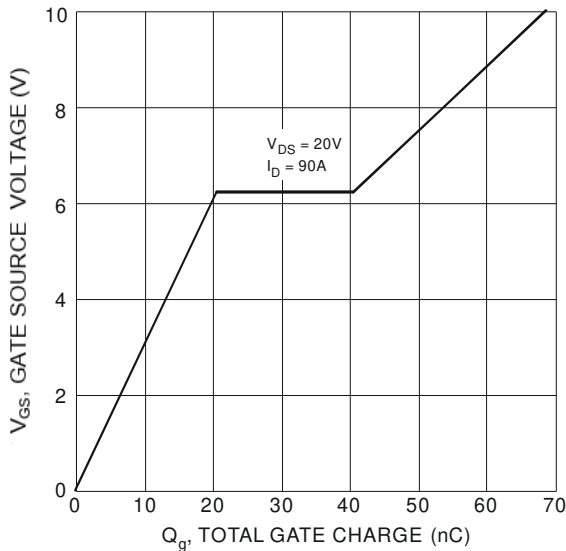


Figure 11 Gate Charge

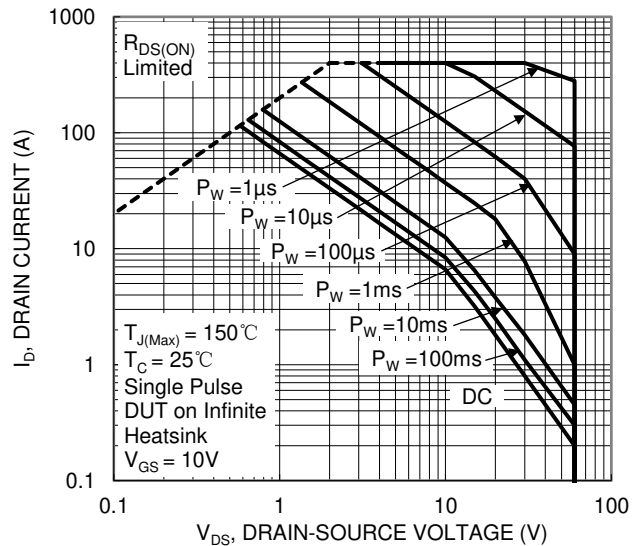


Figure 12 SOA, Safe Operation Area

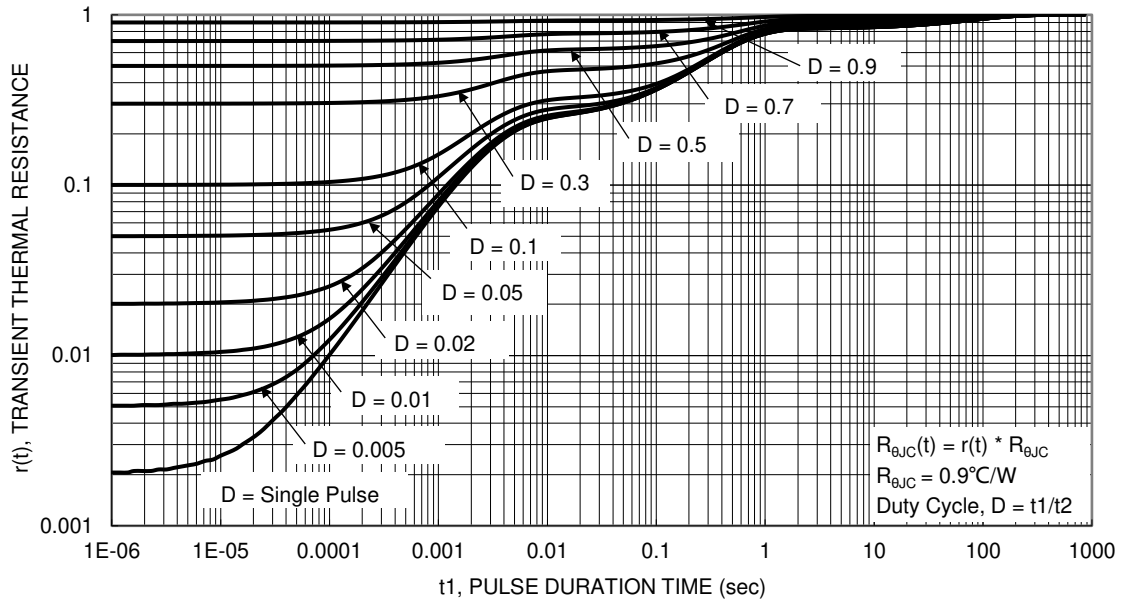


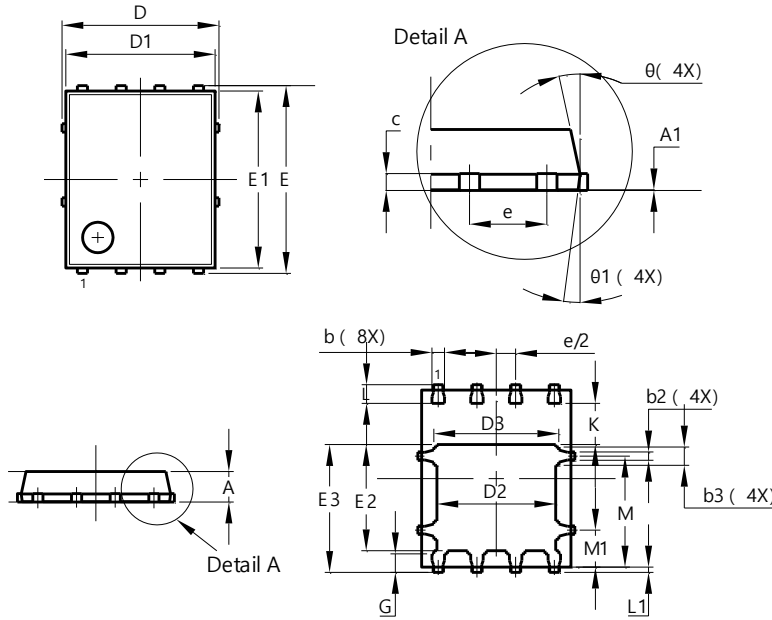
Figure 13. Transient Thermal Resistance

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

Site 1:

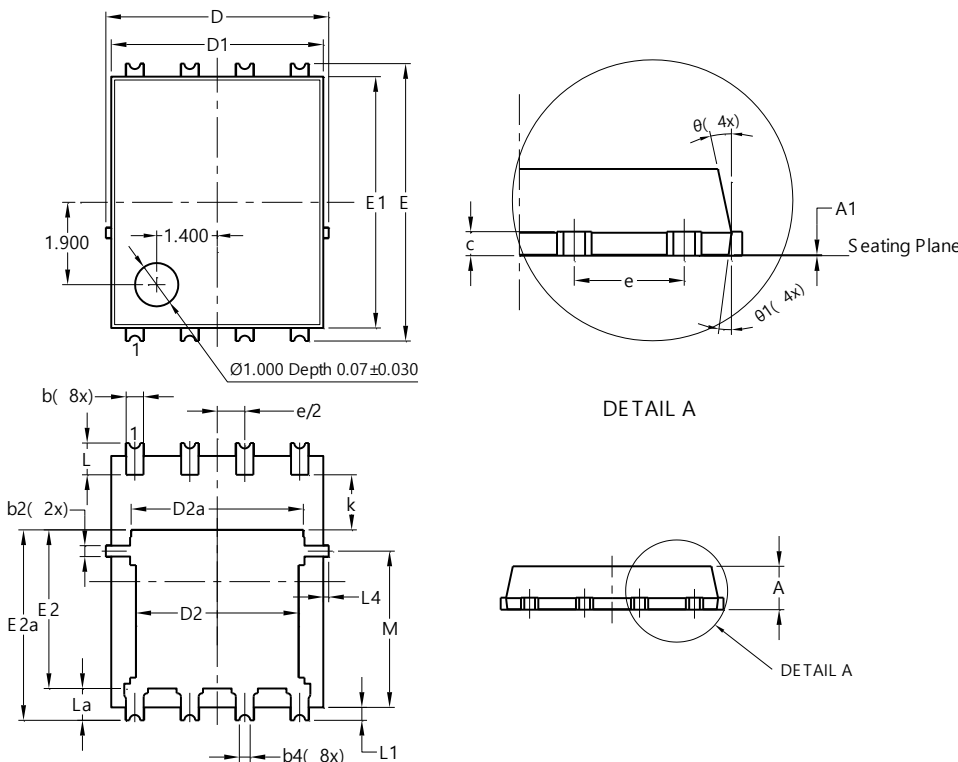
PowerDI5060-8



PowerDI5060-8			
Dim	Min	Max	Typ
A	0.90	1.10	1.00
A1	0.00	0.05	—
b	0.33	0.51	0.41
b2	0.200	0.350	0.273
b3	0.40	0.80	0.60
c	0.230	0.330	0.277
D	5.15 BSC		
D1	4.70	5.10	4.90
D2	3.70	4.10	3.90
D3	3.90	4.30	4.10
E	6.15 BSC		
E1	5.60	6.00	5.80
E2	3.28	3.68	3.48
E3	3.99	4.39	4.19
e	1.27 BSC		
G	0.51	0.71	0.61
K	0.51	—	—
L	0.51	0.71	0.61
L1	0.100	0.200	0.175
M	3.235	4.035	3.635
M1	1.00	1.40	1.21
theta	10°	12°	11°
theta1	6°	8°	7°
All Dimensions in mm			

Site 2:

PowerDI5060-8/SWP (Type UX)



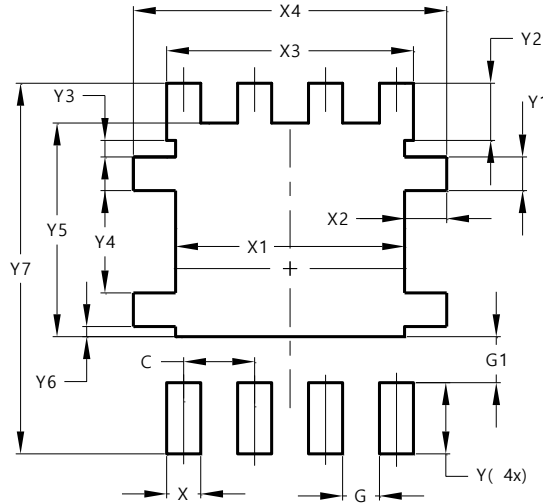
PowerDI5060-8/SWP (Type UX)			
Dim	Min	Max	Typ
A	0.90	1.10	1.00
A1	0	0.05	--
b	0.30	0.50	0.41
b2	0.20	0.35	0.25
b4	0.25REF		
c	0.230	0.330	0.277
D	5.15 BSC		
D1	4.70	5.10	4.90
D2	3.56	3.96	3.76
D2a	3.78	4.18	3.98
E	6.40 BSC		
E1	5.60	6.00	5.80
E2	3.46	3.86	3.66
E2a	4.195	4.595	4.395
e	1.27BSC		
k	1.05	--	--
L	0.635	0.835	0.735
La	0.635	0.835	0.735
L1	0.200	0.400	0.300
L1a	0.050REF		
L4	0.025	0.225	0.125
M	3.205	4.005	3.605
theta	10°	12°	11°
theta1	6°	8°	7°
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

Site 1:

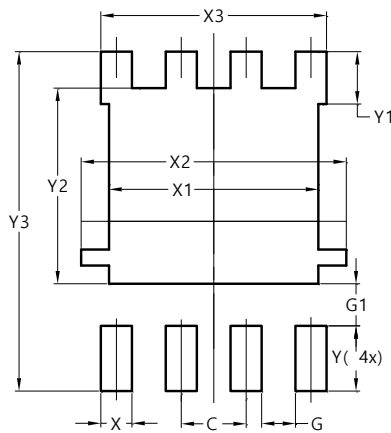
PowerDI5060-8



Dimensions	Value (in mm)
C	1.270
G	0.660
G1	0.820
X	0.610
X1	4.100
X2	0.755
X3	4.420
X4	5.610
Y	1.270
Y1	0.600
Y2	1.020
Y3	0.295
Y4	1.825
Y5	3.810
Y6	0.180
Y7	6.610

Site 2:

PowerDI5060-8/SWP (Type UX)



Dimensions	Value (in mm)
C	1.270
G	0.660
G1	0.820
X	0.610
X1	4.100
X2	5.190
X3	4.420
Y	1.270
Y1	1.020
Y2	3.810
Y3	6.610

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