



Complementary (N- and P-Channel) MOSFET

PRODUCT SUMMARY						
	V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)			
N-Channel	30	0.018 at V _{GS} = 10 V	8.8			
	30	0.027 at V _{GS} = 4.5 V	7.0			
P-Channel	- 8	0.042 at V _{GS} = - 4.5 V	- 5.7			
	- 0	0.060 at V _{GS} = - 2.5 V	- 4.8			

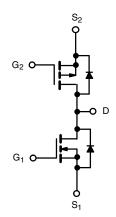
FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFET
- Compliant to RoHS Directive 2002/95/EC



APPLICATIONS

- Level Shift
- · Load Switch



		SO-8		
S ₁	1		8	D
G_1	2		7	D
S_2	3		6	D
G_2	4		5	D
	l	Top View	ı	

Ordering Information: Si4501ADY-T1-E3 (Lead (Pb)-free)

Si4501ADY-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted							
			N-Channel		P-Channel		
Parameter	Symbol	10 s	Steady State	10 s	Steady State	Unit	
Drain-Source Voltage		V_{DS}	30		- 8		V
Gate-Source Voltage		V _{GS}	± 20		± 8		
Continuous Dunin Comment /T 450 90\8 b	T _A = 25 °C	l _D	8.8	6.3	- 5.7	- 4.1	
Continuous Drain Current (T _J = 150 °C) ^{a, b}	T _A = 70 °C		7	5.2	- 4.5	- 3.3	
Pulsed Drain Current		I _{DM}	30 - 30		- 30	Α	
Continuous Source Current (Diode Conduction) ^{a, b}		I _S	1.8	1.0	- 1.8	- 1.0	
Maximum Power Dissipation ^{a, b}	T _A = 25 °C	P _D	2.5	1.3	2.5	1.3	W
	T _A = 70 °C	' D	1.6	0.84	1.6	0.84	
Operating Junction and Storage Temperature	T _J , T _{stg}	- 55 to 150				°C	

THERMAL RESISTANCE RATINGS								
		N-Ch	N-Channel P-Channel					
Parameter	Symbol	Тур.	Max.	Тур.	Max.	Unit		
Manipular to Austrian I	t ≤ 10 s	R _{thJA}	40	50	42	50	°C/W	
Maximum Junction-to-Ambient ^a	Steady State		75	95	76	95		
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	18	23	21	26		

Notes:

a. Surface Mounted on FR4 board.

 $b.\ t \leq 10\ s.$

Si4501ADY

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SPECIFICATIONS $T_J = 25^{\circ}$	C, unless	otherwise noted								
Parameter	Symbol	Test Conditions		Min.	Typ. ^a	Max.	Unit			
Static										
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	N-Ch	0.8		1.8	V			
		$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	P-Ch	- 0.45		- 1.0	v			
Cata Bady Laglaga	1	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$	N-Ch			± 100	nA			
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$	P-Ch			± 100				
		$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}$	N-Ch			1				
Zero Gate Voltage Drain Current	1	V _{DS} = - 8 V, V _{GS} = 0 V	P-Ch			- 1	٦ . ا			
Zero Gate voltage Drain Current	I _{DSS}	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$	N-Ch			5	μΑ			
		$V_{DS} = -8 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$	P-Ch			- 5				
h		$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	N-Ch	30						
On-State Drain Current ^D	I _{D(on)}	V _{DS} = - 5 V, V _{GS} = - 4.5 V	P-Ch	- 20			A			
		V _{GS} = 10 V, I _D = 8.8 A	N-Ch		0.015	0.018				
		V _{GS} = - 4.5 V, I _D = - 5.7 A	P-Ch		0.030	0.042	Ω			
Drain-Source On-State Resistance ^b	R _{DS(on)}	V _{GS} = 4.5 V, I _D = 7.0 A	N-Ch		0.022	0.027				
		V _{GS} = - 2.5 V, I _D = - 4.8 A	P-Ch		0.048	0.060				
<u></u>	9 _{fs}	V _{DS} = 15 V, I _D = 8.8 A	N-Ch		18					
Forward Transconductance ^b		V _{DS} = - 15 V, I _D = - 5.7 A	P-Ch		12		S			
L	V _{SD}	I _S = 1.8 A, V _{GS} = 0 V	N-Ch		0.73	1.1	T			
Diode Forward Voltage ^b		I _S = - 1.8 A, V _{GS} = 0 V	P-Ch		- 0.75	- 1.1	V			
Dynamic ^a						•				
Total Gate Charge	Qg		N-Ch		11.5	20				
Total Gate Charge	αg	N-Channel $V_{DS} = 15 \text{ V}, V_{GS} = 5 \text{ V}, I_{D} = 8.8 \text{ A}$	P-Ch		13.5	20	nC			
Gate-Source Charge	Q_{gs}	VDS = 10 V, VGS = 0 V, ID = 0.0 A	N-Ch		3					
	ys	P-Channel	P-Ch		2.2		_			
Gate-Drain Charge	Q_{gd}	$V_{DS} = -4 \text{ V}, V_{GS} = -5 \text{ V}, I_{D} = -5.7 \text{ A}$	N-Ch		4		-			
	J-		P-Ch N-Ch		3 15	22				
Turn-On Delay Time	t _{d(on)}	N-Channel	P-Ch		21	40	1			
	t _r	$V_{DD} = 15 \text{ V}, R_L = 15 \Omega$	N-Ch		8	15				
Rise Time		$I_D \cong 1 \text{ A}, V_{GEN} = 10 \text{ V}, R_G = 6 \Omega$	P-Ch		45	70	1			
Turn-Off Delay Time	t _{d(off)}	P-Channel	N-Ch		35	50	1			
		$V_{DD} = -4 \text{ V}, R_L = 4 \Omega$	P-Ch		60	100	ns			
Fall Time	t _f	$I_D \cong -1$ A, $V_{GEN} = -4.5$ V, $R_g = 6 \Omega$	N-Ch		10	20				
		-	P-Ch		55	85				
Source-Drain Reverse Recovery Time	t _{rr}	I _F = 1.8 A, dI/dt = 100 A/μs	N-Ch		30	60				
<u> </u>	"		P-Ch		50	100				

Notes:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Guaranteed by design, not subject to production testing.

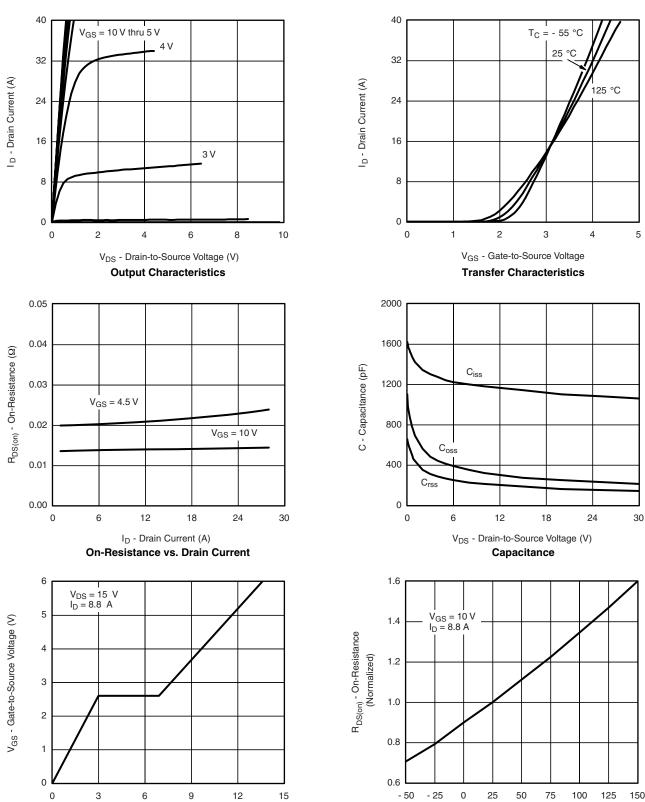
b. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.







N-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Q_g - Total Gate Charge (nC)

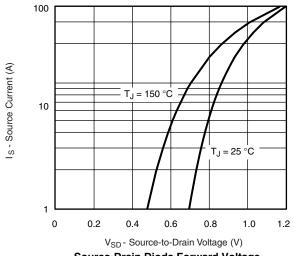
Gate Charge

T_J - Junction Temperature (°C)

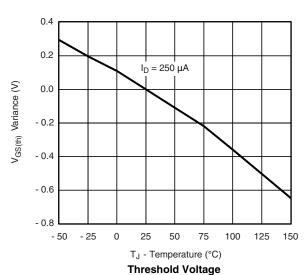
On-Resistance vs. Junction Temperature

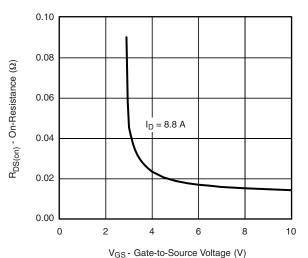
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N-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

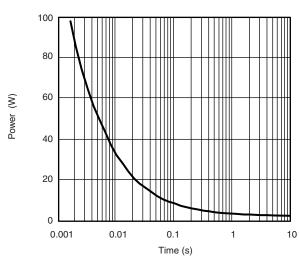


Source-Drain Diode Forward Voltage

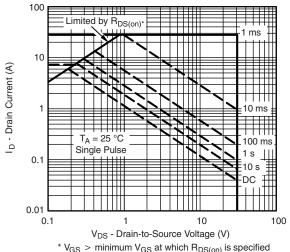




On-Resistance vs. Gate-to-Source Voltage



Single Pulse Power, Junction-to-Ambient



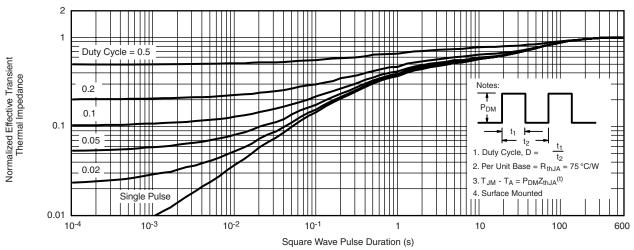
* V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

Safe Operating Area

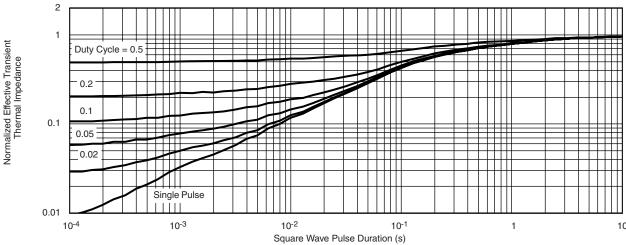




N-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient

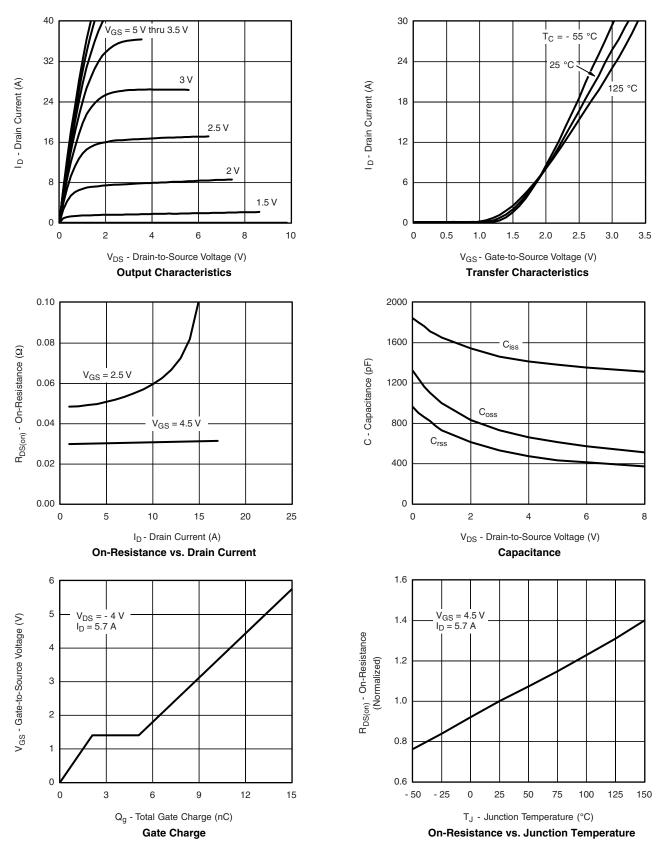


Normalized Thermal Transient Impedance, Junction-to-Foot

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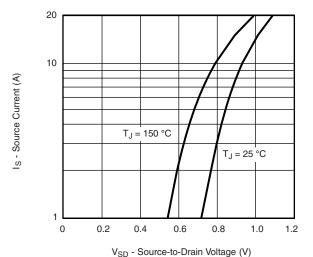
P-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



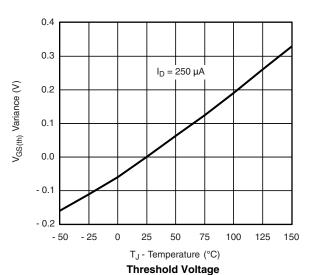




P-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

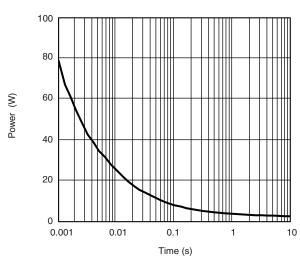


Source-Drain Diode Forward Voltage

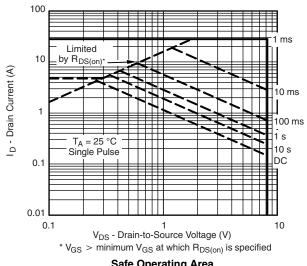


0.20 R_{DS(on)} - On-Resistance (Ω) 0.15 0.10 $I_D = 5.7 A$ 0.05 0.00 2 0 4 V_{GS} - Gate-to-Source Voltage (V)

On-Resistance vs. Gate-to-Source Voltage



Single Pulse Power, Junction-to-Ambient

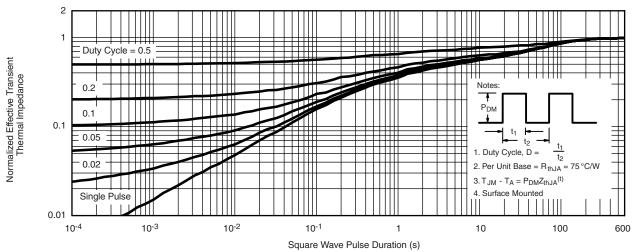


Safe Operating Area

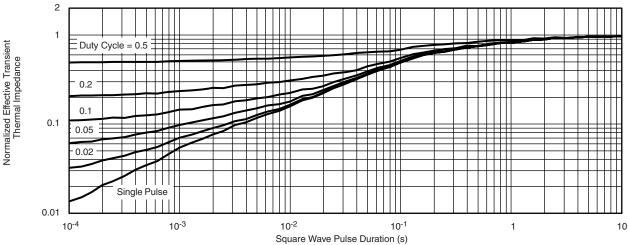
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P-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

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