

#### 20V P-CHANNEL ENHANCEMENT MODE MOSFET

#### **Product Summary**

V <sub>(BR)DSS</sub>	Max R <sub>DS(on)</sub> (Note 6)	Max I <sub>D</sub> T <sub>A</sub> = 25°C
	$60m\Omega @ V_{GS} = -4.5V$	-4.23A
-20V	90mΩ @ V <sub>GS</sub> = -2.5V	-3.49A
	113m $\Omega$ @ $V_{GS} = -1.8V$	-3.11A

### Description

This new generation MOSFET has been designed to minimize the onstate resistance (R<sub>DS(on)</sub>) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

### **Applications**

- DC-DC Converters
- Motor Control
- Power management functions
- Analog Switch

#### **Features**

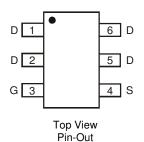
- Low Input Capacitance
- Low On-Resistance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

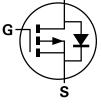
#### **Mechanical Data**

- Case: TSOT26
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.0013 grams (approximate)









**Equivalent Circuit** 

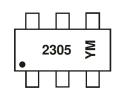
### Ordering Information (Note 4)

Part Number	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
DMP2305UVT-7	2305	7	8	3,000
DMP2305UVT-13	2305	13	8	10,000

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and 1000ppm antimony compounds.
   4. For packaging details, go to our website at http://www.diodes.com.

## **Marking Information**



2305 = Product Type Marking Code YM = Date Code Marking

Y = Year (ex: Y = 2011)M = Month (ex: 9 = September)

Date Code Key

Year	201	1	2012		2013	20	14	2015		2016	2	2017
Code	Υ		Z		Α	[	3	С		D		Е
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D





## Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Drain-Source Voltage	$V_{DSS}$	-20	V		
Gate-Source Voltage	$V_{GSS}$	±8	V		
Continuous Drain Current (Note 6) V <sub>GS</sub> = -4.5V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I <sub>D</sub>	-4.23 -2.98	Α
Continuous Drain Current (Note 6) $V_{GS} = -2.5V$ Steady $T_A = +25^{\circ}C$ State $T_A = +70^{\circ}C$		I <sub>D</sub>	-3.49 -2.79	Α	
Maximum Continuous Body Diode Forward Current	I <sub>S</sub>	-4.23	Α		
Pulsed Drain Current (10μs pulse, duty cycle = 1%	I <sub>DM</sub>	-16	Α		

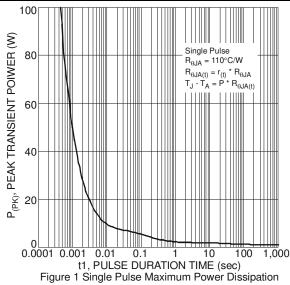
## Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

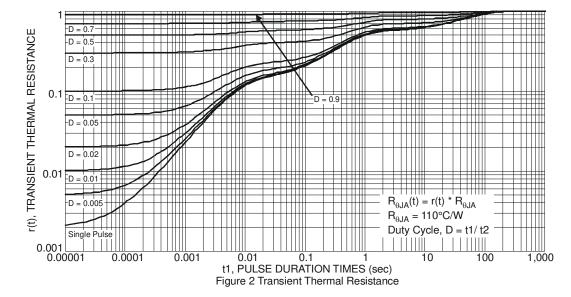
Characteristic	Symbol	Value	Units		
Total Power Dissipation	(Note 5)	В	1.25	W	
Total Fower Dissipation	(Note 6)	P <sub>D</sub>	1.64		
Thermal Desistance Junction to Ambient	(Note 5)	Б	100	,	
Thermal Resistance, Junction to Ambient	(Note 6)	$R_{\thetaJA}$	76	°C/W	
Thermal Resistance, Junction to Case	(Note 6)	$R_{ heta JC}$	14		
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to 150	°C		

 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate. Notes:



# Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)









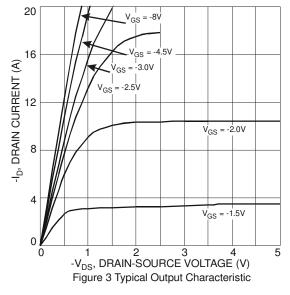
## Electrical Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

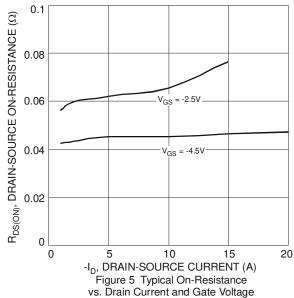
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-20	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$
Zero Gate Voltage Drain Current	I <sub>DSS</sub>		_	-1	μΑ	$V_{DS} = -20V, V_{GS} = 0V$
Gate-Source Leakage	I <sub>GSS</sub>		_	±100	nA	$V_{GS} = \pm 8V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	-0.5	_	-0.9	V	$V_{DS} = V_{GS}$ , $I_D = -250\mu A$
			45	60		$V_{GS} = -4.5V$ , $I_D = -4.2A$
Static Drain-Source On-Resistance	R <sub>DS (ON)</sub>		60	90	mΩ	$V_{GS} = -2.5V$ , $I_D = -3.4A$
			87	113		$V_{GS} = -1.8V, I_D = -2.0A$
Forward Transfer Admittance	Y <sub>fs</sub>		9		S	$V_{DS} = -5V, I_{D} = -4A$
DYNAMIC CHARACTERISTICS (Note 8)						_
Input Capacitance	C <sub>iss</sub>		727			V 00V V 0V
Output Capacitance	Coss		69	1	pF	$V_{DS} = -20V, V_{GS} = 0V$ f = 1.0MHz
Reverse Transfer Capacitance	C <sub>rss</sub>		64	1		1 – 1.000112
Gate Resistance	$R_{G}$		23		Ω	$V_{GS} = 0V, V_{DS} = 0V, f = 1.0MHz$
Total Gate Charge	$Q_g$		7.6	1		
Gate-Source Charge	$Q_{gs}$		1.4		nC	$V_{GS} = -4.5V, V_{DS} = -4V, I_{D} = -3.5A$
Gate-Drain Charge	$Q_{gd}$		1.2	_		
Turn-On Delay Time	t <sub>D(on)</sub>		14.0	_		
Turn-On Rise Time	t <sub>r</sub>		13.0	1	ns	$V_{DS} = -4V$ , $V_{GS} = -4.5V$ ,
Turn-Off Delay Time	t <sub>D(off)</sub>		53.8		IIS	$R_L = 4\Omega$ , $R_G = 6\Omega$ , $I_D = -1A$
Turn-Off Fall Time	t <sub>f</sub>		23.2	_		

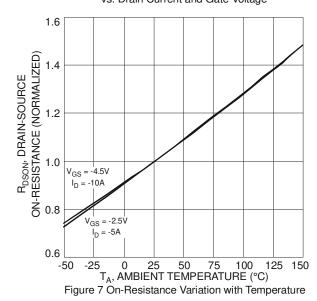
Notes: 7. Short duration pulse test used to minimize self-heating effect.

<sup>8.</sup> Guaranteed by design. Not subject to product testing.









16 V<sub>DS</sub> = -5V

12 T<sub>A</sub> = 150°C

T<sub>A</sub> = 125°C

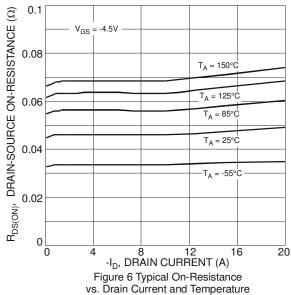
T<sub>A</sub> = 25°C

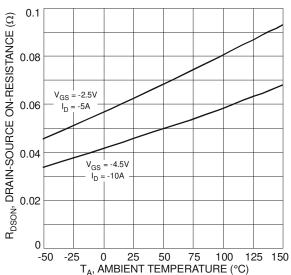
0 0.5 1 1.5 2 2.5 3

-V<sub>GS</sub>, GATE-SOURCE VOLTAGE (V)

Figure 4 Typical Transfer Characteristic

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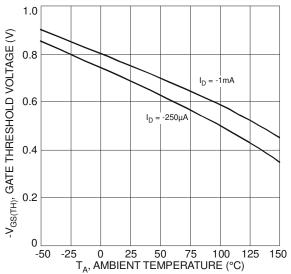
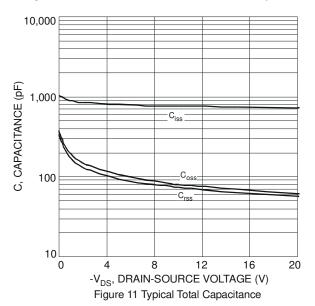
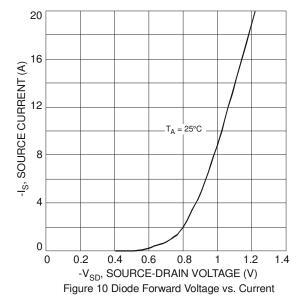


Figure 9 Gate Threshold Variation vs. Ambient Temperature





10,000

T<sub>A</sub> = 150°C

T<sub>A</sub> = 125°C

T<sub>A</sub> = 85°C

T<sub>A</sub> = 85°C

10

0 2 4 6 8 10 12 14 16 18 20

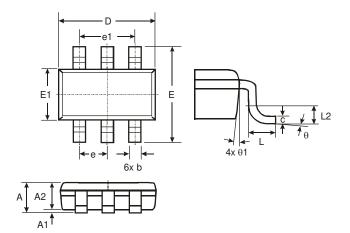
-V<sub>DS</sub>, DRAIN-SOURCE VOLTAGE (V)

Figure 12 Typical Leakage Current vs. Drain-Source Voltage



# **Package Outline Dimensions**

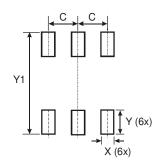
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



TSOT26							
Dim	Min	Max	Тур				
Α	_	1.00	_				
<b>A</b> 1	0.01	0.10	_				
A2	0.84	0.90	-				
D	_	_	2.90				
Е	_	-	2.80				
E1	_	-	1.60				
q	0.30	0.45	_				
С	0.12	0.20	_				
е	-	1	0.95				
e1	_	-	1.90				
L	0.30	0.50					
L2	_	-	0.25				
θ	0°	8°	4°				
θ1	4°	12°	_				
All Dimensions in mm							

# Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
С	0.950
Х	0.700
Υ	1.000
Y1	3.199





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