10V Drive Nch MOS FET

RDN050N20

Structure

Silicon N-channel MOS FET

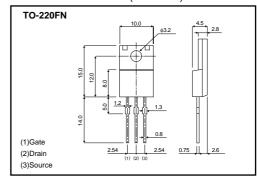
●Features

- 1) Low on-resistance.
- 2) Low input capacitance.
- 3) Exellent resistance to damage from static electricity.

Application

Switching

●External dimensions (Unit : mm)



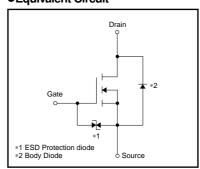
Packaging specifications

	Package	Bulk
Type	Code	_
	Basic ordering unit (pieces)	500
RDN050N20		0

● Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit	
Drain-Source Voltage		V _{DSS}	200	V	
Gate-Source Voltage		V _{GSS}	±30	V	
Drain Current	Continuous	ID	5	A	
Drain Current	Pulsed	IDP *1	20	Α	
Reverse Drain	Continuous	I _{DR}	5	A	
Current	Pulsed	I _{DRP} *1	20	Α	
Source Current (Body Diode)	Continuous	Is	5	Α	
	Pulsed	I _{SP} *1	20	Α	
Avalanche Current		las *2	5	Α	
Avalanche Energy		E _{AS} *2	75	mJ	
Total Power Dissipation (Tc=25°C)		PD	30	W	
Channel Temperature		Tch	150	°C	
Storage Temperature		T _{stg}	-55 to +150	°C	

●Equivalent Circuit



*A protection diode is included between the gate and the source terminals to protect the diode against static electricity when the product is in use. Use the protection circuit when the fixed voltages are exceeded.

Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to case	Rth(ch-c)	4.17	°C/W
Channel to ambient	Rth(ch-a)	62.5	°C/W



^{*1} Pw \leq 10 μ s, Duty cycle \leq 1% *2 L \rightleftharpoons 4.5mH, VDD=50V, RG=25 Ω , 1Pulse, Tch=25°C

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-Source Leakage	Igss	_	_	±10	μΑ	Vgs=±30V, Vds=0V
Drain-Source Breakdown Voltage	V(BR) DSS	200	_	_	V	I _D =250μA, V _G s=0V
Zero Gate Voltage Drain Current	IDSS	_	_	25	μΑ	Vps=200V, Vgs=0V
Gate Threshold Voltage	VGS (th)	2.0	_	4.0	V	VDS=10V, ID=1mA
Static Drain-Source On-State Resistance	RDS (on)	_	0.55	0.72	Ω	Ib=2.5A, Vgs=10V
Forward Transfer Admittance	Yfs *	1.1	1.8	_	S	Vps=10V, Ip=2.5A
Input Capacitance	Ciss	_	292	_	pF	V _{DS} =10V
Output Capacitance	Coss	_	92	_	pF	Vgs=0V
Reverse Transfer Capacitance	Crss	_	28	_	pF	f=1MHz
Turn-On Delay Time	td (on) *	_	10	_	ns	ID=2.5A, VDD ≒100V
Rise Time	tr *	_	22	_	ns	Vgs=10V
Turn-Off Delay Time	td (off) *	_	23	_	ns	RL=40Ω
Fall Time	t _f *	_	28	_	ns	R _G =10Ω
Total Gate Charge	trr *	_	9.3	18.6	nC	VDD=100V
Gate-Source Charge	Qrr *	_	2.8	_	nC	Vgs=10V
Gate-Drain Charge	Q _g *	_	3.7	_	nC	In=5A

^{*} Pulsed

●Body diode characteristics (Source-drain) (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	Vsp *	_	_	2.0	V	I _S = 2.5A, V _{GS} =0V
Reverse recovery time	trr *	_	117	_	ns	I _{DR} = 5A, V _{GS} =0V
Reverse recovery charge	Qrr *	_	0.37	_	μС	di/dt= 100A / μs

^{*} Pulsed

Electrical characteristic curves

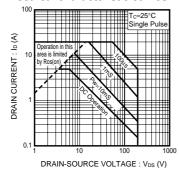


Fig.1 Maximun Safe Operating Area

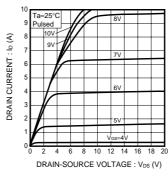


Fig.2 Typical Output Characteristics

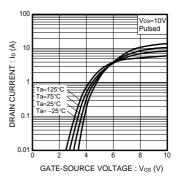


Fig.3 Typical Transfer Characteristics

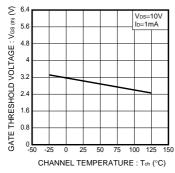


Fig.4 Gate Threshold Voltage vs. Channel Temperature

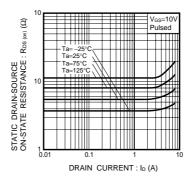


Fig.5 Static Drain-Source On-State Resistance vs. Drain Current

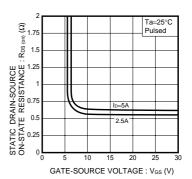


Fig.6 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

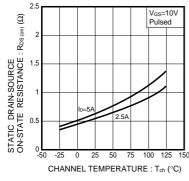


Fig.7 Static Drain-Source On-State Resistance vs. Channel Temperature

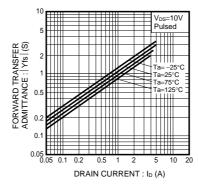


Fig.8 Forward Transfer Admittance vs. Drain Current

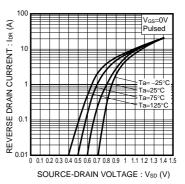


Fig.9 Reverse Drain Current vs. Source-Drain Voltage

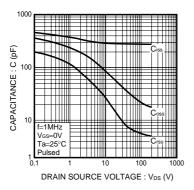


Fig.10 Typical Capacitance vs. Drain-Source Voltage

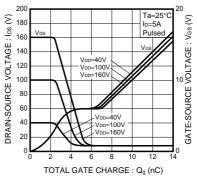


Fig.11 Dynamic Input Characteristics

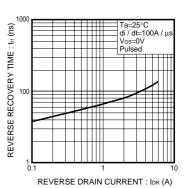


Fig.12 Reverse Recovery Time vs. Reverse Drain Current

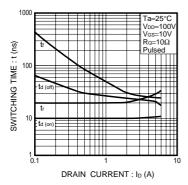


Fig.13 Switching Characteristcs

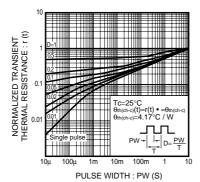


Fig.14 Normalized Transient Thermal Resistance vs. Pulse Width

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