SUP50020EL Vishay Siliconix



N-Channel 60 V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	R _{DS(on)} (Ω) MAX.	I _D (A) ^d	Q _g (TYP.)		
60	0.0023 at V_{GS} = 10 V	120	126 nC		
00	0.0028 at V_{GS} = 4.5 V	120	120110		



Ordering Information:

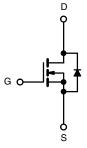
SUP50020EL-GE3 (lead (Pb)-free and halogen-free)

FEATURES

- TrenchFET® power MOSFET
- Maximum 175 °C junction temperature
- Q_{gd}/Q_{gs} ratio < 0.25
- Operable with logic-level gate drive
- 100 % R_g and UIS tested
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

- Power supply
 Secondary synchronous rectification
- DC/DC converter
- Power tools
- Motor drive switch
- DC/AC inverter
- Battery management



RoHS COMPLIANT

HALOGEN

FREE

N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	$(T_C = 25 \ ^{\circ}C, \text{ unless othe})$	rwise noted)			
PARAMETER	SYMBOL	LIMIT	UNIT		
Drain-Source Voltage		V _{DS}	60	v	
Gate-Source Voltage		V _{GS}	± 20		
Continuous Drain Current (T _{.1} = 150 °C)	T _C = 25 °C	1-	120 ^d		
Continuous Drain Current $(1j = 150^{\circ} C)$	T _C = 70 °C	I	120 ^d	А	
Pulsed Drain Current (t = 100 µs)		I _{DM}	300	A	
Avalanche Current	L = 0.1 mH	I _{AS}	75		
Single Avalanche Energy ^a	L = 0.1 IIIH	E _{AS}	281	mJ	
Maximum Davier Dissinction a	T _C = 25 °C	D-	375 ^b	w	
Maximum Power Dissipation ^a	T _C = 125 °C		125 ^b	VV	
Operating Junction and Storage Temperature F	lange	T _J , T _{stg}	-55 to +175	°C	

THERMAL RESISTANCE RATINGS				
PARAMETER	SYMBOL	LIMIT	UNIT	
Junction-to-Ambient (PCB Mount) ^c	R _{thJA}	40	°C/W	
Junction-to-Case (Drain)	R _{thJC}	0.4	0/11	

Notes

- a. Duty cycle \leq 1 %.
- b. See SOA curve for voltage derating.
- c. When mounted on 1" square PCB (FR4 material).
- d. Package limited.

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PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static	•					
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS}=0~V,~I_D=250~\mu A$	60	-	-	V
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$	1.2	-	2.5	V
Gate-Body Leakage	I _{GSS}	$V_{DS}=0~V,~V_{GS}=\pm~20~V$	-	-	± 250	nA
		$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}$	-	-	1	μA
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 125 ^{\circ}\text{C}$	-	-	150	
		$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 175 ^{\circ}\text{C}$	-	-	5	mA
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 10 \text{ V}, V_{GS} = 10 \text{ V}$	120	-	-	А
Ducia Course On Otata Decistance 3	_	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 30 \text{ A}$	-	0.0019	0.0023	Ω
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 20 \text{ A}$	-	0.0023	0.0028	
Forward Transconductance ^a	g fs	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 30 \text{ A}$	-	145	-	S
Dynamic ^b						
Input Capacitance	C _{iss}		-	11 113	-	pF
Output Capacitance	C _{oss}	$V_{GS} = 0 V$, $V_{DS} = 30 V$, f = 1 MHz	-	4625	-	
Reverse Transfer Capacitance	C _{rss}		-	475	-	
Total Gate Charge ^c	Qg		-	126	-	nC
Gate-Source Charge ^c	Q _{gs}	V_{DS} = 30 V, V_{GS} = 10 V, I_{D} = 20 A	-	31.2	-	
Gate-Drain Charge ^c	Q _{gd}		-	7.1	-	
Gate Resistance	Rg	f = 1 MHz	0.32	1.6	3.2	Ω
Turn-On Delay Time ^c	t _{d(on)}		-	15	30	
Rise Time ^c	t _r	$\label{eq:VDD} \begin{array}{l} V_{\text{DD}} = 30 \ \text{V}, \ R_{\text{L}} = 5 \ \Omega \\ I_{\text{D}} \cong 10 \ \text{A}, \ V_{\text{GEN}} = 10 \ \text{V}, \ R_{\text{g}} = 1 \ \Omega \end{array}$	-	20	40	- ns
Turn-Off Delay Time ^c	t _{d(off)}		-	55	100	
Fall Time ^c	t _f		-	11	20	
Drain-Source Body Diode Ratings an	nd Characteris	stics ^b (T _C = 25 °C)	1	1	<u> </u>	
Pulsed Current (t = 100 µs)	I _{SM}		-	-	300	А
Forward Voltage ^a	V _{SD}	$I_F = 10 \text{ A}, V_{GS} = 0 \text{ V}$	-	0.8	1.5	V
Reverse Recovery Time	t _{rr}		-	120	180	ns
Peak Reverse Recovery Charge	I _{RM(REC)}	I _F = 39 A, di/dt = 100 A/μs	-	5	10	А
Reverse Recovery Charge	Q _{rr}		-	0.287	0.430	μC

Notes

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

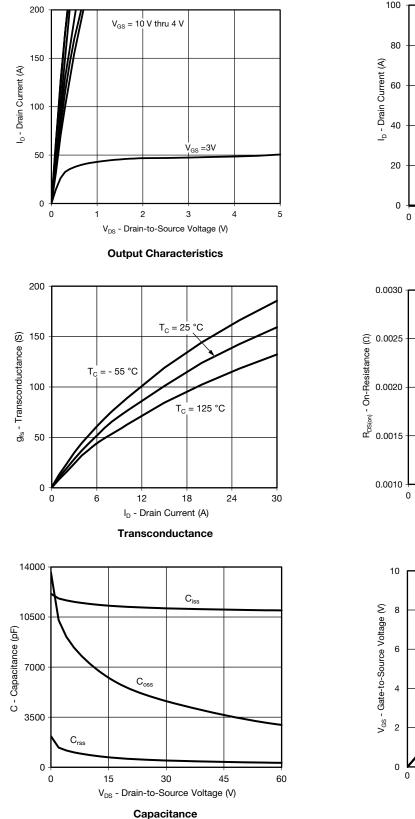
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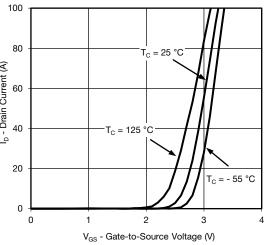


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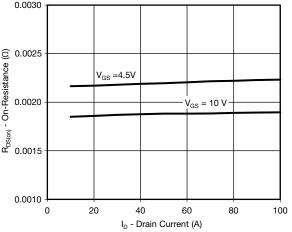
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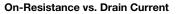
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)

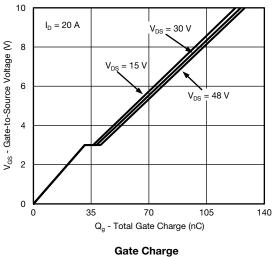




Transfer Characteristics







S15-1868-Rev. A, 10-Aug-15

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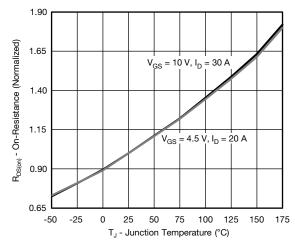
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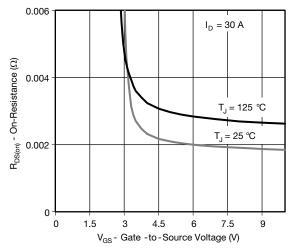


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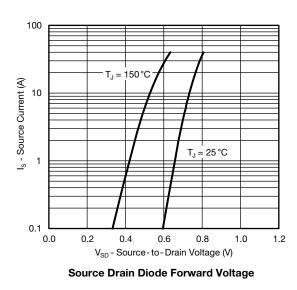
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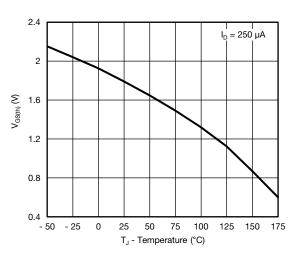




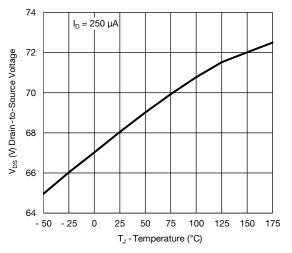


On-Resistance vs. Gate-to-Source Voltage

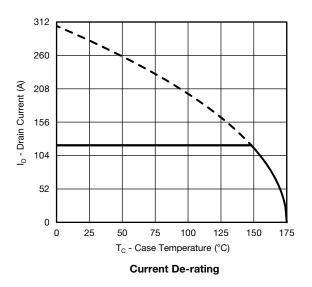




Threshold Voltage



Drain Source Breakdown vs. Junction Temperature



S15-1868-Rev. A, 10-Aug-15

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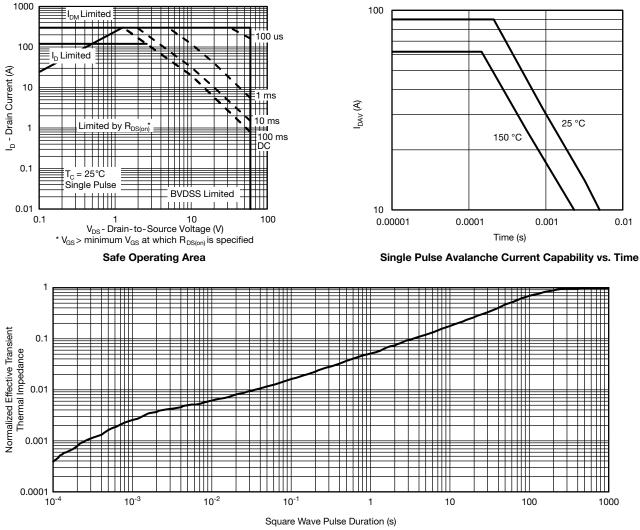
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THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)

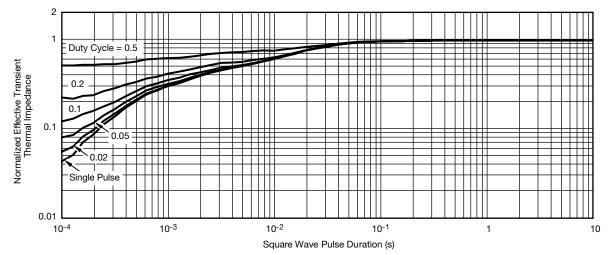


Normalized Thermal Transient Impedance, Junction-to-Ambient



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THERMAL RATINGS ($T_A = 25 \text{ °C}$, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Case

Note

• The characteristics shown in the two graphs

- Normalized Transient Thermal Impedance Junction to Ambient (25 °C)

- Normalized Transient Thermal Impedance Junction to Case (25 °C)

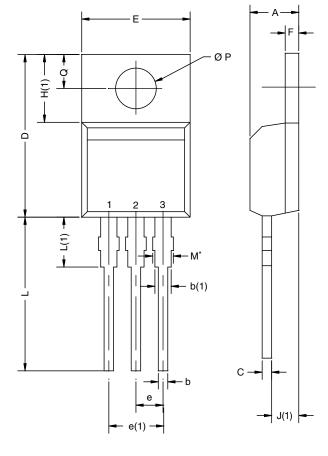
are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?68273.



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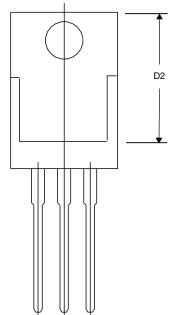
TO-220AB



	MILLIMETERS		INCHES	
DIM.	MIN.	MAX.	MIN.	MAX.
А	4.25	4.65	0.167	0.183
b	0.69	1.01	0.027	0.040
b(1)	1.20	1.73	0.047	0.068
С	0.36	0.61	0.014	0.024
D	14.85	15.49	0.585	0.610
D2	12.19	12.70	0.480	0.500
E	10.04	10.51	0.395	0.414
е	2.41	2.67	0.095	0.105
e(1)	4.88	5.28	0.192	0.208
F	1.14	1.40	0.045	0.055
H(1)	6.09	6.48	0.240	0.255
J(1)	2.41	2.92	0.095	0.115
L	13.35	14.02	0.526	0.552
L(1)	3.32	3.82	0.131	0.150
ØР	3.54	3.94	0.139	0.155
Q	2.60	3.00	0.102	0.118
	0413-Rev. P,		0.102	0.118

Note

 * M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM



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