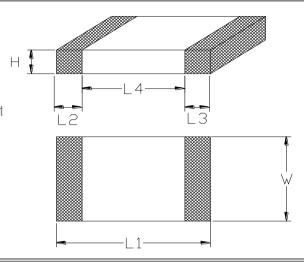


Multilayer Ceramic Chip Capacitor

1812J6300103JGT

Knowles offers Multilayer Ceramic Chip Capacitors of the highest quality and reliability for a range of demanding applications. Manufactured using our "Wet Process", the range encompasses 10V to 12kV capability, with a variety of termination options including FlexiCap™, the worlds first commercially available flexible termination



Mechanical Specification

Size Code Length (L1) mm Width (W) mm Thickness (H) mm Termination Bands (L2,L3)

Termination Bands (L2,L3) mm Minimum Band Gap (L4) mm Termination Material

Solderability RoHS Compliant

Packaging

1812

 4.5 ± 0.3 3.2 ± 0.2

2.5 Max. 0.25 - 1.143

2.15

100% Matte Sn over Ni

IEC 60068-2-58

2011/65/EU as amended by 2015/863/EU

Taped and Reeled, 7 inch Reel

General Electrical Specification

Rated Voltage
Nominal Capacitance Value
Capacitance Tolerance
Tangent of Loss Angle (Tan δ)

Capacitance and Tan δ Test Conditions

Voltage Proof

(Voltage applied for 5 secs max. @ 50mA max. charge current)

Min Insulation Resistance (IR) Dielectric Classification Rated Temperature Range

Maximum Capacitance Change over

Temperature Range Climatic Category (IEC) Ageing Characteristic 630V 10nF ± 5%

≤0.0015 1Vrms @ 1kHz 1.5 x Rated Voltage

100.00GΩ C0G (NP0)

-55°C to +125°C No DC Voltage

Rated DC Voltage 55/125/56

Zero

0±30ppm/°C

This datasheet is for a standard item and is confirmed valid on the date generated, the latest published data for this part may differ and is available at www.knowlescapacitors.com/syfer or by contacting us at syfersales@knowles.com

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Tel: 01603 723300 Email: syfersales@knowles.com Web: www.knowlescapacitors.com/syfer Description:-

1812 630V 10nF ± 5% C0G (NP0)

The information contained on this drawing is confidential and may not be copied in whole or part in any form or disclosed to a third party without the consent of Knowles. and any customer mentioned within this specification. Name: J So

Date: 26 July 2017

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