

Figure 1.1. Top View of AHV12VN6KV1MAW



Figure 1.2. Side View



Figure 1.4. Side View



Figure 1.3. Side View

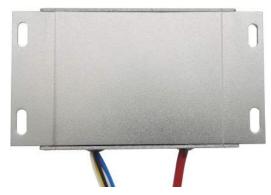


Figure 1.5. Bottom View



## **FEATURES**

• Input Power Voltage: 12V ± 1V

Input Current Range: 150mA to 720mA
Output Voltage: 0 to -6kV@CTRL = 0 to 5V

Max. Output Current: 1mA
Reference Voltage: 5V ± 0.05V
Input Control Voltage: 0 to 5V

• Full Span Modulation on Output Voltage

Electronic Shutdown Control



Figure 2. The Connecting Lead Wires of AHV12VN6KV1MA

# **APPLICATIONS**

This power module, AHV12VN6KV1MAW, is designed for achieving DC-DC conversion from low voltage to high voltage as a power supply source which is widely used in scientific research and other fields including:

- X-ray Machine
- Spectral Analysis
- Nondestructive Inspection
- Semiconductor Manufacturing Equipment
- CRT Monitor Test
- Particle Accelerator
- Capillary Electrophoresis
- Particles Injection
- Semiconductor Technology
- Physical Vapor Phase Deposition
- Radio Frequency Amplification
- Electrospinning Preparation of Nanofiber
- Glass / Fabric Coating
- DC Reactive Magnetron Sputtering
- Cyclotron Accelerator

**Table 1. Pin Names, Colors, Functions and Specifications.** 

No.	Name	Color		Color		Color Type		Min.	Тур.	Max.
1	SDN	Blue		Digital input	Shutdown logic low	0V		0.8V		
1	SDN	blue		Digital input	Shutdown logic high	1.2V		5V		
2	5VR	Yellow		Analog output	Reference voltage		5V			
3	CTRL	White		Analog input	Regulation	0V		5V		
4	VPS	Red		Power input	Input voltage		12V			
5	GND	Black	•	Ground for analog, digital and power signals.	Ground electrode		0V			
6	VOUT	Brown		Power output	Output high voltage	0V		−6kV		

## **DESCRIPTION**

Figure 2 shows the connecting wires of AHV12VN6KV1MAW, of which their detail information given in Table 1. The output voltage can be set to a constant value by connecting the CTRL port to the central tap of a POT (Potentiometer) or modulated by an AC signal ranging from 0V to 5V corresponding to 0V to –6kV proportionally at the output VOUT port as shown in Figure 3 and Figure 4 respectively.

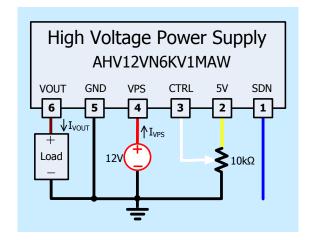


Figure 3. Setting Output to be a Constant Voltage

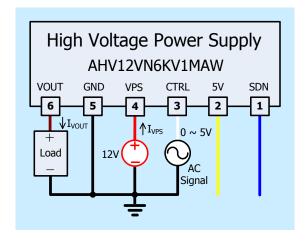


Figure 4. Modulating Output by an AC Signal Source

Please note that the modulation signal must have a low frequency  $\leq$  10Hz and the value range must be  $0V \leq V_{CTRL} \leq 5V$ . The equivalent input circuit for the CTRL is shown in Figure 5.

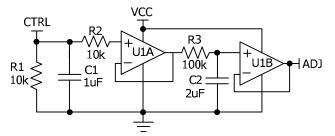


Figure 5. The Equivalent Circuit for CTRL Port

To shutdown AHV12VN6KV1MAW, pull down SDN pin to <0.8V; to turn it on, leave SDN pin unconnected or pull it >1.2V. The maximum voltage allowed on the SDN pin is 5V. The equivalent circuit for SDN port is shown in Figure 6.

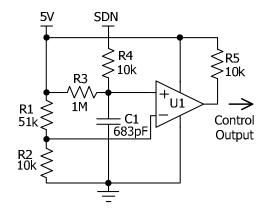


Figure 6. The Equivalent Circuit for SDN Port

# **USING AHV12VN6KV1MAW**

This high voltage power supply must be mounted tightly onto a metal plate, ideally, thus expanding its heating sinking capacity of the metal enclosure. Sufficient ventilation must be provided to keep the power supply surface temperature under 55°C.

# **SAFETY PRECAUTIONS**

Although AHV12VN6KV1MAW high voltage power supply comes with an over current protection circuit, a short circuit at the output should always be avoided. Make sure the high voltage wire for connecting VOUT node has sufficient insulation capability with its surrounding objects.



# **SPECIFICATIONS**

Table 2. Characteristics.  $T_A = 25$ °C, unless otherwise noted.

Parameter		Symbol	Test Conditions	Min.	Тур.	Max.	Unit/Note
Input Power Voltage		V <sub>VPS</sub>		11	12	13	V
Input Power Quiescent Current		Ivps_qc	I <sub>VOUT</sub> = 0mA	150	160	170	mA
Input Power Co	urrent at Full Load	I <sub>VPS_FL</sub>	$I_{VOUT} = 1.0 mA$	710	720	730	mA
•	ver Current at utdown	Ivps_shdn	$T_A = -10^{\circ}C \sim 55^{\circ}C$		18		mA
Power Supply Rejection Ratio		PSRR <sup>(1)</sup>	$V_{VPS} = 11V \sim 13V$ $V_{CTRL} = V_{5VR} = 5V$ $V_{VOUT} = -6kV$ $I_{VOUT} = 1.0mA$		TBD		dB
	Voltage Range cy on CTRL	f <sub>CTRL</sub>		0		12	Hz
		$I_{SDNL}$	$V_{SDNL} < 0.8V$	-5		-4.2	μA
Silutuowii	Shutdown Port Current		$1.2V < V_{SDNL} < 5V$	0		3.8	μA
Shutdown Vo	oltage Logic Low	$V_{SDNL}$		0		0.8	V
Shutdown Vo	ltage Logic High	$V_{SDNH}$		1.2		5	V
Outp	ut Voltage	V <sub>VOUT</sub>	$I_{VOUT} = 0 \sim 1.0 \text{mA}$	0		-6000	V
Output Co	urrent Range	I <sub>VOUTMAX</sub>	$V_{VPS} = 11V \sim 13V$	0		1.0	mA
Reference Voltage Output Range		V <sub>5VR</sub>	$T_A = -10^{\circ}\text{C} \sim 55^{\circ}\text{C}$ $I_{5VR} \leq 5\text{mA}$	4.98	5	5.02	V
Output I	Load Range			6		∞	ΜΩ
Output Vo	oltage Ripple	Vvout_rp	Bandwidth = $1MHz$ $R_{LOAD} = 6 M\Omega$	≤3.0			V <sub>P-P</sub>
Output Voltage	Ripple Frequency	f <sub>VOUT_RP</sub>		TBD			Hz
Output Voltage Temperature Coefficient		TCVvouт (2)	$V_{VPS} = 12V$ $V_{CTRL} = V_{5VR} = 5V$ $V_{VOUT} = -6kV$ $I_{VOUT} = 1mA$ $T_A = -10^{\circ}C \sim 55^{\circ}C$		≤0.01		%/°C
Output Voltage Range v.s. Temperature		Vvout(T)	$V_{VPS} = 12V$ $V_{CTRL} = V_{5VR} = 5V$ $V_{VOUT} = -6kV$ $I_{VOUT} = 1mA$ $T_A = -10^{\circ}C \sim 55^{\circ}C$	0.99Vvоит	Vvout	1.01Vvout	V
Output	Short Term Drift	$\frac{\left \Delta V_{VOUT}/V_{VOUT}\right }{\Delta t \text{ (min)}}$	$V_{VPS} = 12V$ $V_{CTRL} = V_{5VR} = 5V$		≤0.5		%/min
Voltage Drift	Long Term Drift	$\frac{\left \Delta V_{VOUT}/V_{VOUT}\right }{\Delta t (h)}$	$V_{VOUT} = -6kV$ $I_{VOUT} = 1mA$ $T_A = -10^{\circ}C \sim 55^{\circ}C$		≤1		%/h

Outrout Valtage Dise Time		$V_{VOUT}(t_1) = -600V$ $V_{VOUT}(t_2) = -5400V$ No-Load		50		ms
Output Voltage Rise Time	tr	$V_{VOUT}(t_1) = -600V$ $V_{VOUT}(t_2) = -5400V$ $R_{Load} = 6 M\Omega$		TBD		ms
Output Voltage Fall Time	t <sub>f</sub>	$V_{VOUT}(t_2) = -5400V$ $V_{VOUT}(t_3) = -600V$ No-Load		40		ms
Output Voltage Fair Time	Lf	$V_{VOUT}(t_2) = -5400V$ $V_{VOUT}(t_3) = -600V$ $R_{Load} = 6 M\Omega$		TBD		ms
Mean Time Between Failure	MTBF			TBD		h
Instantaneous Short Circuit Current at the Output	Ivout_sc			≤500		mA
Load Regulation	$\frac{\left \Delta V_{\text{VOUT}}/V_{\text{VOUT}}\right }{\Delta I_{\text{VOUT}}}$	$V_{VOUT} = -6kV$ $I_{VOUT} = 1mA$		≤0.05		%/mA
Full Load Efficiency	η <sup>(3)</sup>	$V_{VPS} = 12V$ $V_{VOUT} = -6kV$ $I_{VOUT} = 1mA$		≥70		%
Operating Temperature Range	T <sub>opr</sub>		-10		55	°C
Storage Temperature Range	T <sub>stg</sub>		-20		85	°C
Thermal resistance housing- ambient	Ө <sub>НА</sub> <sup>(4)</sup>	$V_{VPS} = 12V$ $V_{CTRL} = V_{5VR} = 5V$ $V_{VOUT} = -6kV$ $I_{VOUT} = 1mA$		TBD		°C/W
External Dimensions			82×55×28		3	mm
EXTERNAL DIMENSIONS			3.23×2.17×1.10		inch	
				210		g
Weight				0.46		lbs
				7.4		Oz

Note 1: PSRR = 
$$20\log_{10} \frac{\Delta V_{VOUT}/V_{VOUT}}{\Delta V_{VPS}/V_{VPS}}$$
 (dB)

$$\Delta V_{\text{VOUT}} = V_{\text{VOUT}} \; (V_{\text{VPS}} = 12.5 \text{V}) - V_{\text{VOUT}} \; (V_{\text{VPS}} = 11.5 \text{V}), \; V_{\text{VOUT}} \; (V_{\text{VPS}} = 12.5 \text{V}) = V_{\text{VOUT}} \; (V_{\text{VPS}} = 120 \text{V})$$
 
$$\Delta V_{\text{VPS}} = 12.5 \text{V} - 11.5 \text{V}, \; V_{\text{VPS}} = 12 \text{V}$$

Note 2: TCV<sub>VOUT</sub> = 
$$\frac{\left|\Delta V_{VOUT}\right|}{V_{VOUT} \Delta T}$$

Note 3: 
$$\eta = \frac{V_{VOUT}}{V_{VPS}}$$
 IVPS



## **TESTING DATA**

Test conditions:  $V_{VPS} = 12V$ ,  $T_A = 25$ °C,  $R_{LOAD} = 6M\Omega$ 

#### **DC Testing**

The measured output voltage, V<sub>VOUT</sub>, corresponding to the control port input voltage, V<sub>CTRL</sub>, is shown in Figure 7.

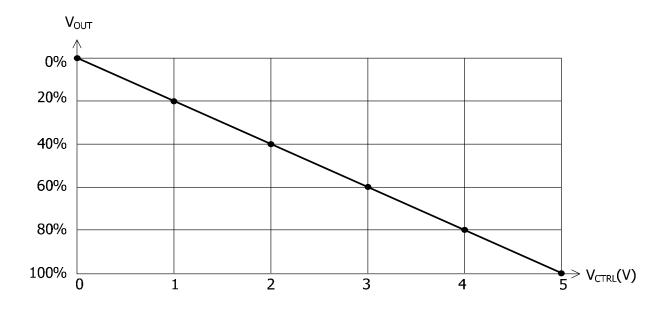


Figure 7. VCTRL vs. VVOUT

#### **AC Testing**

To test the analog modulation function, a triangle and sine-wave voltage signals are applied to the CTRL port as the input source signal respectively. Figure 8 and 9 show both the input signal and the output signal waveforms when using the triangle and sine-wave signals at the CTRL port respectively.

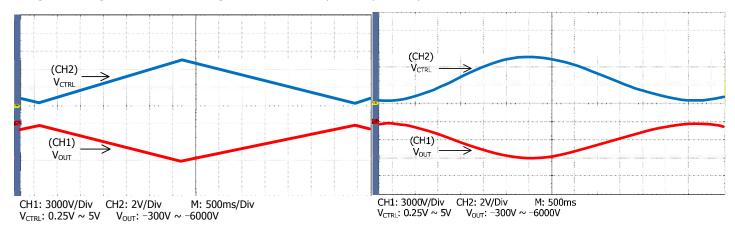
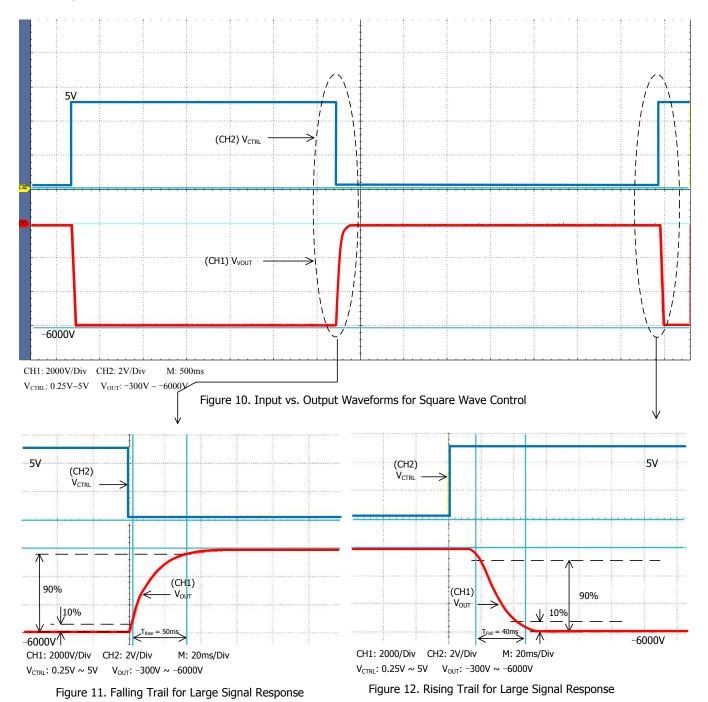


Figure 8. Input vs. Output Waveforms for Triangle Wave Control

Figure 9. Input vs. Output Waveforms for Sine Wave Control



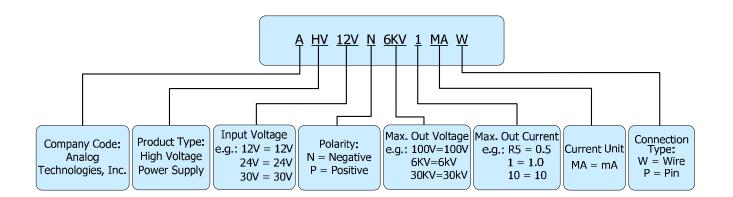
To test the rise and fall times at the output, a step function signal is applied to the CTRL port. The testing results are shown in Figure 10, Figure 11, and Figure 12. As shown in Figure 11 and Figure 12, a square wave of  $0.25V \sim 5V$ , f = 0.10Hz, is applied to CTRL port, the output waveform fall time is measured to be about 100ms and the rise time is about 30ms. These two values are not the same, that is because on the rising trail, the power supply injects a current to the load; while on the falling trail, the best the power supply can do is to stop its output current and let the load resistor drain the output filtering capacitor to a lower voltage, and the draining current is much smaller than the injection current.



1161 Ringwood Ct, #110, San Jose, CA 95131, U. S. A. Tel.: (408) 748-9100, Fax: (408) 770-9187



# **NAMING PRINCIPLE**



Naming Principle of AHV12VN6KV1MAW

## **DIMENSIONS**

### **Connecting Lead Wire Sizes and Lengths**

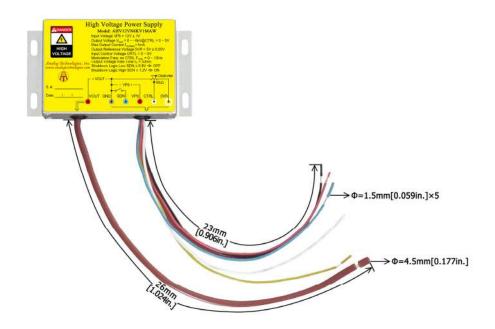


Figure 13. Connecting Lead Wires of AHV12VN6KV1MAW

Lead Wires		neter	Length		
		inch	mm	inch	
Thick brown lead wire	4.5	0.177	26 ± 1	1.024 ± 0.039	
Yellow, red, blue, black and white lead wires	1.5	0.059	23 ± 1	0.906 ± 0.039	



#### **Outline Dimensions**

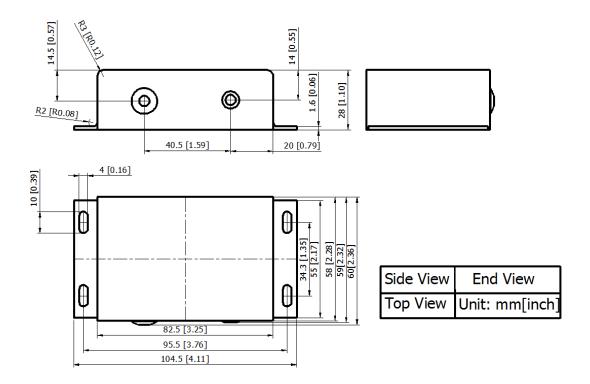


Figure 14. Outline Dimensions

# **ORDERING INFORMATION**

Quantity	1~9pcs	10~49pcs	50~99pcs	≥100pcs
AHV12VN6KV1MAW	\$140	\$130	\$120	\$110

# **High Voltage Power Supply**



**AHV12VN6KV1MA** 

## **NOTICE**

- 1. ATI warrants performance of its products for one year to the specifications applicable at the time of sale, except for those being damaged by excessive abuse. Products found not meeting the specifications within one year from the date of sale can be exchanged free of charge.
- 2. ATI reserves the right to make changes to its products or to discontinue any product or service without notice, and advise customers to obtain the latest version of relevant information to verify, before placing orders, that the information being relied on is current and complete.
- 3. All products are sold subject to the terms and conditions of sale supplied at the time of order acknowledgment, including those pertaining to warranty, patent infringement, and limitation of liability. Testing and other quality control techniques are utilized to the extent ATI deems necessary to support this warranty. Specific testing of all parameters of each device is not necessarily performed, except those mandated by government requirements.
- 4. Customers are responsible for their applications using ATI products. In order to minimize risks associated with the customers' applications, adequate design and operating safeguards must be provided by the customers to minimize inherent or procedural hazards. ATI assumes no liability for applications assistance or customer product design.
- 5. ATI does not warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right of ATI covering or relating to any combination, machine, or process in which such products or services might be or are used. ATI's publication of information regarding any third party's products or services does not constitute ATI's approval, warranty or endorsement thereof.
- 6. IP (Intellectual Property) Ownership: ATI retains the ownership of full rights for special technologies and/or techniques embedded in its products, the designs for mechanics, optics, plus all modifications, improvements, and inventions made by ATI for its products and/or projects.