

PC910L0NSZ0F Series

High Speed 10Mb/s, High CMR DIP 8 pin *OPIC Photocoupler



■ Description

PC910L0NSZ0F Series contains a LED optically coupled to an OPIC chip.

It is packaged in a 8 pin DIP.

Input-output isolation voltage(rms) is 5.0kV, High speed response (TYP. 10 Mb/s) and CMR is MIN. 10 kV/ μs .

■ Features

- 1. DIP 8 pin package
- 2. Double transfer mold package (Ideal for Flow Soldering)
- 3. High speed response

(t_{PHL}: TYP. 48 ns, t_{PLH}: TYP. 50 ns)

- 4. Low input current drive (I_{FHL}: MAX. 5 mA)
- 5. Instantaneous common mode rejection voltage (CM_H: MIN.10 kV/μs, CM_L: MIN. -10 kV/μs)
- 6. TTL and LSTTL compatible output
- 7. High isolation voltage between input and output

 $(V_{iso(rms)}: 5.0 \text{ kV})$

8. Lead-free and RoHS directive compliant

■ Agency approvals/Compliance

- 1. Recognized by UL1577 (Double protection isolation), file No. E64380 (as model No. **PC910L**)
- 2. Approved by VDE, DIN EN60747-5-2^(*) (as an option), file No. 40008898 (as model No. **PC910L**)
- 3. Package resin: UL flammability grade (94V-0)

(*) DIN EN60747-5-2: successor standard of DIN VDE0884

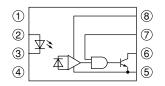
■ Applications

- 1. High speed interfaces for computer peripherals
- 2. Programmable controllers
- 3. Inverter

^{* &}quot;OPIC"(Optical IC) is a trademark of the SHARP Corporation. An OPIC consists of a light-detecting element and a signal-processing circuit integrated onto a single chip.



■ Internal Connection Diagram



- 1 NC ⑤ GND
- 2 Anode ⑥ V_O (Open collector)
- √ V_E (Enable) ③ Cathode 4 NC
 - 8 V_{CC}

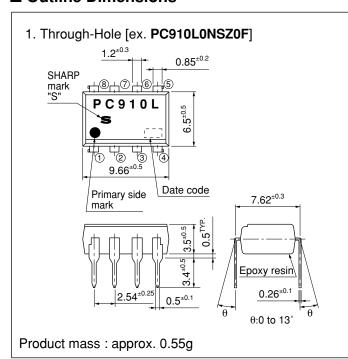
■ Truth table

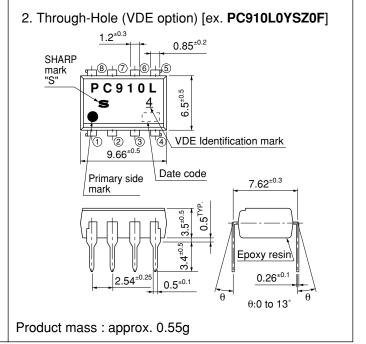
Input	Enable	Output
Н	Н	L
L	Н	Н
Н	L	Н
L	L	Н

L:Logic (0) H:Logic (1)

■ Outline Dimensions

(Unit: mm)





Plating material: SnCu (Cu: TYP. 2%)



Date code (2 digit)

1st digit				2nd digit		
Year of production				Month of production		
A.D.	Mark	A.D	Mark	Month	Mark	
1990	A	2002	P	January	1	
1991	В	2003	R	February	2	
1992	С	2004	S	March	3	
1993	D	2005	T	April	4	
1994	Е	2006	U	May	5	
1995	F	2007	V	June	6	
1996	Н	2008	W	July	7	
1997	J	2009	X	August	8	
1998	K	2010	A	September	9	
1999	L	2011	В	October	0	
2000	M	2012	С	November	N	
2001	N	:	:	December	D	

repeats in a 20 year cycle

Country of origin Japan

Rank mark

There is no rank mark indicator.



Absolute Maximum Ratings

•	I Absolute Maximum Ratings $(T_a=25^{\circ}C)$						
	Parameter	Symbol	Rating	Unit			
<u>=</u>	Forward current	I_F	20	mA			
Input	Reverse voltage	V_R	5	V			
Ξ	Power dissipation	P	40	mW			
	Supply voltage	V_{CC}	7	V			
nt	*1 Enable voltage	V_{E}	5.5	V			
Output	High level output voltage	V _{OH}	7	V			
Ō	Low level output current	I_{OL}	50	mA			
	Collector power dissipation	P _C	85	mW			
	Operating temperature	Topr	-40 to +85	°C			
	Storage temperature	T_{stg}	-55 to +125	°C			
*2	Isolation voltage	V _{iso (rms)}	5.0	kV			
*3	Soldering temperature	T _{sol}	270	°C			

^{*1} Shall not exceed 500mV from supply voltage (V $_{CC}$) *2 40 to 60%RH, AC for 1minute, f=60Hz

■ Electro-optical Characteristics*4

(unles otherwise specified T_a=-40 to 85°C)

= Elect	10-	optical Characteristics	5 7	(unle	es otherwi	se specifi	$ed T_a = -40$	0 to 85°C)
	Parameter			Symbol Conditions		TYP.	MAX.	Unit
	For	rward voltage	V_{F}	$T_a=25$ °C, $I_F=10$ mA		1.6	1.9	V
Input	Reverse current		I_R	$T_a=25^{\circ}C, V_R=5V$	_	_	10	μΑ
	Terminal capacitance		Ct	T _a =25°C, V=0, f=1MHz	_	60	150	pF
	High level output voltage		I _{OH}	$V_{CC}=V_{O}=5.5V, V_{E}=2.0V, I_{F}=250\mu A$	-	0.02	100	μΑ
	Low level output voltage		V_{OL}	V_{CC} =5.5V, V_{E} =2.0V, I_{F} =5mA, I_{OL} =13mA	_	0.4	0.6	V
	Hig	gh level enable current	I_{EH}	V_{CC} =5.5V, V_{E} =2.0V	_	-0.5	-1.6	mA
Output	Lo	w level enable current	I _{EL}	V_{CC} =5.5V, V_{E} =0.5V	_	-0.7	-1.6	mA
	11:	- h 11h	I _{CCH}	V_{CC} =5.5V, V_{E} =2.0V, I_{F} =0	_	5	10	mA
	HI	High level supply current		V_{CC} =5.5V, V_{E} =0.5V, I_{F} =0	_	5	_	mA
	T -			V_{CC} =5.5V, V_{E} =2.0V, I_{F} =10mA	_	7	13	mA
	Low level supply current		I _{CCL}	V_{CC} =5.5V, V_{E} =0.5V, I_{F} =10mA	_	5.5	_	mA
	"High→Low" input threshold current		${ m I}_{ m FHL}$	V_{CC} =5 V , V_{E} =2.0 V , V_{O} =0.8 V , R_{L} =350 Ω	_	2.5	5	mA
	Iso	Isolation resistance		T _a =25°C, DC500V, 40 to 60%RH	5×10 ¹⁰	1011	_	Ω
	Floating capacitance		$C_{\rm f}$	T _a =25°C, V=0, f=1MHz	-	0.6	5	pF
	"High→Low" propagation delay time "Low→High" propagation delay time Rise time Fall time		t _{PHL}		25	48	75	ns
			t _{PLH}	$T_a=25$ °C, $I_F=7.5$ mA,	25	50	75	ns
			t _r	V_{CC} =5V, R_L =350 Ω ,	_	10	_	ns
			t_{f}	$C_L=15pF$,	_	20	_	ns
Transfer	Response time	*5 Distortion of pulse width	Δt_{w}		_	_	35	ns
charac- teristics		"High→Low" enable propagation delay time	t _{EHL}	$T_a=25$ °C, $I_F=7.5$ mA, $V_{CC}=5$ V,	_	15	_	ns
		"Low→High" enable propagation delay time	t _{ELH}	R_{L} =350 Ω , C_{L} =15pF, V_{EH} =3V V_{EL} =0.5V	_	10	-	ns
	Instantaneous common mode rejection voltage (High level output)		CM _H	$\begin{split} T_{a} = & 25^{\circ}C, \ I_{F} = 0, \ V_{CC} = 5V, \\ V_{CM} = & 1kV_{(P-P)}, \\ R_{L} = & 350\Omega, \ V_{O \ (Min)} = 2V \end{split}$	10	20	_	kV/μs
	Instantaneous common mode rejection voltage (Low level output)		CM_L	$T_{a}\text{=}25^{\circ}\text{C}, I_{F}\text{=}5\text{mA}, V_{CC}\text{=}5\text{V}, \\ V_{CM}\text{=}1\text{kV}_{(P\text{-}P)}, \\ R_{L}\text{=}350\Omega, V_{O\text{ (Max)}}\text{=}0.8\text{V}$	-10	-20	_	kV/μs

^{*4} It shall connect a by-pass capacitor of 0.01µF or more between V_{CC} (pin ®) and GND (pin ⑤) near the device, when it measures the transfer characteristics and the output side characteristics

^{*3} For 10s

^{*5} Distortion of pulse width $\Delta t_w \!\!=\! \mid t_{PHL} \!\!-\! t_{PLH} \mid$



■ Model Line-up

Lead Form	Throug	gh-Hole	SMT Gullwing		
Doolroos	Sleeve		Taping		
Package	50pcs/sleeve		1 000pcs/reel		
DIN EN60747-5-2	—— Approved			Approved	
Model No.	PC910L0NSZ0F PC910L0YSZ0F			PC910L0YIP0F	

Please contact a local SHARP sales representative to inquire about production status.



Fig.1 Test Circuit for Propagation Delay Time and Rise Time, Fall Time

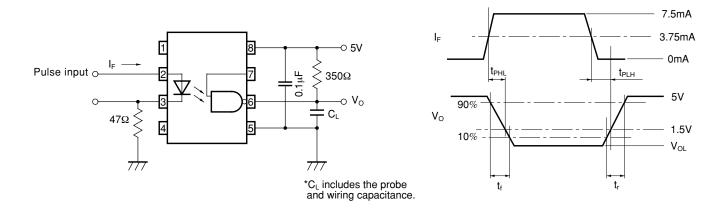


Fig.2 Test Circuit for Enable Propagation Delay Time

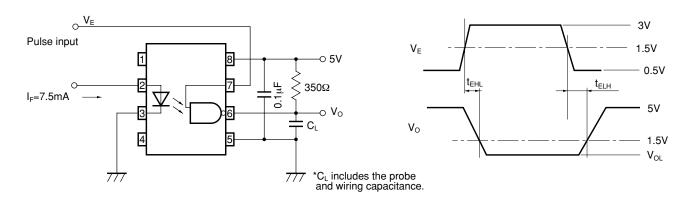
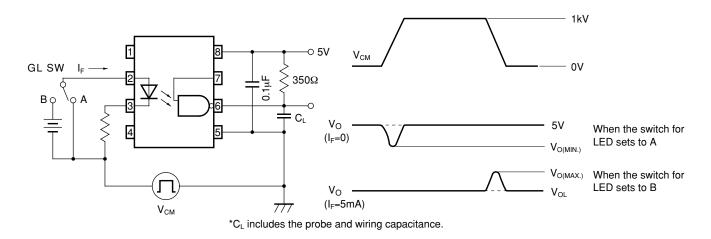


Fig.3 Test Circuit for Instantaneous Common Mode Rejection Voltage



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Fig.4 Forward Current vs. Ambient Temperature

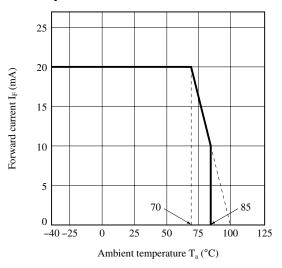


Fig.6 Forward Current vs. Forward Voltage

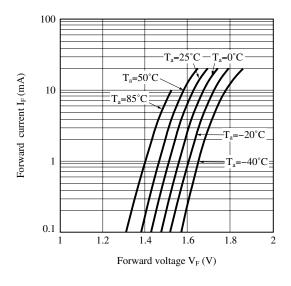


Fig.8 Low Level Output Voltage vs. Ambient Temperature

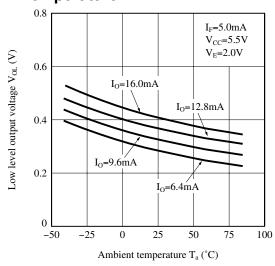


Fig.5 Collector Power Dissipation vs. Ambient Temperature

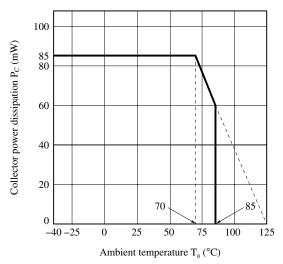


Fig.7 High Level Output Current vs. Ambient Temperature

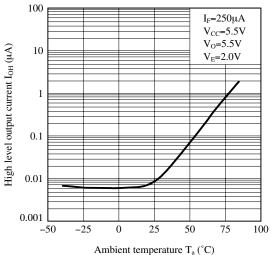


Fig.9 Input Threshold Current vs.
Ambient Temperature

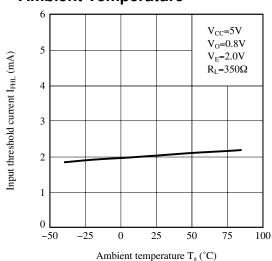




Fig.10 Output Voltage vs. Forward Current

Fig.11 Propagation Delay Time vs. Forward Current

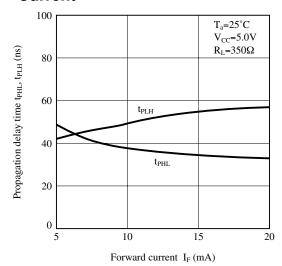
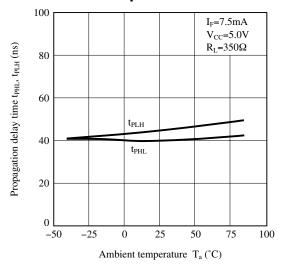


Fig.12 Propagation Delay Time vs.
Ambient Temperature



Remarks: Please be aware that all data in the graph are just for reference and not for guarantee.



■ Design Considerations

Recommended operating conditions

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Low level input current	I_{FL}	0	-	250	μΑ
High level input current	I_{FH}	8	-	15	mA
High level enable input voltage	V_{EH}	2.0	-	V_{CC}	V
Low level enable input voltage	V_{EL}	0	-	0.8	V
Supply voltage	V_{CC}	4.5	_	5.5	V
Fan out (TTL load)	N	-	_	8	_
Operating temperature	T_{opr}	-40	_	70	°C

Notes about static electricity

Transistor of detector side in bipolar configuration may be damaged by static electricity due to its minute design.

When handling these devices, general countermeasure against static electricity should be taken to avoid breakdown of devices or degradation of characteristics.

Design guide

In order to stabilize power supply line, we should certainly recommend to connect a by-pass capacitor of $0.01\mu F$ or more between V_{CC} and GND near the device.

In case that some sudden big noise caused by voltage variation is provided between primary and secondary terminals of photocoupler some current caused by it is floating capacitance may be generated and result in false operation since current may go through LED or current may change.

If the photocoupler may be used under the circumstances where noise will be generated we recommend to use the bypass capacitors at the both ends of LED.

The detector which is used in this device, has parasitic diode between each pins and GND.

There are cases that miss operation or destruction possibly may be occurred if electric potential of any pin becomes below GND level even for instant.

Therefore it shall be recommended to design the circuit that electric potential of any pin does not become below GND level.

This product is not designed against irradiation and incorporates non-coherent LED.

Degradation

In general, the emission of the LED used in photocouplers will degrade over time.

In the case of long term operation, please take the general LED degradation (50% degradation over 5 years) into the design consideration.

Please decide the input current which become 2 times of MAX. I_{FHL}.

[☆] For additional design assistance, please review our corresponding Optoelectronic Application Notes.



■ Manufacturing Guidelines

Soldering Method

Flow Soldering:

Due to SHARP's double transfer mold construction submersion in flow solder bath is allowed under the below listed guidelines.

Flow soldering should be completed below 270°C and within 10s.

Preheating is within the bounds of 100 to 150°C and 30 to 80s.

Please don't solder more than twice.

Hand soldering

Hand soldering should be completed within 3s when the point of solder iron is below 400°C.

Please don't solder more than twice.

Other notices

Please test the soldering method in actual condition and make sure the soldering works fine, since the impact on the junction between the device and PCB varies depending on the tooling and soldering conditions.

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Cleaning instructions

Solvent cleaning:

Solvent temperature should be 45°C or below Immersion time should be 3 minutes or less

Ultrasonic cleaning:

The impact on the device varies depending on the size of the cleaning bath, ultrasonic output, cleaning time, size of PCB and mounting method of the device.

Therefore, please make sure the device withstands the ultrasonic cleaning in actual conditions in advance of mass production.

Recommended solvent materials:

Ethyl alcohol, Methyl alcohol and Isopropyl alcohol

In case the other type of solvent materials are intended to be used, please make sure they work fine in actual using conditions since some materials may erode the packaging resin.

Presence of ODC

This product shall not contain the following materials.

And they are not used in the production process for this product.

Regulation substances: CFCs, Halon, Carbon tetrachloride, 1.1.1-Trichloroethane (Methylchloroform)

Specific brominated flame retardants such as the PBBOs and PBBs are not used in this product at all.

This product shall not contain the following materials banned in the RoHS Directive (2002/95/EC).

•Lead, Mercury, Cadmium, Hexavalent chromium, Polybrominated biphenyls (PBB), Polybrominated diphenyl ethers (PBDE).



■ Package specification

Sleeve package

Package materials

Sleeve: HIPS (with anti-static material)

Stopper: Styrene-Elastomer

Package method

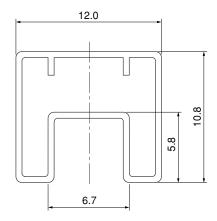
MAX. 50 pcs. of products shall be packaged in a sleeve.

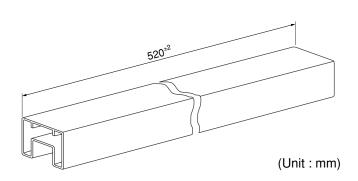
Both ends shall be closed by tabbed and tabless stoppers.

The product shall be arranged in the sleeve with its primary side mark on the tabless stopper side.

MAX. 20 sleeves in one case.

Sleeve outline dimensions







● Tape and Reel package

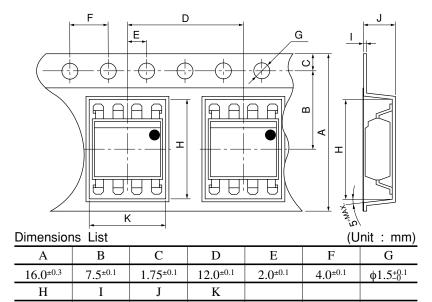
Package materials

Carrier tape: A-PET (with anti-static material)

Cover tape: PET (three layer system)

Reel: PS

Carrier tape structure and Dimensions



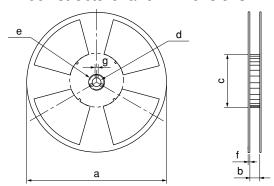
 $10.2^{\pm0.1}$

Reel structure and Dimensions

 $10.4^{\pm0.1}$

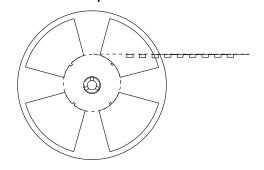
 $0.4^{\pm0.05}$

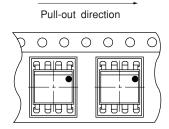
 $4.2^{\pm0.1}$



Dimensio	ns List	(Unit: mm)		
a	b	c	d	
330	17.5 ^{±1.5}	100±1.0	13±0.5	
e	f	g		
23 ^{±1.0}	2.0 ^{±0.5}	2.0 ^{±0.5}		

Direction of product insertion





[Packing: 1 000pcs/reel]



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 - --- Office automation equipment
 - --- Telecommunication equipment [terminal]
 - --- Test and measurement equipment
 - --- Industrial control
 - --- Audio visual equipment
 - --- Consumer electronics
- (ii) Measures such as fail-safe function and redundant design should be taken to ensure reliability and safety when SHARP devices are used for or in connection

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- --- Traffic signals
- --- Gas leakage sensor breakers
- --- Alarm equipment
- --- Various safety devices, etc.
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