



40V N-Channel Enhancement Mode MOSFET

Voltage

40 V

Current

6.5 A

Features

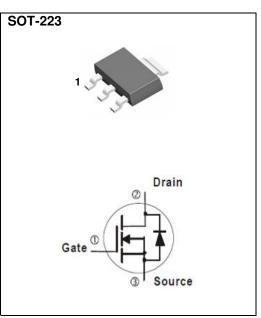
- $R_{DS(ON)}$, V_{GS} @10V, I_D @5A<42m Ω
- $R_{DS(ON)}$, V_{GS} @4.5V, I_{D} @4A<51m Ω
- Advanced Trench Process Technology
- High density cell design for ultra low on-resistance
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

• Case: SOT-223 Package

• Terminals : Solderable per MIL-STD-750, Method 2026

• Approx. Weight: 0.043 ounces, 0.123 grams



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V_{DS}	40	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	
Gate-Source Voltage		V_{GS}	<u>+</u> 20	V	
Continuous Drain Current (Note 4)	T _A =25°C	I _D	6.5		
	T _A =70°C		5	Α	
Pulsed Drain Current (Note 1)		I _{DM}	26		
Power Dissipation	T _A =25°C	· P _D	3.1	W	
	T _A =70°C		2		
Operating Junction and Storage Temperature Range		T_{J} , T_{STG}	-55~150	°C	
Typical Thermal Resistance - Junction to Ambient (Note 4,5)		$R_{ hetaJA}$	40.3	°C/W	

Limited only By Maximum Junction Temperature





Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	40	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=250uA$	1	1.5	2.5	
Drain-Source On-State Resistance	R _{DS(on)}	V_{GS} =10V, I_D =5A	-	35	42	mΩ
		V_{GS} =4.5V, I_D =4A	-	44	51	
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} =40V, V_{GS} =0V	-	-	1	uA
Gate-Source Leakage Current	I _{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	<u>+</u> 100	nA
Dynamic (Note 6)						
Total Gate Charge	Qg	V _{DS} =20V, I _D =4.3A, V _{GS} =4.5V (Note 1,2)	-	4.8	-	nC
Gate-Source Charge	Q_{gs}		-	1.4	-	
Gate-Drain Charge	Q_{gd}		-	1.8	-	
Input Capacitance	Ciss	V _{DS} =20V, V _{GS} =0V,	-	410	-	pF
Output Capacitance	Coss		-	50	-	
Reverse Transfer Capacitance	Crss	f=1MHZ	-	30	-	
Turn-On Delay Time	td _(on)	$V_{DD}{=}20V, I_{D}{=}3.5A, \ V_{GS}{=}10V, \ R_{G}{=}1\Omega^{(Note~1,2)}$	-	4	-	ns
Turn-On Rise Time	t _r		-	30	-	
Turn-Off Delay Time	td _(off)		-	15	-	
Turn-Off Fall Time	t _f		-	8	-	
Drain-Source Diode						
Maximum Continuous Drain-Source					6.5	
Diode Forward Current	I _S		-	-	6.5	Α
Diode Forward Voltage	V_{SD}	I _S =1A, V _{GS} =0V	-	0.78	1.2	V

NOTES:

- 1. Pulse width <300us, Duty cycle <2%.
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}=150$ °C. Ratings are based on low frequency and duty cycles to keep initial $T_J=25$ °C.
- 4. The maximum current rating is package limited.
- 5. R_{BJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
- 6. Guaranteed by design, not subject to production testing.





TYPICAL CHARACTERISTIC CURVES

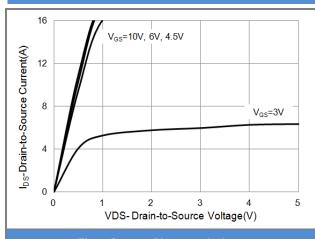


Fig.1 Output Characteristics

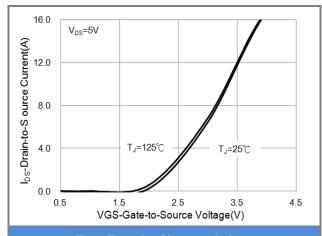


Fig.2 Transfer Characteristics

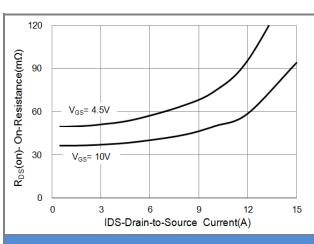


Fig.3 On-Resistance vs. Drain Current

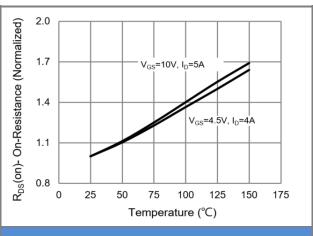
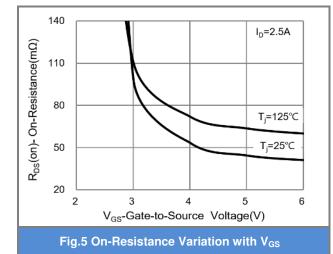
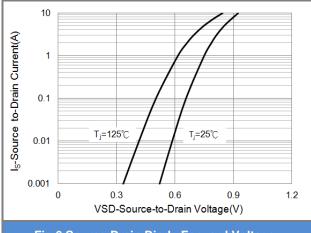


Fig.4 On-Resistance vs. Junction temperature











TYPICAL CHARACTERISTIC CURVES

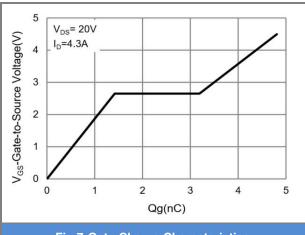


Fig.7 Gate-Charge Characteristics

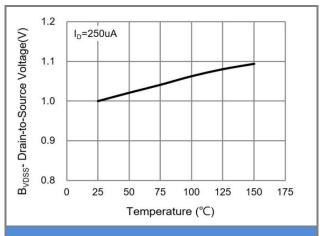


Fig.8 Breakdown Voltage Variation vs. Temperature

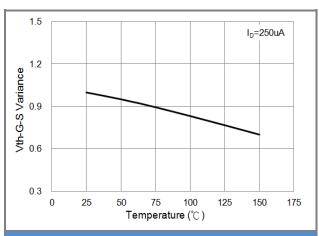


Fig.9 Threshold Voltage Variation with Temperature

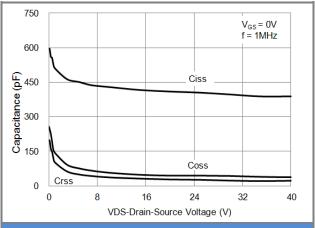


Fig.10 Capacitance vs. Drain-Source Voltage

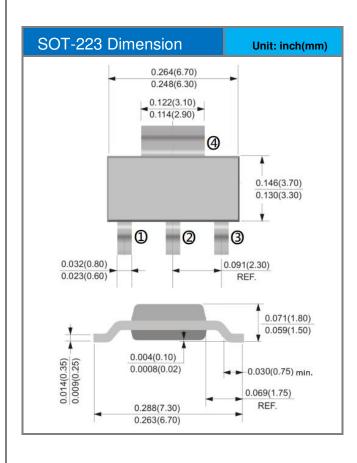


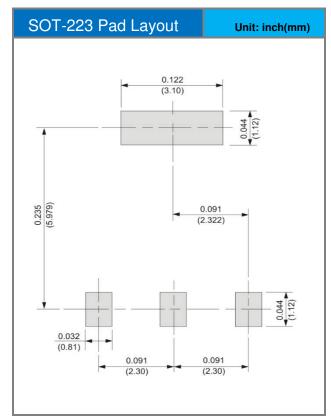


Part No Packing Code Version

Part No Packing Code	Package Type	Packing Type	Marking	Version	
PJW7N04_R2_00001	SOT-223	2,500pcs / 13" reel	W7N04	Halogen free	

Packaging Information & Mounting Pad Layout









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