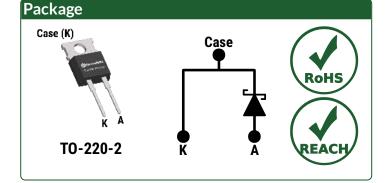
Silicon Carbide Schottky Diode



V <sub>RRM</sub> =	1200 V
$I_{F(T_{c} = 154^{\circ}C)} =$	20 A
Q <sub>C</sub> =	107 nC

### Features

- Low V<sub>F</sub> for High Temperature Operation
- Enhanced Surge and Avalanche Robustness
- Superior Figure of Merit Q<sub>C</sub>/I<sub>F</sub>
- Low Thermal Resistance
- Low Reverse Leakage Current
- Temperature Independent Fast Switching
- Positive Temperature Coefficient of V<sub>F</sub>
- High dV/dt Ruggedness



### Advantages

- Improved System Efficiency
- High System Reliability
- Optimal Price Performance
- Reduced Cooling Requirements
- Increased System Power Density
- Zero Reverse Recovery Current
- Easy to Parallel without Thermal Runaway
- Enables Extremely Fast Switching

### Applications

- Power Factor Correction (PFC)
- Electric Vehicles and Battery Chargers
- Solar Inverters
- High Frequency Converters
- Switched Mode Power Supply (SMPS)
- Motor Drives
- Anti-Parallel / Free-Wheeling Diode
- Induction Heating & Welding

### Absolute Maximum Ratings (At T<sub>c</sub> = 25°C Unless Otherwise Stated)

Parameter	Symbol	Conditions	Values	Unit	Note
Repetitive Peak Reverse Voltage	V <sub>RRM</sub>		1200	V	
		T <sub>C</sub> = 100°C, D = 1	43		
Continuous Forward Current	IF	I <sub>F</sub> T <sub>C</sub> = 135°C, D = 1		А	Fig. 4
		T <sub>C</sub> = 154°C, D = 1	20		
Non-Repetitive Peak Forward Surge Current, Half Sine	Isou	T <sub>C</sub> = 25°C, t <sub>P</sub> = 10 ms	200	٨	
Wave	IF,SM	T <sub>C</sub> = 150°C, t <sub>P</sub> = 10 ms	160	A	
Repetitive Peak Forward Surge Current, Half Sine Wave	lenu	T <sub>C</sub> = 25°C, t <sub>P</sub> = 10 ms	120	А	
Repetitive Feak Forward Surge Guitent, than Sine Wave	IF,RM	T <sub>C</sub> = 150°C, t <sub>P</sub> = 10 ms	84	A	
Non-Repetitive Peak Forward Surge Current	I <sub>F,MAX</sub>	T <sub>C</sub> = 25°C, t <sub>P</sub> = 10 μs	1000	А	
i <sup>2</sup> t Value	∫i²dt	$T_{C}$ = 25°C, $t_{P}$ = 10 ms	200	A <sup>2</sup> s	
Non-Repetitive Avalanche Energy	E <sub>AS</sub>	L = 1.8 mH, I <sub>AS</sub> = 20 A	360	mJ	
Diode Ruggedness	dV/dt	V <sub>R</sub> = 0 ~ 960 V	200	V/ns	
Power Dissipation	Ртот	T <sub>C</sub> = 25°C	279	W	Fig. 3
Operating and Storage Temperature	Tj, T <sub>stg</sub>		-55 to 175	°C	



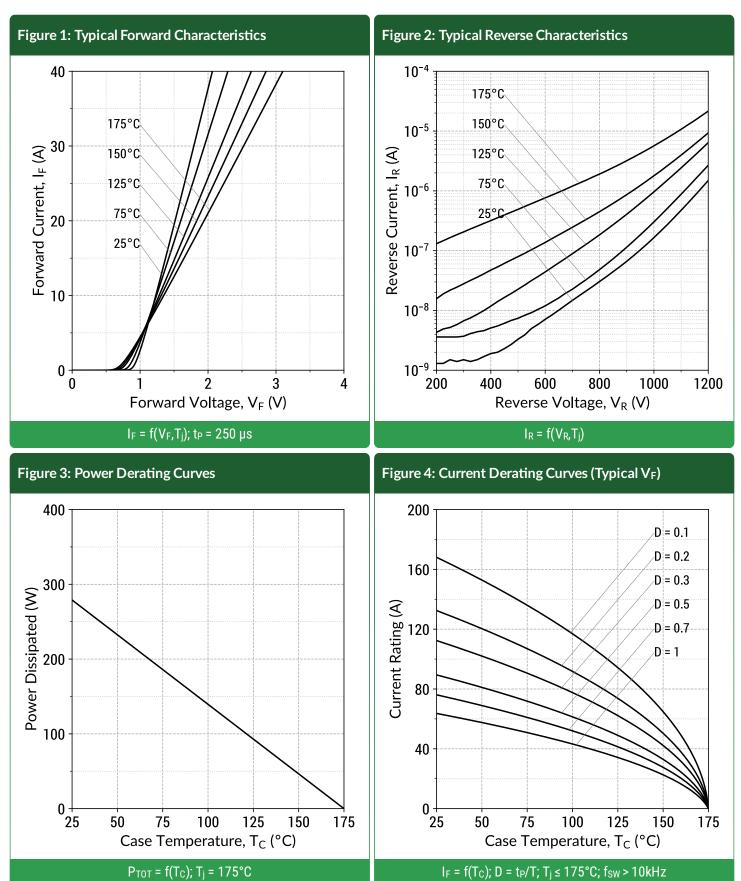
## **Electrical Characteristics**

Parameter	Symbol	Conditions		Values			11	Note
Falallelel	Symbol			Min.	Тур.	Max.	Unit	Note
Diode Forward Voltage	VF	I <sub>F</sub> = 20 A, T <sub>j</sub> = 25°C			1.5	1.8	V	Fig. 1
	VF	I <sub>F</sub> = 20 A, T <sub>j</sub> = 175°C			1.9			
Reverse Current	I_	V <sub>R</sub> = 1200 V, T <sub>j</sub> = 25°C			2	10		Fig. 2
	IR	V <sub>R</sub> = 1200 V, T <sub>j</sub> = 175°C			22		μA	
Total Capacitive Charge	0	V <sub>R</sub> = 400 V			74		-0	<b>Fig. 7</b>
	Qc	I <sub>F</sub> ≤ I <sub>F,MAX</sub>	V <sub>R</sub> = 800 V		107		nC	Fig. 7
Switching Time	+	dl <sub>F</sub> /dt = 200 A/µs	V <sub>R</sub> = 400 V		. 10			
	ts		V <sub>R</sub> = 800 V		< 10		ns	
Total Capacitance	0	V <sub>R</sub> = 1 V, f = 1MHz			1218		<b>"Г</b>	Fig. 6
	С	V <sub>R</sub> = 800 V, f = 1MHz			71		pF	

## Thermal/Package Characteristics

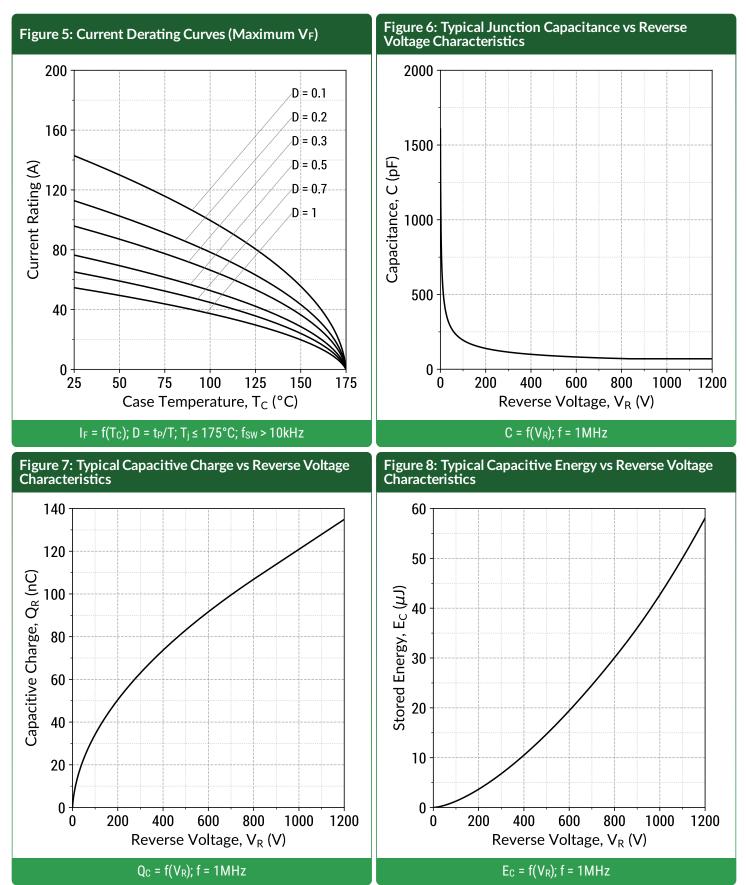
Devementer	Symbol	Conditions	Values			11	Note
Parameter			Min.	Тур.	Max.	Unit	Note
Thermal Resistance, Junction - Case	RthJC			0.54		°C/W	Fig. 9
Weight	WT			2.0		g	
Mounting Torque	T <sub>M</sub>	Screws to Heatsink			1.0	Nm	





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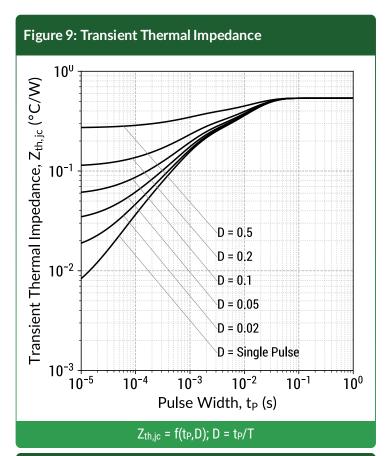
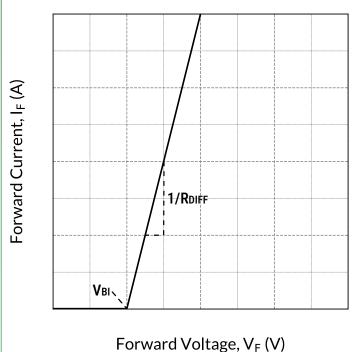


Figure 10: Forward Curve Model



waru voltage, v<sub>F</sub>

 $I_F = f(V_F, T_j)$ 

# GeneSic SEMICONDUCTOR

 $I_F = (V_F - V_{BI})/R_{DIFF} (A)$ 

Built-In Voltage (V<sub>BI</sub>):

 $V_{BI}(T_j) = m \times T_j + n (V)$ m = -0.00123 (V/°C) n = 0.995 (V)

Differential Resistance (RDIFF):

 $R_{DIFF}(T_j) = a \times T_j^2 + b \times T_j + c (\Omega)$ a = 5.96e-07 (\Omega/°C<sup>2</sup>) b = 8.46e-05 (\Omega/°C) c = 0.0251 (\Omega)

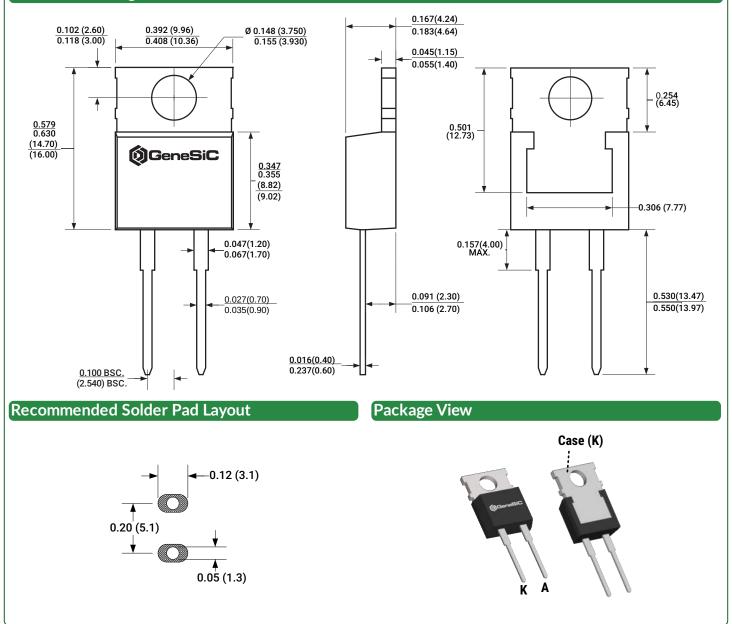
Forward Power Loss Equation:

 $P_{LOSS} = V_{BI}(T_j) \times I_{AVG} + R_{DIFF}(T_j) \times I_{RMS}^2$ 



### Package Dimensions

### TO-220-2 Package Outline



#### NOTE

- 1. CONTROLLED DIMENSION IS INCH. DIMENSION IN BRACKET IS MILLIMETER.
- 2. DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH, MATERIAL PROTRUSIONS.



### Compliance

#### **RoHS Compliance**

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS 2), as adopted by EU member states on January 2, 2013 and amended on March 31, 2015 by EU Directive 2015/863. RoHS Declarations for this product can be obtained from your GeneSiC representative.

#### **REACH Compliance**

REACH substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact a GeneSiC representative to insure you get the most up-to-date REACH SVHC Declaration. REACH banned substance information (REACH Article 67) is also available upon request.

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### **Related Links**

SPICE Models:	https://www.genesicsemi.com/sic-schottky-mps/GC20MPS12-220/GC20MPS12-220_SPICE.zip
PLECS Models:	https://www.genesicsemi.com/sic-schottky-mps/GC20MPS12-220/GC20MPS12-220_PLECS.zip
CAD Models:	https://www.genesicsemi.com/sic-schottky-mps/GC20MPS12-220/GC20MPS12-220_3D.zip
• Evaluation Boards	: https://www.genesicsemi.com/technical-support
• Reliability:	https://www.genesicsemi.com/reliability
Compliance:	https://www.genesicsemi.com/compliance
• Quality Manual:	https://www.genesicsemi.com/quality

#### **Revision History**

- Rev 21/Jul: Updated with most recent test data
- Supersedes: Rev 19/Apr, Rev 20/Apr, Rev 20/Aug



## www.genesicsemi.com/sic-schottky-mps/



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