

# 10A, 45V - 60V Trench Schottky Surface Mount Rectifier

#### **FEATURES**

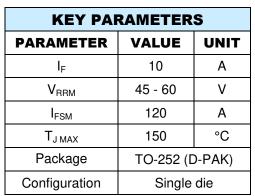
- Patented Trench Schottky technology
- Low power loss / high efficiency
- Ideal for automated placement
- Guard ring for over-voltage protection
- High forward surge capability
- Moisture sensitivity level: level 1, per J-STD-020
- RoHS Compliant
- Halogen-free according to IEC 61249-2-21

### **APPLICATIONS**

- Switching mode power supply (SMPS)
- Adapters
- DC to DC converter

### **MECHANICAL DATA**

- Case: TO-252 (D-PAK)
- Molding compound meets UL 94V-0 flammability rating
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 1A whisker test
- Polarity: As marked
- Weight: 0.400g (approximately)







TO-252 (D-PAK)



PARAMETER	SYMBOL	TSSD10L45SW	TSSD10L60SW	UNIT
Marking code on the device		10L45SW	10L60SW	
Repetitive peak reverse voltage	$V_{RRM}$	45	60	V
Reverse voltage, total rms value	$V_{R(RMS)}$	31	42	V
Forward current	I <sub>F</sub>	10		Α
Peak forward surge current, 8.3ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	120		Α
Junction temperature	$T_J$	- 55 to +150		°C
Storage temperature	T <sub>STG</sub>	- 55 to +150		°C

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THERMAL PERFORMANCE			
PARAMETER	SYMBOL	TYP	UNIT
Junction-to-lead thermal resistance	R <sub>OJL</sub>	16	°C/W
Junction-to-ambient thermal resistance	$R_{\Theta JA}$	51	°C/W
Junction-to-case thermal resistance	R <sub>eJC</sub>	19	°C/W

Thermal Performance Note: Units mounted on PCB (16mm x 16mm Cu pad test board)

ELECTRICAL SPECIFICATIONS (T <sub>A</sub> = 25°C unless otherwise noted)						
PARAMETER		CONDITIONS	SYMBOL	TYP	MAX	UNIT
	TSSD10L45SW	I <sub>F</sub> = 5A, T <sub>J</sub> = 25°C	V <sub>F</sub>	0.43	-	V
		$I_F = 10A, T_J = 25^{\circ}C$		0.49	0.55	V
Forward voltage <sup>(1)</sup>		I <sub>F</sub> = 5A, T <sub>J</sub> = 125°C		0.33	-	V
		I <sub>F</sub> = 10A, T <sub>J</sub> = 125°C		0.41	0.50	V
	TSSD10L60SW	I <sub>F</sub> = 5A, T <sub>J</sub> = 25°C		0.47	-	V
		I <sub>F</sub> = 10A, T <sub>J</sub> = 25°C		0.55	0.66	V
		I <sub>F</sub> = 5A, T <sub>J</sub> = 125°C		0.36	-	V
		I <sub>F</sub> = 10A, T <sub>J</sub> = 125°C		0.47	0.64	V
Reverse current @ rated V <sub>R</sub> <sup>(2)</sup>		T <sub>J</sub> = 25°C	I <sub>R</sub>	-	50	μΑ
		T <sub>J</sub> = 125°C		-	20	mA
Junction capacitance	TSSD10L45SW	41411	CJ	1290	-	pF
	TSSD10L60SW	$1MHz, V_R = 4.0V$		1585	-	рF

### Notes:

- 1. Pulse test with PW = 0.3ms
- 2. Pulse test with PW = 30ms

ORDERING INFORMATION				
ORDERING CODE <sup>(1)</sup>	PACKAGE	PACKING		
TSSD10LxSW	TO-252 (D-PAK)	2,500 / Tape & Reel		

## Notes:

1. "x" defines voltage from 45V(TSSD10L45SW) to 60V(TSSD10L60SW)



## **CHARACTERISTICS CURVES**

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$ 

Fig.1 Forward Current Derating Curve

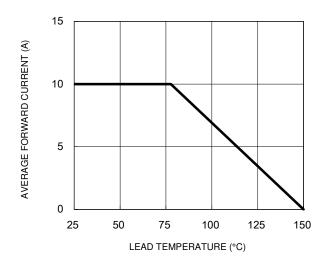


Fig.3 Typical Reverse Characteristics

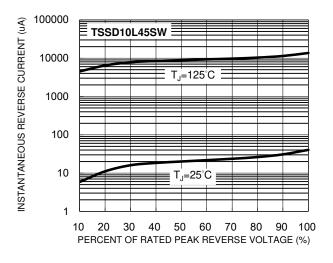


Fig.5 Typical Reverse Characteristics

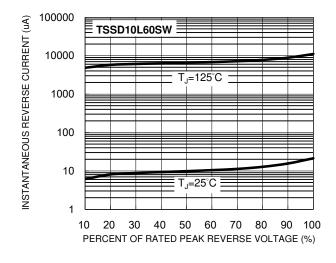


Fig.2 Typical Junction Capacitance

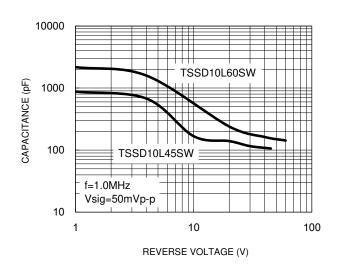


Fig.4 Typical Forward Characteristics

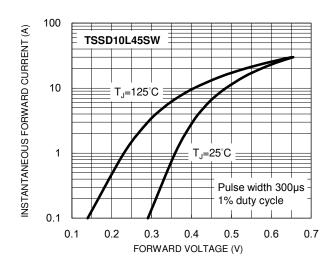
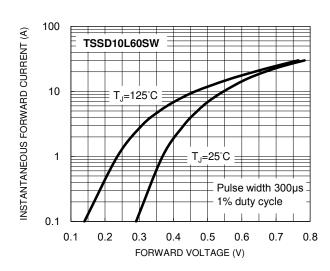


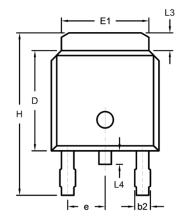
Fig.6 Typical Forward Characteristics

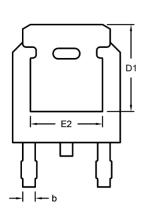


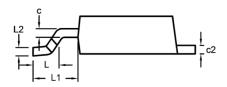
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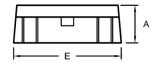
## **PACKAGE OUTLINE DIMENSIONS**

TO-252 (D-PAK)



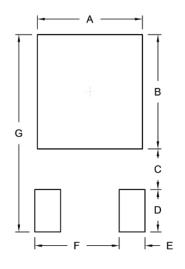






DIM	Unit (mm)		Unit (	(inch)
DIN	Min	Max	Min	Max
Α	2.20	2.38	0.087	0.094
b	0.64	0.88	0.025	0.035
b2	0.77	1.14	0.030	0.045
С	0.45	0.60	0.018	0.024
c2	0.45	0.58	0.018	0.023
D	6.00	6.22	0.236	0.245
D1	5.30	-	0.209	-
E	6.41	6.73	0.252	0.265
E1	5.21	5.47	0.205	0.215
E2	4.40	-	0.173	-
е	2.286	2.286 (REF)		90
Н	9.40	10.40	0.370	0.409
L	1.40	1.77	0.055	0.070
L1	2.743 (REF)		0.1	07
L2	0.508	0.508 (REF)		)20
L3	0.89	1.27	0.035	0.050
L4	0.64	1.01	0.025	0.040

# **SUGGESTED PAD LAYOUT**



Symbol	Unit (mm)	Unit (inch)
Α	5.69	0.224
В	6.18	0.243
С	2.20	0.087
D	2.29	0.090
E	1.40	0.055
F	4.57	0.180
G	10.67	0.420

## **MARKING DIAGRAM**



P/N = Marking Code YWW = Date Code F = Factory Code



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