# Old Company Name in Catalogs and Other Documents

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HAT2043R

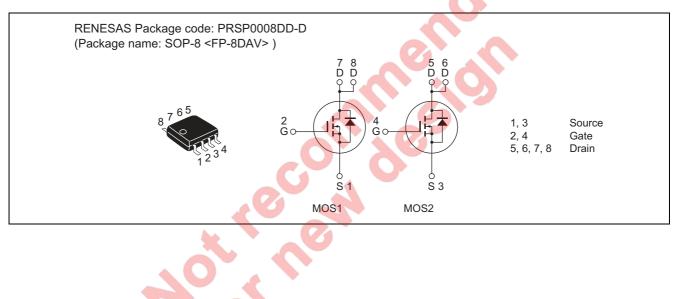
Silicon N Channel Power MOS FET High Speed Power Switching

> REJ03G1169-0600 (Previous: ADE-208-668D) Rev.6.00 Sep 07, 2005

## Features

- Low on-resistance
- Capable of 4 V gate drive
- Low drive current
- High density mounting

### Outline





## **Absolute Maximum Ratings**

			$(Ta = 25^{\circ}C)$
Item	Symbol	Value	Unit
Drain to source voltage	V <sub>DSS</sub>	30	V
Gate to source voltage	V <sub>GSS</sub>	±20	V
Drain current	ID	8	А
Drain peak current	Note 1 D (pulse)	64	А
Body-drain diode reverse drain current	I <sub>DR</sub>	8	А
Channel dissipation	Pch Note 2	2.0	W
Channel dissipation	Pch Note 3	3.0	W
Channel temperature	Tch	150	٥°
Storage temperature	Tstg	-55 to +150	°C

Notes: 1.  $PW \le 10 \propto s$ , duty cycle  $\le 1\%$ 

2. 1 Drive operation: When using the glass epoxy board (FR4 40  $\cdot~$  40  $\cdot~$  1.6 mm), PW  $\leq$  10 s

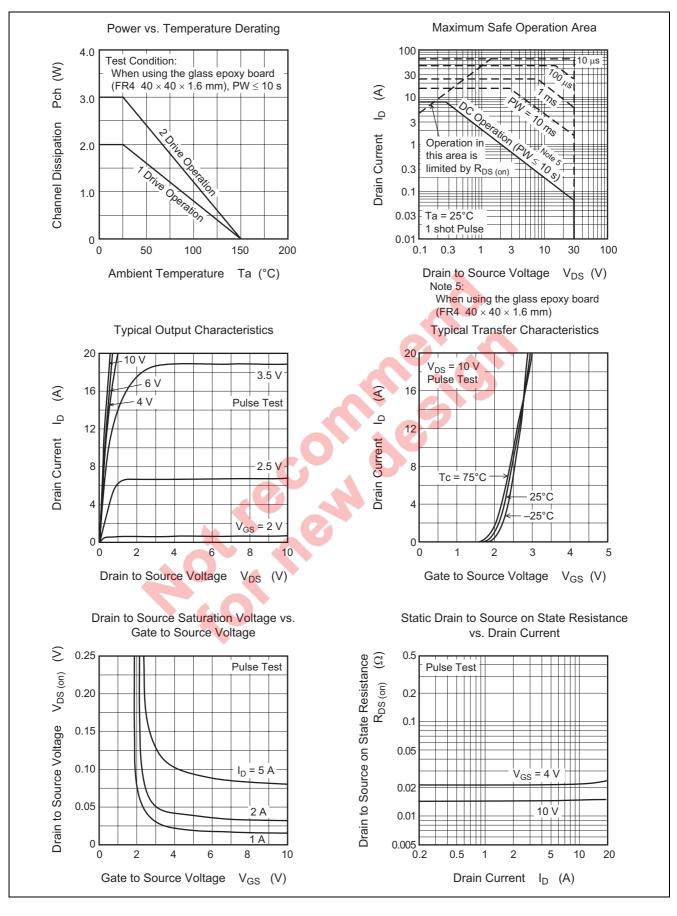
3. 2 Drive operation: When using the glass epoxy board (FR4 40  $\cdot~$  40  $\cdot~$  1.6 mm), PW  $\leq$  10 s

### **Electrical Characteristics**

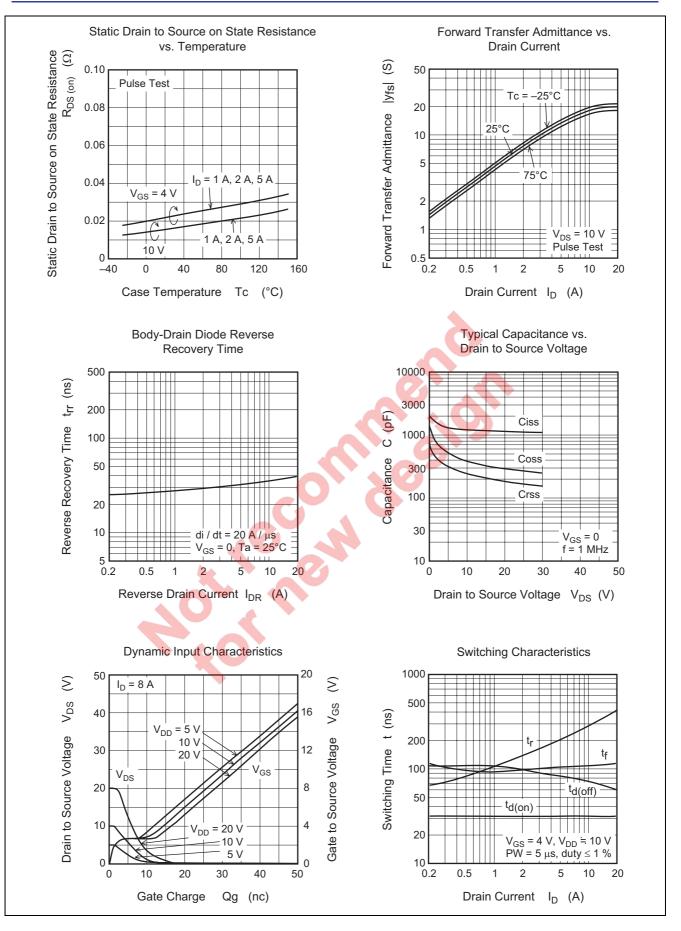
						$(Ta = 25^{\circ}C)$
ltem	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V (BR) DSS	30	_	5	V	$I_{D} = 10 \text{ mA}, V_{GS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_		±0.1	∝A	$V_{GS} = \pm 20 \text{ V}, \text{ V}_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	_		1	∝A	$V_{DS} = 30 V, V_{GS} = 0$
Gate to source cutoff voltage	V <sub>GS (off)</sub>	1.0	_	2.5	V	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$
Static drain to source on state resistance	R <sub>DS (on)</sub>		0.016	0.022	Ω	$I_D = 4 \text{ A}, V_{GS} = 10 \text{ V}^{Note 4}$
	R <sub>DS (on)</sub>	-	0.022	0.029	Ω	$I_D = 4 A, V_{GS} = 4 V^{Note 4}$
Forward transfer admittance	y <sub>fs</sub>	9	14		S	$I_D = 4 \text{ A}, V_{DS} = 10 \text{ V}^{Note 4}$
Input capacitance	Ciss	-	1170		pF	V <sub>DS</sub> = 10 V
Output capacitance	Coss		390		pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss		240		pF	f = 1 MHz
Total gate charge	Qg	5)—	32		nC	V <sub>DD</sub> = 10 V
Gate to source charge	Qgs	_	22		nC	V <sub>GS</sub> = 10 V
Gate to drain charge	Qgd	_	10		nC	I <sub>D</sub> = 8 A
Turn-on delay time	t <sub>d (on)</sub>	_	32		ns	$V_{GS} = 4 V, I_D = 4 A,$
Rise time	tr	_	190		ns	$V_{DD} \cong 10 \text{ V}$
Turn-off delay time	t <sub>d (off)</sub>	_	85		ns	
Fall time	t <sub>f</sub>	_	110		ns	
Body-drain diode forward voltage	V <sub>DF</sub>	_	0.84	1.09	V	$I_F = 8 A, V_{GS} = 0^{Note 4}$
Body-drain diode reverse recovery time	t <sub>rr</sub>	—	35	_	ns	$I_F = 8 A, V_{GS} = 0$
						di <sub>F</sub> /dt = 20 A/∞s

Note: 4. Pulse test

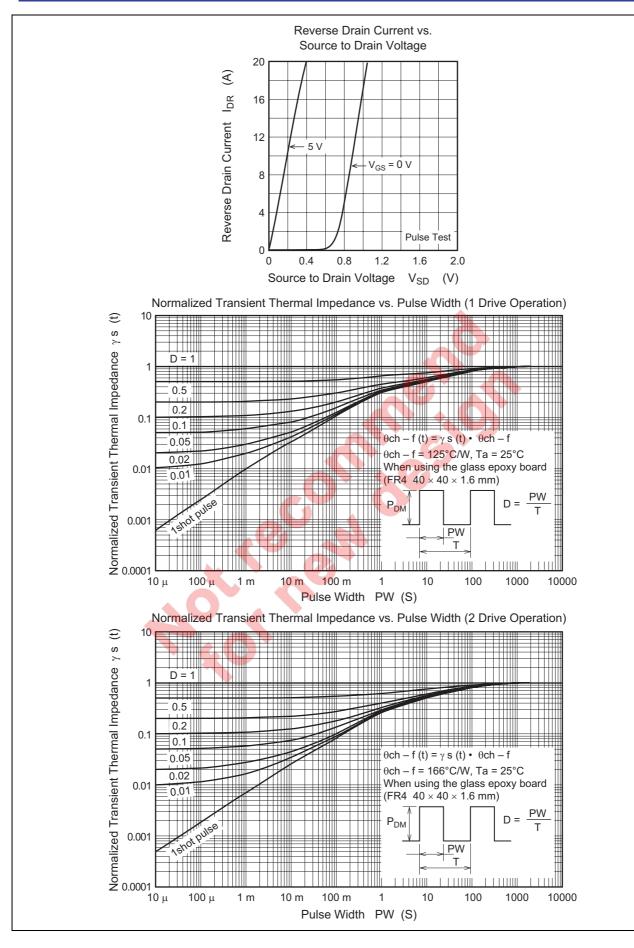
### **Main Characteristics**



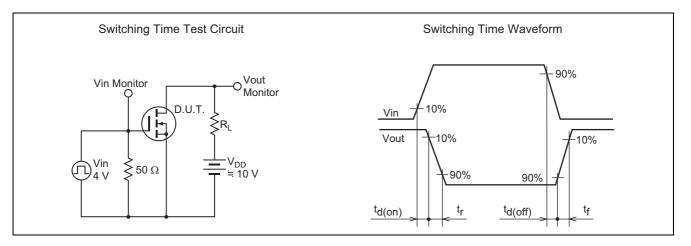






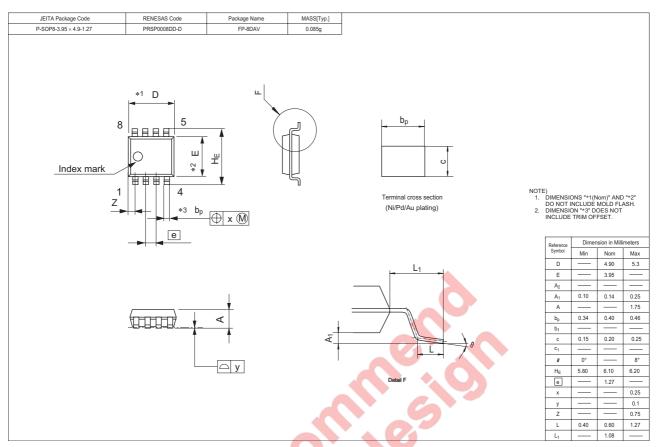








## **Package Dimensions**



## **Ordering Information**

Part Name	Quantity	Shipping Container
HAT2043R-EL-E	2500 pcs	Taping

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