BLP27M810

Power LDMOS transistor

Rev. 1 — 11 February 2016

1. Product profile

1.1 General description

10 W LDMOS power transistor for broadcast and Industrial, Scientific and Medical (ISM) applications at frequencies from HF to 2700 MHz.

The BLP27M810 driver is designed for high power CW applications and is assembled in a high performance thermally enhanced plastic package.

Table 1.Typical performance

RF performance at V_{DS} = 32 V; I_{Dq} = 100 mA; T_{case} = 25 °C in a class-AB application circuit.

| Test signal | f | I _{Dq} | V _{DS} | P _{L(AV)} | G _p | η _D |
|-------------|-------|-----------------|-----------------|--------------------|----------------|----------------|
| | (MHz) | (mA) | (V) | (W) | (dB) | (%) |
| CW | 2450 | 100 | 32 | 10 | 18.4 | 50.6 |
| Pulsed CW | 2700 | 110 | 28 | 2 | 17 | 19 |

1.2 Features and benefits

- High efficiency
- High power gain
- Excellent ruggedness
- Excellent thermal stability
- Integrated ESD protection
- Designed for broadband operation (HF to 2700 MHz)
- Compliant to Directive 2002/95/EC, regarding Restriction of Hazardous Substances (RoHS)

1.3 Applications

 Broadcast and Industrial, Scientific and Medical applications in the frequency range from HF to 2700 MHz

2. Pinning information

| Pin | Description | Simplified outline | Graphic symbol [1] |
|---------------------------|-------------|---|-------------------------------------|
| 1, 2, 7, 8, 9, 10, 15, 16 | n.c. | 40 | |
| 3, 4, 5, 6 | gate | 16 9 10000000 | 11, 12 لـــــا |
| 11, 12, 13, 14 | drain | | |
| exposed die-pad | source [2] | Image: marginal system 1 8 Transparent top view | 5, 6 13, 14 <i>aaa-017947</i> |

[1] To be used in single ended applications only.

[2] Connected to flange.

3. Ordering information

Table 3.Ordering information

| Type number | Package | | | | |
|-------------|---------|---|-----------|--|--|
| | Name | Description | Version | | |
| BLP27M810 | HVSON16 | plastic thermal enhanced very thin small outline package; no leads;16 terminals; body $4 \times 6 \times 0.85$ mm | SOT1371-1 | | |

4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol | Parameter | Conditions | Min | Max | Unit |
|------------------|----------------------|------------|------|------|------|
| V _{DS} | drain-source voltage | | - | 65 | V |
| V _{GS} | gate-source voltage | | -0.5 | +13 | V |
| T _{stg} | storage temperature | | -65 | +150 | °C |
| Tj | junction temperature | | - | 225 | °C |

5. Thermal characteristics

Table 5.Thermal characteristics

| Symbol | Parameter | Conditions | Тур | Unit |
|----------------------|--|---------------------------------|-----|------|
| R _{th(j-c)} | thermal resistance from junction to case | T_{case} = 80 °C; P_L = 2 W | 3.2 | K/W |

6. Characteristics

Table 6.DC characteristics

 T_i = 25 °C; unless otherwise specified.

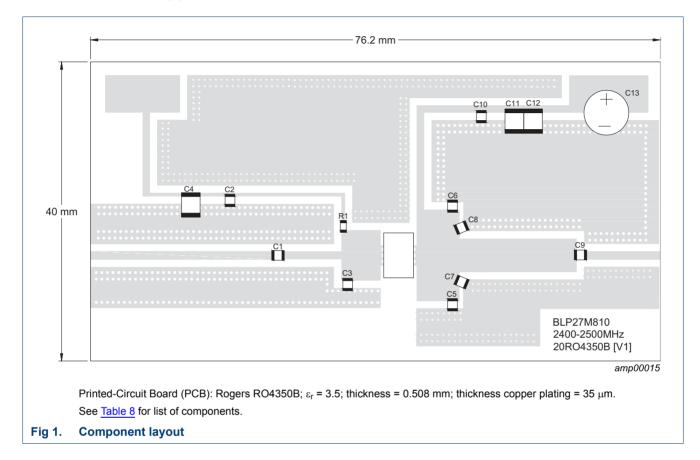
| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|------------------------|----------------------------------|--|------|------|------|------|
| V _{(BR)DSS} | drain-source breakdown voltage | V _{GS} = 0 V; I _D = 0.18 mA | 65 | - | - | V |
| V _{GS(th)} | gate-source threshold voltage | V _{DS} = 10 V; I _D = 18 mA | 1.5 | 1.9 | 2.3 | V |
| I _{DSS} | drain leakage current | V _{GS} = 0 V; V _{DS} = 28 V | -1.4 | - | +1.4 | μA |
| I _{DSX} | drain cut-off current | $V_{GS} = V_{GS(th)} + 3.75 V$ | - | 3.2 | - | А |
| I _{GSS} | gate leakage current | V _{GS} = 11 V; V _{DS} = 0 V | - | - | 140 | nA |
| g _{fs} | forward transconductance | V _{DS} = 10 V; I _D = 18 mA | - | 160 | - | mS |
| R _{DS(on)} | drain-source on-state resistance | $V_{GS} = V_{GS(th)} + 3.75 V;$ $V_{DS} = 10 V; I_D = 630 mA$ | - | 1000 | - | mΩ |

Table 7. RF characteristics

A derivative functional RF test is performed in production. The performance as mentioned below is verified by design and characterization in an Ampleon class-AB application board. Test signal: pulsed CW: $\delta = 10\%$: $t_p = 100$ µs: $V_{DS} = 28$ V: $I_{DS} = 110$ mA: $T_{rase} = 25$ °C: f = 2140 MHz

| rest signe | $r_{case} = 20^{\circ}$ r_{c | | | | | |
|---------------------|--|--------------------------|-----|-----|-----|------|
| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
| G _p | power gain | P _{L(AV)} = 2 W | 16 | 17 | - | dB |
| η_D | drain efficiency | P _{L(AV)} = 2 W | 17 | 19 | - | % |
| P _{L(1dB)} | output power at 1 dB gain compression | | 10 | - | - | W |

7. Application information

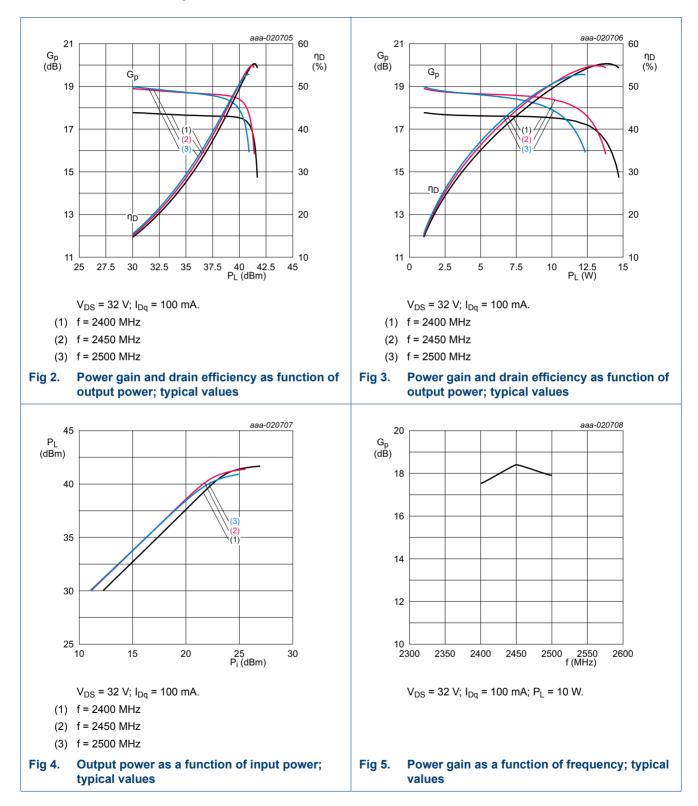


7.1 Application circuit

Table 8. List of components

See Figure 1 for component layout.

| Description | Value | Remarks | | | |
|-----------------------------------|--|---|--|--|--|
| multilayer ceramic chip capacitor | 0.5 pF | ATC 100A | | | |
| multilayer ceramic chip capacitor | 15 pF | ATC 100A | | | |
| multilayer ceramic chip capacitor | 2.2 pF | ATC 100A | | | |
| multilayer ceramic chip capacitor | 1 μF, 50 V | Murata: GRM32RR71H105KA01L | | | |
| multilayer ceramic chip capacitor | 2.4 pF | ATC 100A | | | |
| multilayer ceramic chip capacitor | 1.5 pF | ATC 100A | | | |
| multilayer ceramic chip capacitor | 15 pF | ATC 100A | | | |
| electrolytic capacitor | 10 μF, 63 V | | | | |
| chip resistor | 5.1 Ω | SMD 0805 | | | |
| | Description multilayer ceramic chip capacitor multilayer ceramic chip capacitor electrolytic capacitor | DescriptionValuemultilayer ceramic chip capacitor0.5 pFmultilayer ceramic chip capacitor15 pFmultilayer ceramic chip capacitor2.2 pFmultilayer ceramic chip capacitor1 μF, 50 Vmultilayer ceramic chip capacitor2.4 pFmultilayer ceramic chip capacitor1.5 pFmultilayer ceramic chip capacitor1.5 pFmultilayer ceramic chip capacitor1.5 pFmultilayer ceramic chip capacitor15 pFmultilayer ceramic chip capacitor10 μF, 63 V | | | |



7.2 Graphical data

8. Test information

8.1 Ruggedness in class-AB operation

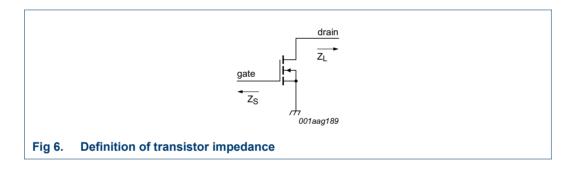
The BLP27M810 is capable of withstanding a load mismatch corresponding to VSWR = 10 : 1 through all phases under the following conditions: V_{DS} = 32 V; I_{Dg} = 100 mA; P_L = 10 W.

Table 9.Typical impedance

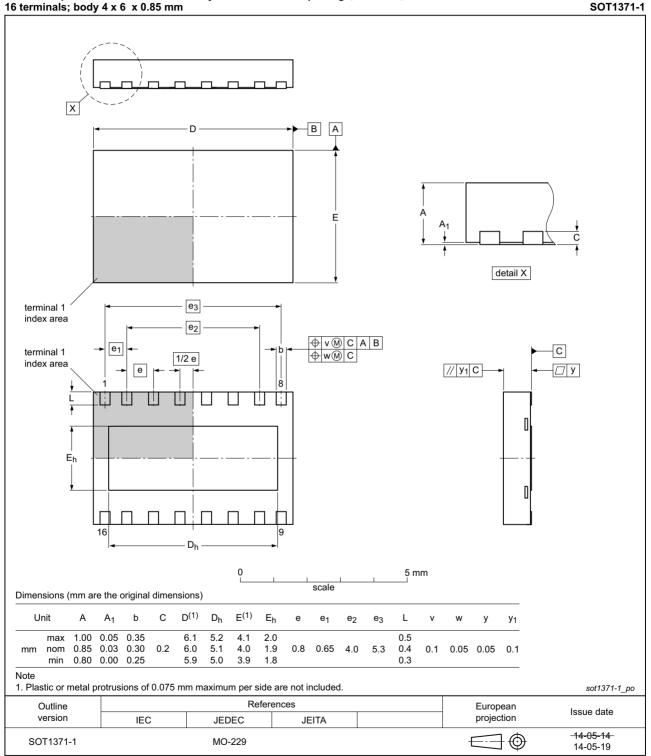
Measured load-pull data. Typical values unless otherwise specified. I_{Dq} = 120 mA; V_{DS} = 28 V.

| f | Z _S [1] | ZL [1] |
|-------|--------------------|------------|
| (MHz) | (Ω) | (Ω) |
| 2400 | 0.6 – j3.0 | 2.2 + j0.2 |
| 2450 | 0.6 – j3.3 | 2.4 - j0.1 |
| 2500 | 0.6 – j3.5 | 2.5 – j0.3 |

[1] Z_S and Z_L defined in Figure 6.



9. Package outline



HVSON16: plastic thermal enhanced very thin small outline package; no leads; 16 terminals: body 4 x 6 x 0.85 mm

Fig 7. Package outline SOT1371-1 (HVSON16)

10. Handling information

CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Observe precautions for handling electrostatic sensitive devices.

Such precautions are described in the ANSI/ESD S20.20, IEC/ST 61340-5, JESD625-A or equivalent standards.

11. Abbreviations

| Table 10. Abbreviations | | | |
|-------------------------|--|--|--|
| Acronym | Description | | |
| CW | Continuous Wave | | |
| ESD | ElectroStatic Discharge | | |
| HF | High Frequency | | |
| LDMOS | Laterally Diffused Metal-Oxide Semiconductor | | |
| SMD | Surface Mounted Device | | |
| VSWR | Voltage Standing-Wave Ratio | | |

12. Revision history

Table 11.Revision history

| Document ID | Release date | Data sheet status | Change notice | Supersedes |
|---------------|--------------|--------------------|---------------|------------|
| BLP27M810 v.1 | 20160211 | Product data sheet | - | - |

13. Legal information

13.1 Data sheet status

| Document status ^{[1][2]} | Product status ^[3] | Definition |
|-----------------------------------|-------------------------------|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
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| Product [short] data sheet | Production | This document contains the product specification. |

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