

Features

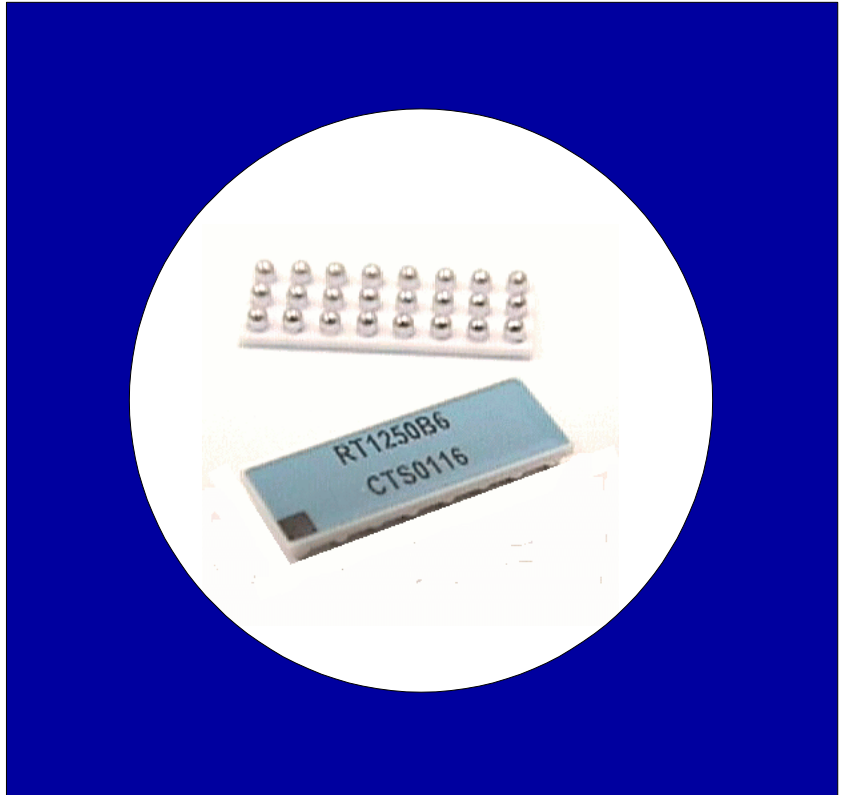
- 8 Bit Thevenin Termination Set
- Compliant for LVPECL Termination
- Excellent High Frequency Performance
- Slim BGA Package
- 1% Resistor Tolerance
- Low Channel to Channel Cross Talk

Description

These integrated termination networks provide high performance line termination for LVPECL busses.

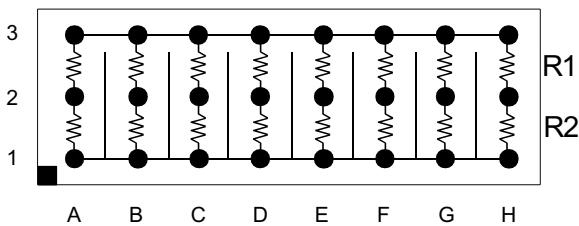
Specially designed shielding inside the package combined with a ceramic substrate virtually eliminates cross talk between channels that is common in other termination networks and resistor arrays.

The BGA packaging has been proven to reduce rework and improve reliability.

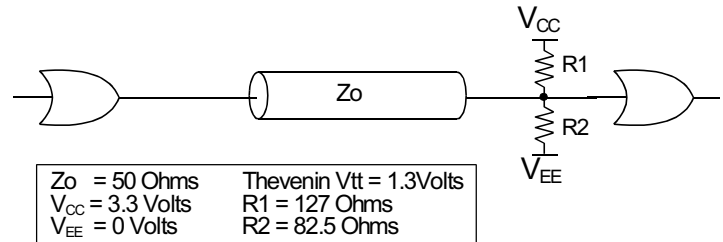


Custom resistor values and tolerances available upon request.

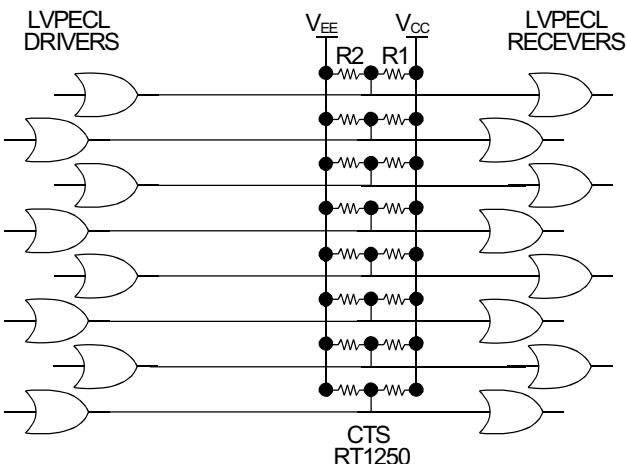
Schematic Diagram



LVPECL Thevenin Termination



Typical Application



Ordering Information

Part Number	R1 Ohms	R2 Ohms	Bits	Pitch(mm)
RT1250B6	127	82.5	8	1.27
RT1250B7	127	82.5	8	1.00

Tape and Reel Packaging

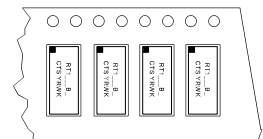
TR13 Suffix	
Tape Width	24 mm
Carrier Pitch	8 mm
Reel Diameter	13 inch
Parts/Reel	4,000
TR7 Suffix	
Tape Width	24 mm
Tape Pitch	8 mm
Reel Diameter	7 inch
Parts/Reel	1,000

Part Number Coding

7 inch reel, Add TR7 to part number, example RT1250B7TR7

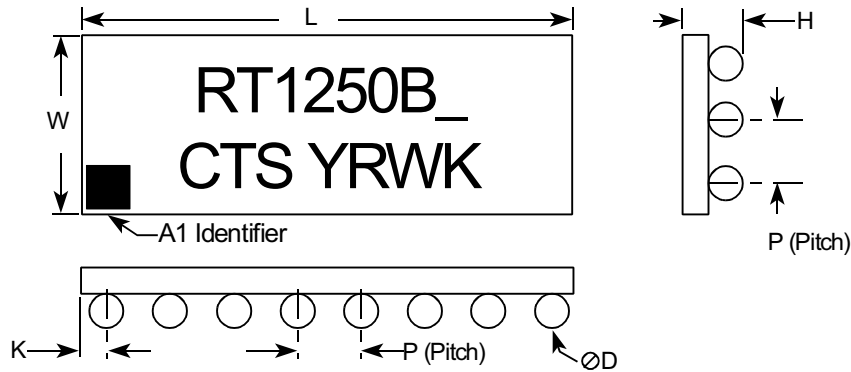
13 inch reel, Add TR13 to part number, example RT1250B7TR13

(Bulk packaging is not available)



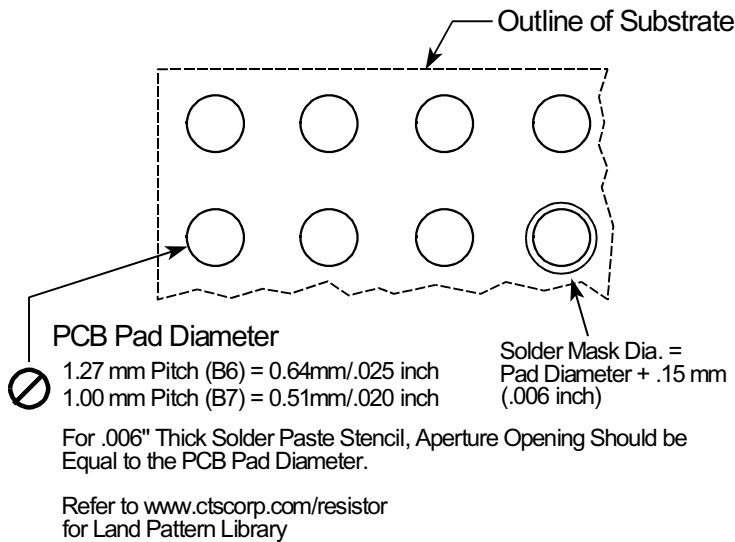
Direction of Feed →

Mechanical Diagram

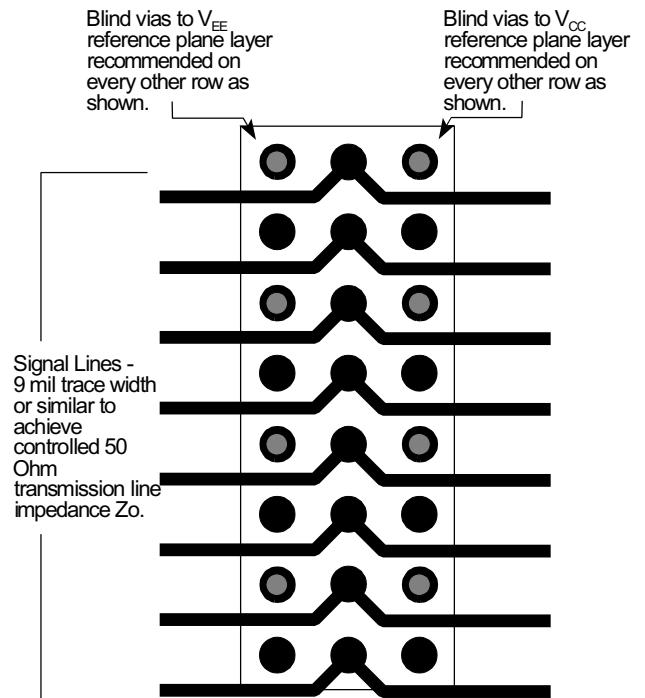


1.27 mm Pitch		L	W	H	P	D	K	Co-planarity
RT1250B6	mm inch	10.16±0.15 .400±.006	3.81±0.15 .150±.006	1.32±0.15 .052±.006	1.27±0.25 .050±.010	0.76±0.05 .030±.002	0.64±0.25 .025±.010	0.15 .006
1.00 mm Pitch		L	W	H	P	D	K	Co-planarity
RT1250B7	mm inch	8.00±0.15 .315±.006	3.00±0.15 .118±.006	1.19±0.15 .047±.006	1.00±0.25 .039±.010	0.64±0.05 .025±.002	0.50±0.25 .020±.010	0.15 .006

Recommended Land Pattern



BGA Routing Scheme



Electrical Specifications

Resistor Tolerance:
±1.0%

TCR:
±200 ppm/°C

Max. Resistor Power:
R1 = 0.08 Watts at 70°C
R2 = 0.03 Watts at 70°C

Operating Temp. Range:
-55°C to +125°C

Max. Package Power:
1.0 Watts at 70°C