

# **R6024VNX**

# Nch 600V 127mohm(typ.) Power MOSFET

V <sub>DSS</sub> (@Tj max.)*5	650V
$R_{DS(on)}(Max.)$	0.153Ω
I <sub>DP</sub> *2	±72A
P <sub>D</sub>	70W

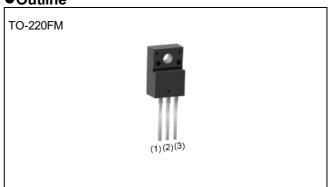
# ● Features

- 1) Fast reverse recovery time (trr)
- 2) Low on-resistance
- 3) Fast switching speed
- 4) Drive circuits can be simple
- 5) Pb-free plating; RoHS compliant
- 6) Halogen free mold compound

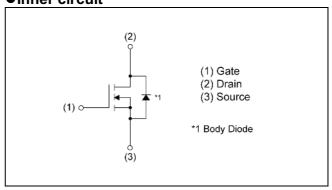
# Application

Switching applications

# Outline



# •Inner circuit



Marking	R6024VNX
Marking	R6024VIX

# ● **Absolute maximum ratings** (T<sub>a</sub> = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain - Source voltage	$V_{DSS}$	600	V
Continuous drain current (T <sub>c</sub> = 25°C)	I <sub>D</sub> *1	±13	Α
Pulsed drain current	I <sub>DP</sub> *2	±72	А
Gate - Source voltage	$V_{GSS}$	±30	V
Avalanche current, single pulse	I <sub>AS</sub> *3	1.9	Α
Avalanche energy, single pulse	E <sub>AS</sub> *3	102	mJ
MOSFET dv/dt	dv/dt*4	120	V/ns
Power dissipation (T <sub>c</sub> = 25°C)	P <sub>D</sub>	70	W
Junction temperature	T <sub>j</sub>	150	°C
Operating junction and storage temperature range	T <sub>stg</sub>	-55 to +150	°C

# ●Thermal resistance

Daramatar	Cymah al	Values			1.1
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R <sub>thJC</sub>	-	-	1.78	°C/W
Thermal resistance, junction - ambient	R <sub>thJA</sub>	-	-	62.5	°C/W
Soldering temperature, wavesoldering for 10s	T <sub>sold</sub>	-	-	265	°C

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

Darameter	Cumb al	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 1mA	600	-	-	V
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = 600V, V <sub>GS</sub> = 0V	-	-	100	μA
Gate - Source leakage current	I <sub>GSS</sub>	$V_{GS} = \pm 30 V, V_{DS} = 0 V$	-	-	±100	nA
Gate threshold voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 0.7 \text{mA}$	4.5	5.5	6.5	V
Static drain - source	D *5	V <sub>GS</sub> = 15V, I <sub>D</sub> = 6A	-	0.127	0.153	Ω
on - state resistance	R <sub>DS(on)</sub> *5	V <sub>GS</sub> = 10V, I <sub>D</sub> = 6A	-	0.138	0.166	Ω
Gate resistance	$R_{G}$	f = 1MHz, open drain	-	1.8	-	Ω

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

Davanastan	Cymaela al	Canditiana	Values			Linit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 100V	-	1800	-	
Output capacitance	C <sub>oss</sub>	f = 100kHz	-	65	-	
Effective output capacitance energy related	C <sub>o(er)</sub> *6	V <sub>GS</sub> = 0V	-	55	-	pF
Effective output capacitance time related	C <sub>o(tr)</sub> *7	V <sub>DS</sub> = 0V to 480V	-	330	1	
Turn - on delay time	t <sub>d(on)</sub> *5	$V_{DD} \simeq 300V$ , $V_{GS} = 15V$	-	30	-	
Rise time	t <sub>r</sub> *5	I <sub>D</sub> = 6A	-	15	-	
Turn - off delay time	t <sub>d(off)</sub> *5	R <sub>L</sub> ~ 50Ω	-	70	-	ns
Fall time	<b>t</b> <sub>f</sub> *5	$R_G = 10\Omega$	-	12	-	

# ● Gate charge characteristics (T<sub>a</sub> = 25°C)

Darameter	Cumb al	Conditions	Values			Unit
Parameter	Symbol	Symbol Conditions -		Тур.	Max.	Offic
Total gate charge	$Q_g^{*5}$	V <sub>DD</sub> ≃ 300V	-	38	-	
Gate - Source charge	Q <sub>gs</sub> *5	I <sub>D</sub> = 6A	-	15	-	nC
Gate - Drain charge	Q <sub>gd</sub> *5	V <sub>GS</sub> = 10V	-	20	-	
Gate plateau voltage	V <sub>(plateau)</sub>	$V_{DD} \simeq 300V$ , $I_D = 6A$	-	8.0	-	V

● Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

Parameter	Cumph of	Conditions	Values			Unit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Orill	
Source current	I <sub>S</sub> *1	T = 25°C	-	-	13	Α	
Pulsed source current	l <sub>SP</sub> *2	T <sub>C</sub> = 25°C	-	-	72	Α	
Source-Drain voltage	$V_{SD}^{*5}$	$V_{GS} = 0V$ , $I_S = 6A$	-	-	1.5	V	
Reverse recovery time	t <sub>rr</sub> *5	. V <sub>DD</sub> ≃ 400V	-	80	-	ns	
Reverse recovery charge	Q <sub>rr</sub> *5	I <sub>S</sub> = 6A	-	270	-	nC	
Peak reverse recovery current	I <sub>m</sub> *5	di/dt = 100A/μs	-	7.5	-	А	

<sup>\*1</sup> Limited only by maximum temperature allowed.

<sup>\*2</sup> Pw ≤ 10µs, Duty cycle ≤ 1%

<sup>\*3</sup> L  $\simeq$  50mH, V<sub>DD</sub> = 50V, R<sub>G</sub> = 25 $\Omega$ , starting T<sub>i</sub> = 25°C

<sup>\*4</sup>  $V_{DS}$  = 0 to 400V

<sup>\*5</sup> Pulsed

<sup>\*6</sup> Co(er) is a fixed capacitance that gives the same stored energy as Coss while  $V_{DS}$  is rising from 0 to 80%  $V_{DSS}$ .

<sup>\*7</sup> Co(tr) is a fixed capacitance that gives the same charging time as Coss while  $V_{DS}$  is rising from 0 to 80%  $V_{DSS}$ .

### • Electrical characteristic curves

Fig.1 Power Dissipation Derating Curve

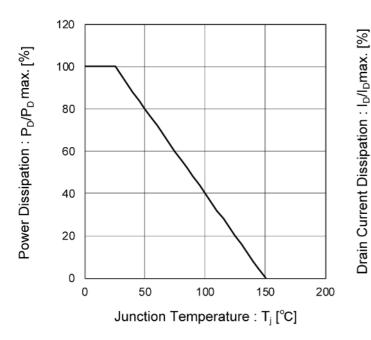


Fig.2 Drain Current Derating Curve

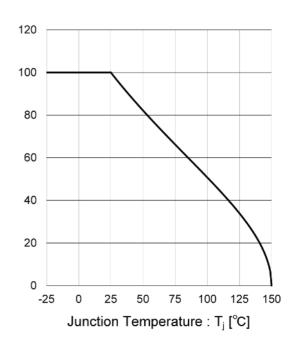


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

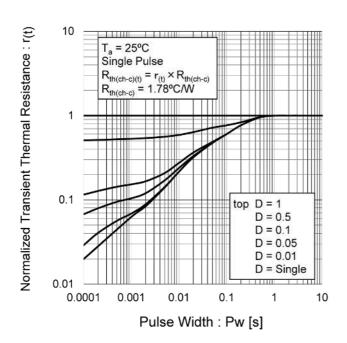
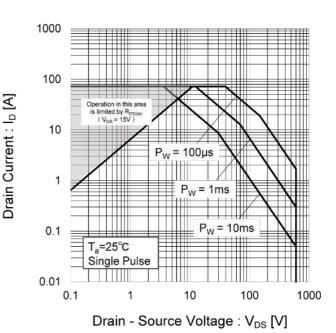


Fig.4 Maximum Safe Operating Area



### Electrical characteristic curves

Fig.5 Avalanche Energy Derating Curve

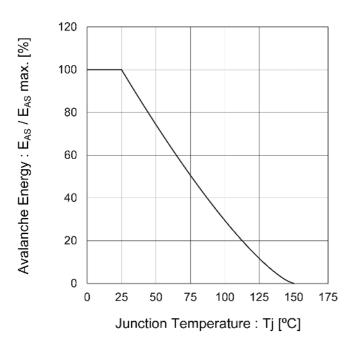


Fig.6 Normalized Breakdown Voltage vs. Junction Temperature

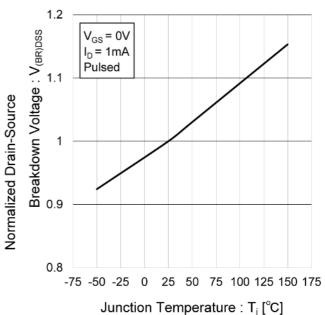


Fig.7 Output Characteristics(I)

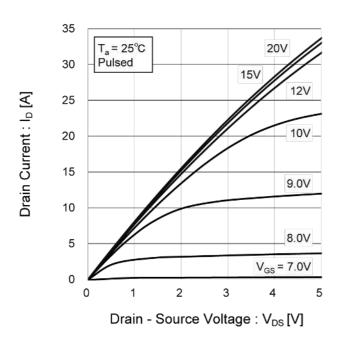
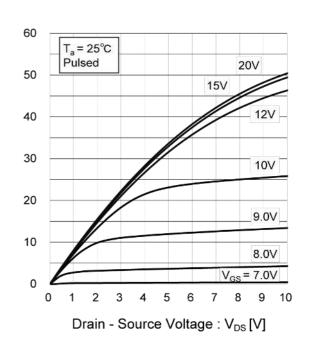


Fig.8 Output Characteristics(II)



Drain Current: Ip [A]

#### Electrical characteristic curves

Fig.9 Gate Threshold Voltage vs. Drain current

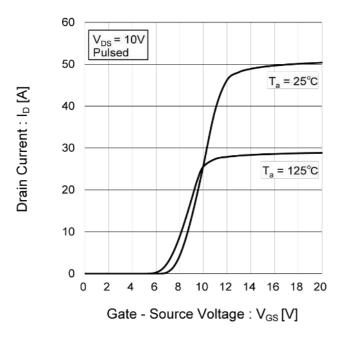


Fig.10 Normalized Gate Threshold .

Voltage vs Junction Temperature

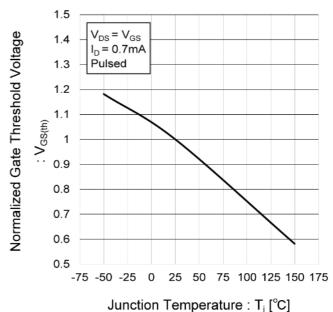


Fig.11 Static Drain - Source On - State Resistance vs. Gate Source Voltage

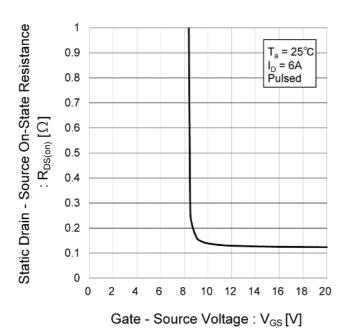
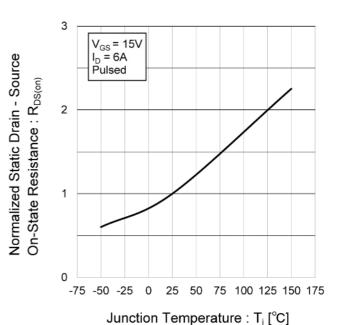


Fig.12 Normalized Static Drain - Source On - State Resistance vs. Junction Temperature



### • Electrical characteristic curves

Fig.13 Static Drain - Source On - State Resistance vs. Drain Current

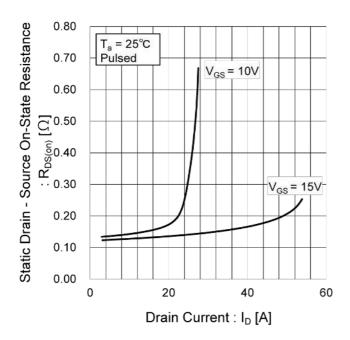


Fig.14 Capacitances

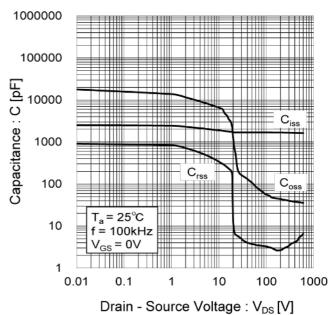


Fig.15 Coss Stored Energy

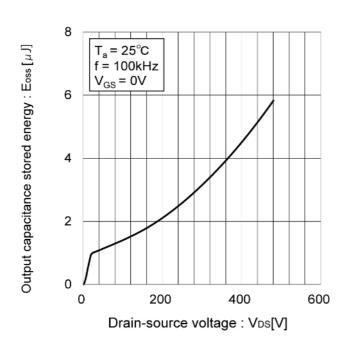
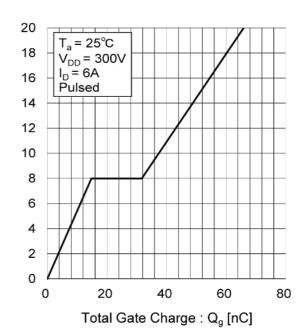


Fig.16 Gate Charge



Gate - Source Voltage : V<sub>GS</sub> [V]

Source Current : Is [A]

# • Electrical characteristic curves

Fig.17 Source Current vs. Source - Drain Voltage

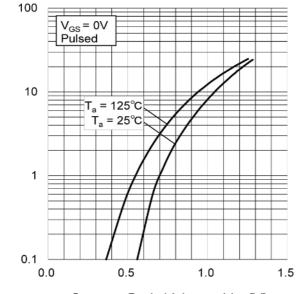
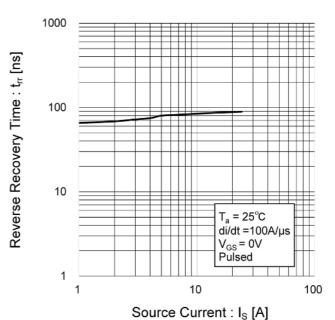


Fig.18 Reverse Recovery Time vs. Source Current



## Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

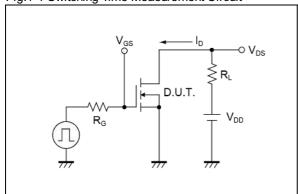


Fig.2-1 Gate Charge Measurement Circuit

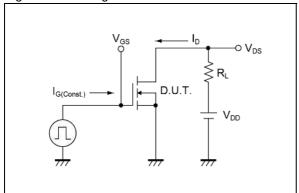


Fig.3-1 Avalanche Measurement Circuit

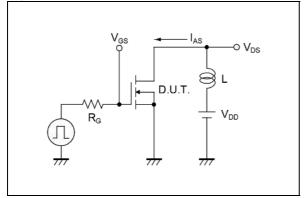


Fig.4-1 Diode Recovery Measurement Circuit

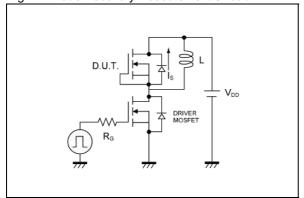


Fig.1-2 Switching Waveforms

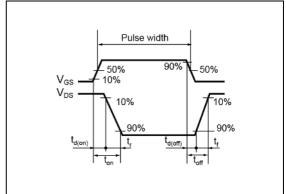


Fig.2-2 Gate Charge Waveform

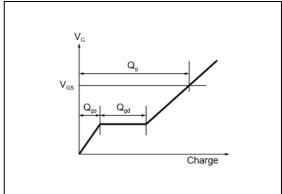


Fig.3-2 Avalanche Waveform

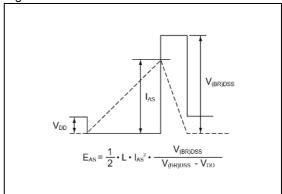
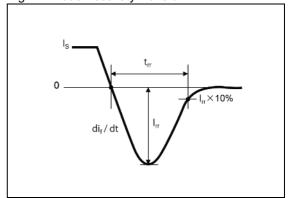
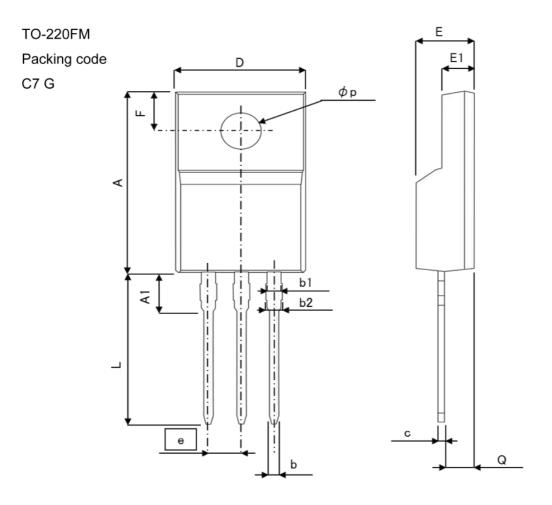


Fig.4-2 Diode Recovery Waveform



# Dimensions



DIM	MILIMETERS		INC	HES
DIM	MIN	MAX	MIN	MAX
Α	15.67	16.27	0.617	0.641
A1	3.03	3.43	0.119	0.135
b	0.70	0.95	0.028	0.037
b1	1.00	1.40	0.039	0.055
b2	1.10	1.50	0.043	0.059
С	0.45	0.65	0.018	0.026
D	9.90	10.30	0.390	0.406
E	4.60	5.00	0.181	0.197
E1	2.44	2.74	0.096	0.108
е	2.	54	0.1	00
F	3.10	3.50	0.122	0.138
L	12.6	13.6	0.946	0.535
р	2.98	3.38	0.117	0.133
Q	2.25	3.25	0.089	0.128

Dimension in mm/inches



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CLASSIV	CLASSII	CLASSⅢ	CLASSⅢ

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  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
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- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
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For details, please refer to ROHM Mounting specification

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
  may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
  exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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