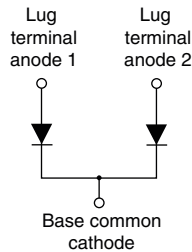


## FRED Pt<sup>®</sup> Gen 5, Ultrafast Rectifier Diode, 600 V, 300 A


**TO-244**

**FEATURES**

- Ultrafast and optimized  $Q_{rr}$
- Best in class forward voltage drop and switching losses trade off
- Optimized for high speed operation
- 175 °C maximum operation junction temperature
- UL approved file E222165
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)


**BENEFITS**

- Reduced RFI and EMI
- Higher frequency operation
- Reduced snubbing
- Reduced parts count

**DESCRIPTION / APPLICATIONS**

Featuring a unique combination of low conduction and switching losses the FRED Pt<sup>®</sup> Gen 5 is the right choice for soft switched and resonant converters, as well as medium frequency hard switching converters.

These devices are also ideally suited for HF welding, power converters, and other applications where switching losses are significant portion of the total losses.

**PRIMARY CHARACTERISTICS**

$I_{F(AV)}$ at 81 °C (per module)	300 A
$V_R$	600 V
$Q_{rr}$ (typical)	620 nC
$t_{rr}$	68 ns
Type	Modules - diode, FRED Pt <sup>®</sup>
Package	TO-244
Circuit configuration	Two diodes common cathode

**ABSOLUTE MAXIMUM RATINGS**

PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Cathode to anode voltage	$V_R$		600	V
Continuous forward current per diode	$I_{F(DC)}$	$T_C = 25\text{ °C}$	259	A
		$T_C = 85\text{ °C}$	185	
		$T_C = 109\text{ °C}$	150	
Non-repetitive single pulse forward current per diode	$I_{FSM}$	$T_C = 25\text{ °C}$	1150	
Maximum power dissipation per diode	$P_D$	$T_C = 25\text{ °C}$	468	W
		$T_C = 109\text{ °C}$	206	
Storage temperature range	$T_{Stg}$		-40 to +150	°C
Operating junction temperature range	$T_J$		-40 to +175	°C

**ELECTRICAL SPECIFICATIONS PER LEG ( $T_J = 25\text{ °C}$  unless otherwise specified)**

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage	$V_{BR}$	$I_R = 200\text{ }\mu\text{A}$	600	-	-	V
Forward voltage	$V_{FM}$	$I_F = 150\text{ A}$	-	1.36	1.70	
		$I_F = 300\text{ A}$	-	1.52	2.20	
		$I_F = 150\text{ A}, T_J = 150\text{ °C}$	-	1.09	-	
		$I_F = 300\text{ A}, T_J = 150\text{ °C}$	-	1.31	-	
Reverse leakage current	$I_{RM}$	$T_J = 150\text{ °C}, V_R = 600\text{ V}$	-	0.23	0.7	mA
Series inductance	$L_S$	From top of terminal hole to mounting plane	-	5	-	nH
Maximum junction capacitance per leg	$C_T$	$V_{DC} = 5\text{ V}, f = 1\text{ MHz}, 25\text{ °C}$	-	-	1.0	nF



<b>DYNAMIC RECOVERY CHARACTERISTICS</b> ( $T_J = 25\text{ }^\circ\text{C}$ unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Reverse recovery time	$t_{rr}$	$T_J = 25\text{ }^\circ\text{C}$	$I_F = 50\text{ A}$ $di_F/dt = 200\text{ A}/\mu\text{s}$ $V_R = 300\text{ V}$	-	68	-	ns
		$T_J = 125\text{ }^\circ\text{C}$		-	170	-	
Peak recovery current	$I_{RRM}$	$T_J = 25\text{ }^\circ\text{C}$		-	7	-	A
		$T_J = 125\text{ }^\circ\text{C}$		-	22	-	
Reverse recovery charge	$Q_{rr}$	$T_J = 25\text{ }^\circ\text{C}$		-	620	-	nC
		$T_J = 125\text{ }^\circ\text{C}$		-	3150	-	

<b>THERMAL - MECHANICAL SPECIFICATIONS</b>						
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNITS	
Thermal resistance, junction to case	per leg	-	-	0.32	°C/W	
	per module	-	-	0.16		
Thermal resistance, case to heatsink	$R_{thCS}$	-	0.10	-		
Weight		-	68	-	g	
		-	2.4	-	oz.	
Mounting torque		30 (3.4)	-	40 (4.6)	lbf · in (N · m)	
Mounting torque center hole		12 (1.4)	-	18 (2.1)		
Terminal torque		30 (3.4)	-	40 (4.6)		
Vertical pull		-	-	80	lbf · in	
2" lever pull		-	-	35		
Case style		TO-244				

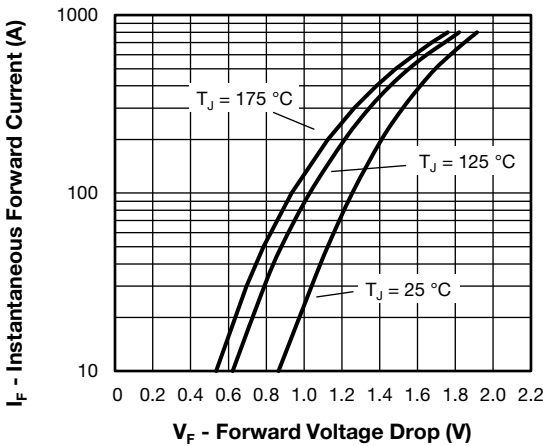


Fig. 1 - Typical Forward Voltage Drop vs. Instantaneous Forward Current (Per Diode)

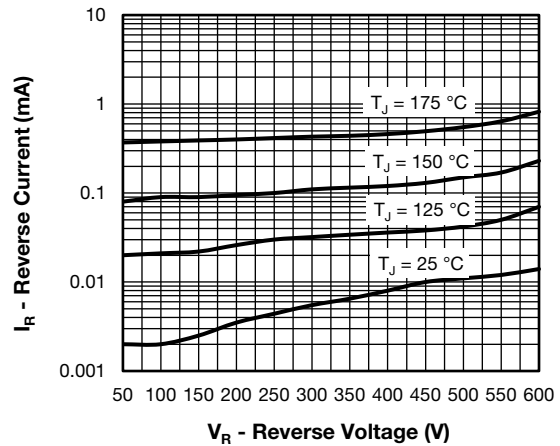


Fig. 2 - Typical Reverse Current vs. Reverse Voltage (Per Diode)

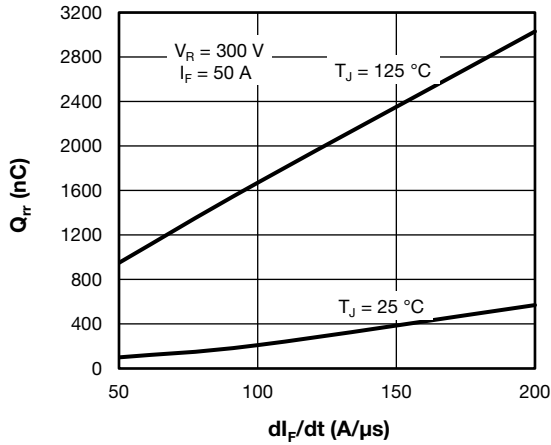


Fig. 3 - Typical Reverse Recovery Charge vs  $di_F/dt$  (Per Diode)

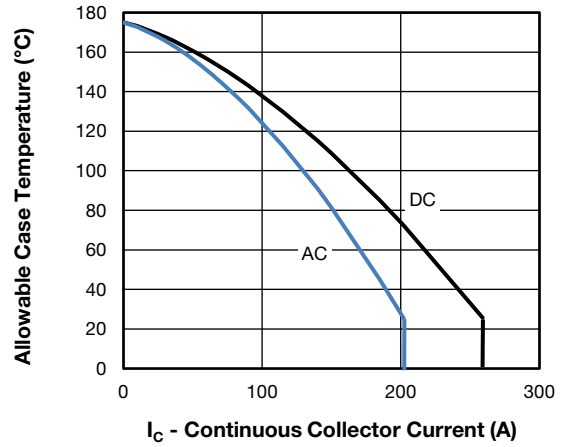


Fig. 6 - Maximum Continuous Forward Current vs. Case Temperature

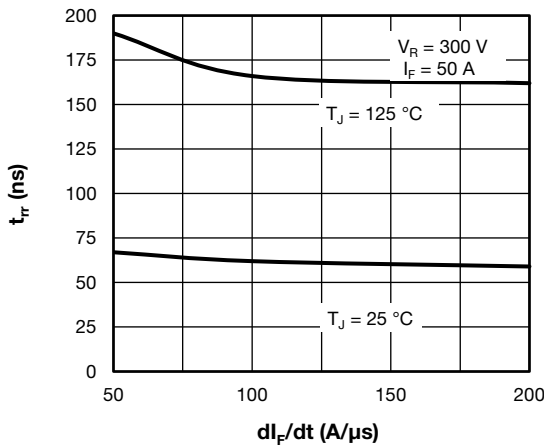


Fig. 4 - Typical Reverse Recovery Time vs  $di_F/dt$  (Per Diode)

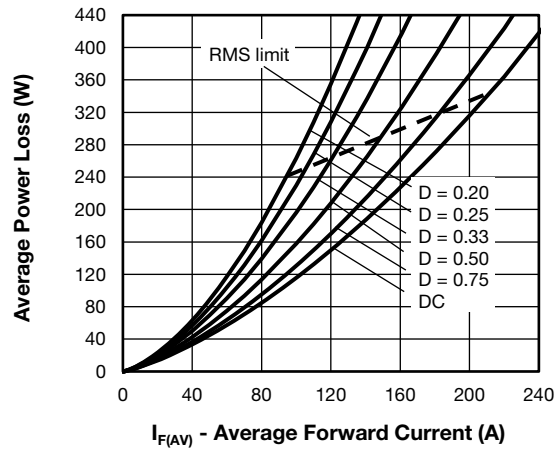


Fig. 7 - Average Power Loss vs. Average Forward Current (Forward Power Loss Characteristics)

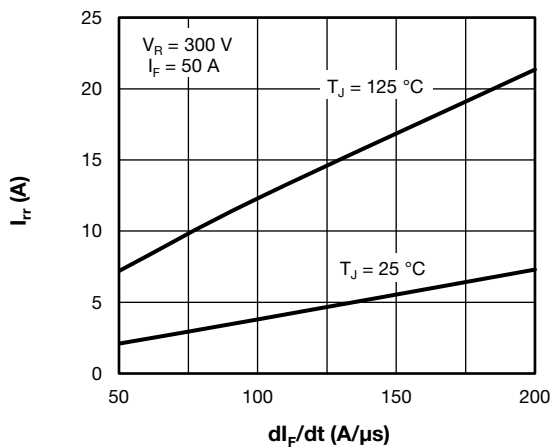


Fig. 5 - Typical Reverse Recovery Current vs  $di_F/dt$  (Per Diode)

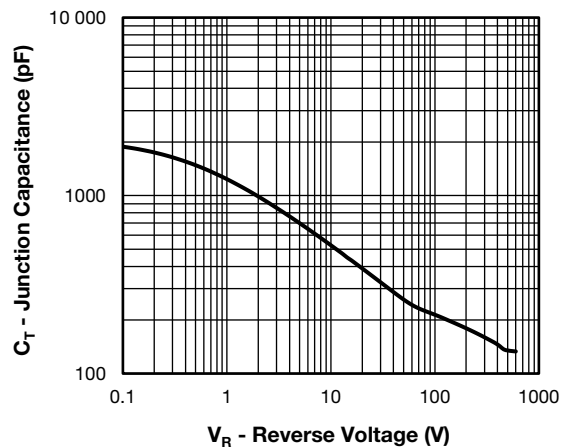


Fig. 8 - Typical Junction Capacitance vs. Reverse Voltage

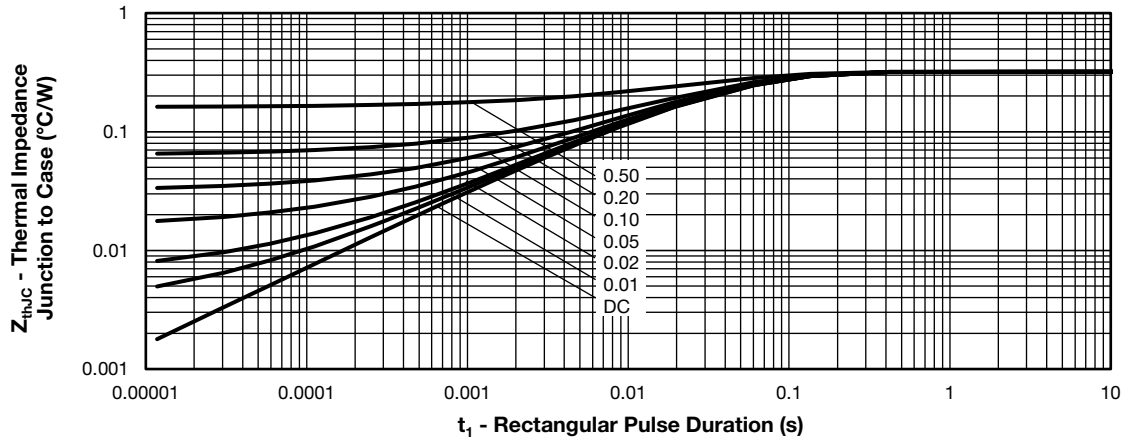


Fig. 9 -  $Z_{thJC}$  Maximum Thermal Impedance Junction to Case vs.  $t_1$  Rectangular Pulse Duration

**ORDERING INFORMATION TABLE**

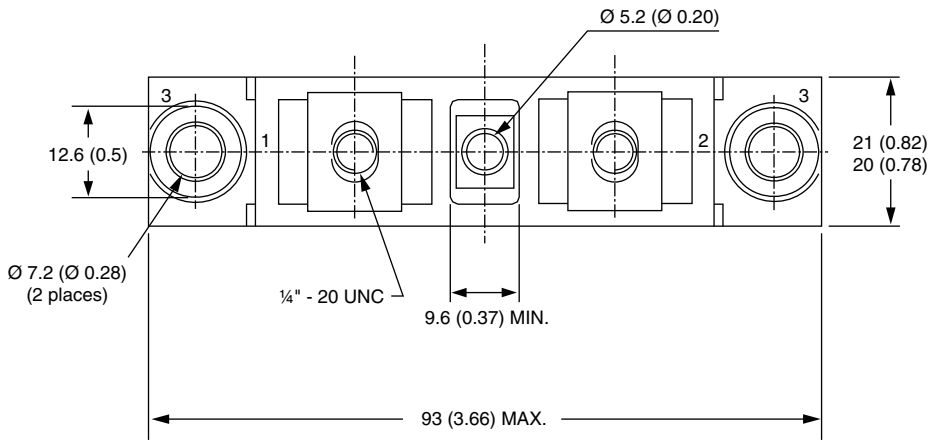
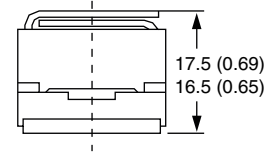
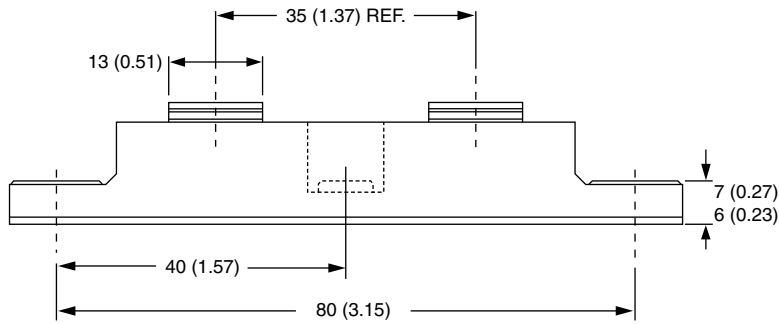
Device code	<b>VS-VS</b>	<b>5HD</b>	<b>300</b>	<b>C</b>	<b>W</b>	<b>60</b>
	①	②	③	④	⑤	⑥
	1	2	3	4	5	6
	-	-	-	-	-	-
	-	-	-	-	-	-
	-	-	-	-	-	-
	-	-	-	-	-	-
	-	-	-	-	-	-
	-	-	-	-	-	-

CIRCUIT CONFIGURATION		
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING
Two diodes common cathode	C	

LINKS TO RELATED DOCUMENTS	
Dimensions	<a href="http://www.vishay.com/doc?95021">www.vishay.com/doc?95021</a>



## DIMENSIONS in millimeters (inches)





## Disclaimer

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