

TYPES SN5423, SN5425, SN7423, SN7425 DUAL 4-INPUT NOR GATES WITH STROBE

REVISED DECEMBER 1983

- Package Options Include Plastic and Ceramic DIPs
 - Dependable Texas Instruments Quality and Reliability

description

These devices contain dual 4-input positive NOR gates with strobe. The SN5423 and SN7423 are expandable, and perform the Boolean functions:

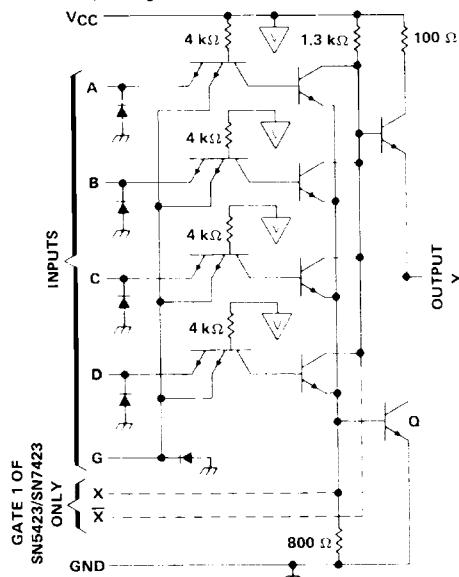
$$1Y = \overline{1G(1A + 1B + 1C + 1D) + X} \text{ and}$$

with X = output of SN5460/SN7460. The SN5425 and SN7425 perform the Boolean function:

$$Y = \overline{G(A + B + C + D)}$$

The SN5423 and the SN5425 are characterized for operation over the full military temperature range of -55°C to 125°C . The SN7423 and the SN7425 are characterized for operation from 0°C to 70°C .

schematic (each gate)

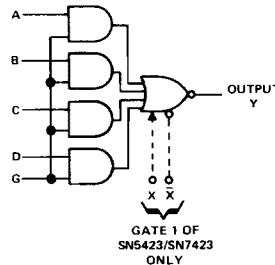


Notes:

- A. Component values shown are nominal.
- B. Both expander inputs are used simultaneously for expanding.
- C. If expander is not used leave X and X open.
- D. A total of four expander gates can be connected to the expander inputs.

 $V = V_{CC}$ bus

logic diagram



TRUTH TABLE

INPUTS					OUTPUT
A	B	C	D	G	Y
H	X	X	X	H	L
X	H	X	X	H	L
X	X	H	X	H	L
X	X	X	H	H	L
L	L	L	L	X	H
X	X	X	X	L	H

Expander inputs are open.
H = high level, L = low level, X = irrelevant

TYPES SN5423, SN5425, SN7423, SN7425 DUAL 4-INPUT NOR GATES WITH STROBE

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

NOTES: 1. Voltage values, except interemitter voltage, are with respect to network ground terminal.
2. This is the voltage between two emitters of a multiple-emitter transistor.

recommended operating conditions

			'23, '25			UNIT	
			MIN	NOM	MAX		
V _{CC}	Supply voltage		54 Family	4.5	5	5.5	V
			74 Family	4.75	5	5.25	
V _{IH}	High-level input voltage			2			V
V _{IL}	Low-level input voltage				0.8		V
I _{OH}	High-level output current				-0.8		mA
I _{OL}	Low-level output current		54 Family		16		mA
			74 Family		16		
T _A	Operating free-air temperature range		54 Family	-55	125		°C
			74 Family	0	70		

The '23 is designed for use with up to four '60 expanders

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER		TEST CONDITIONS†		MIN	TYP‡	MAX	UNIT	
V _I		V _{CC} = MIN,	I _I = - 12 mA			- 1.5	V	
V _{OH}		V _{CC} = MIN,	V _{IL} = 0.8 V,	I _{OH} = - 0.8 mA	2.4	3.4	V	
V _{OL}		V _{CC} = MIN,	V _{IH} = 2 V,	I _{OL} = 16 mA	0.2	0.4	V	
I _I		V _{CC} = MAX,	V _I = 5.5 V			1	mA	
I _{IH}	data inputs	V _{CC} = MAX,				40		
	strobe inputs	V _{CC} = MAX,				160	µA	
I _{IL}	data inputs	V _{CC} = MAX,				- 1.6		
	strobe inputs	V _{CC} = MAX,				- 6.4	mA	
I _{OS\$}		V _{CC} = MAX		54 Family	- 20	- 55		
				74 Family	- 18	- 55	mA	
I _{CCH}		V _{CC} = MAX,	All inputs at 0 V			8	16	mA
I _{CCL}		V _{CC} = MAX,	All inputs at 5 V			10	19	mA

† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions for the applicable device type. Σ indicates Σ_{IC} and Σ_{VDD} are on-chip.

All typical values are at $V_{DD} = 5$ V, $T_J = 25^\circ\text{C}$.

- All typical values are at $V_{CC} = 5$ V, $I_A = 25$ C.
- Not more than one output should be shorted at a time.

**TYPES SN5423, SN5425, SN7423, SN7425
DUAL 4-INPUT NOR GATES WITH STROBE**

electrical characteristics (SN5423 circuits) using expander inputs, $V_{CC} = 4.5\text{ V}$, $T_A = -55^\circ\text{C}$

PARAMETER	TEST CONDITIONS	MIN	TYP†	MAX	UNIT
$I_X^{\bar{X}}$ Expander current	$V_{XX} = 0.4\text{ V}$, $I_{OL} = 16\text{ mA}$			-3.5	mA
$V_{BE(Q)}$ Base-Emitter voltage of output transistor (Q)	$I_{OL} = 16\text{ mA}$, $I_X + I_{\bar{X}} = 0.41\text{ mA}$, $R_{XX} = 0$			1.1	V
V_{OH} High-level output voltage	$I_{OH} = -0.4\text{ mA}$, $I_X = 0.15\text{ mA}$, $I_{\bar{X}} = -0.15\text{ mA}$	2.4	3.4		V
V_{OL} Low-level output voltage	$I_{OL} = 16\text{ mA}$, $I_X + I_{\bar{X}} = 0.3\text{ mA}$, $R_{XX} = 114\text{ }\Omega$			0.2	0.4

electrical characteristics (SN7423 circuits) using expander inputs, $V_{CC} = 4.75\text{ V}$, $T_A = 0^\circ\text{C}$

PARAMETER	TEST CONDITIONS	MIN	TYP†	MAX	UNIT
$I_X^{\bar{X}}$ Expander current	$V_{XX} = 0.4\text{ V}$, $I_{OL} = 16\text{ mA}$			-3.8	mA
$V_{BE(Q)}$ Base-Emitter voltage of output transistor (Q)	$I_{OL} = 16\text{ mA}$, $I_X + I_{\bar{X}} = 0.62\text{ mA}$, $R_{XX} = 0$			1	V
V_{OH} High-level output voltage	$I_{OH} = -0.4\text{ mA}$, $I_X = 0.27\text{ mA}$, $I_{\bar{X}} = -0.27\text{ mA}$	2.4	3.4		V
V_{OL} Low-level output voltage	$I_{OL} = 16\text{ mA}$, $I_X + I_{\bar{X}} = 0.43\text{ mA}$, $R_{XX} = 130\text{ }\Omega$			0.2	0.4

† All typical values are at $V_{CC} = 5\text{ V}$, $T_A = 25^\circ\text{C}$.

switching characteristics, $V_{CC} = 5\text{ V}$, $T_A = 25^\circ\text{C}$, $N = 10$, (see note 3)

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
t_{PLH}	$R_L = 400\text{ }\Omega$, $C_L = 15\text{ pF}$		13	22	ns
t_{PHL}	$R_L = 400\text{ }\Omega$, $C_L = 15\text{ pF}$		8	15	ns

NOTE 3: Switching characteristics of the SN5423 and SN7424 are tested with the expander pins open.

3

TTL DEVICES