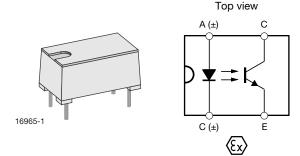
# CNY65Exi

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Vishay Semiconductors

# **Optocoupler, Phototransistor Output, ATEX Certified**



### LINKS TO ADDITIONAL RESOURCES



### DESCRIPTION

The CNY65Exi consists of a phototransistor optically coupled to an infrared-emitting diode in a 4 pin plastic package. The components are mounted opposite one another, with a distance between input and output of > 3.0 mm; meeting the highest of safety requirements.

The CNY65Exi is ATEX certificated for explosive atmospheres according to the Directive 2014/34/EU

### AGENCY APPROVALS

- ATEX (Ex):PTB 03 ATEX 2033 U
- EN 60079-0 : 2018 EN 60079-11 : 2012 EN 60079-26 : 2015

### FEATURES

- Suitable for intrinsic safe circuits for gas and dust
- Gas safety provision: II (1) G [Ex ia] IIC
- Dust safety provision: II (1) D [Ex ia] IIIC
- Conforms to latest EN 60079-0 : 2018
- Qualified for continuously, longterm, or frequently dangerous explosive environments, zone 0
- Isolation voltage (V<sub>ISO</sub>) of 11 600 V<sub>peak</sub> for 1 minute
- Distance from emitter to detector through insulation  $\geq 3 \text{ mm}$
- CTR from 50 % to 300 %
- Very low coupling capacity (C<sub>K</sub>)
  - 0.3 pF superior noise immunity between input and output pins
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### APPLICATIONS

- Electronics used in potentially explosive gas and dust environments
  - Safety related process automation and instrumentation
  - Natural gas metering and flow measurement
  - Power and motor switching
  - Power supplies, metering, and data acquisition
  - Lighting and signaling
  - Petrol and grain transport and storage

ORDERING INFORMATION						
C N Y PART NUMBER	6 5 X E 2					
AGENCY CERTIFIED / PACKAGE	CTR (%)					
ATEX	50 to 300	100 to 200				
DIP-4, HV, high isolation distance	CNY65Exi	CNY65BExi				

1

Pb-free

BoHS

COMPLIANT



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<b>ABSOLUTE MAXIMUM RATINGS</b> ( $T_{amb} = 25 \text{ °C}$ , unless otherwise specified)								
PARAMETER	CONDITION	SYMBOL	VALUE	UNIT				
INPUT								
Reverse voltage		V <sub>R</sub>	5	V				
Forward current		١ <sub>F</sub>	75	mA				
Forward surge current	$t_p \le 10 \ \mu s$	I <sub>FSM</sub>	1.5	А				
Power dissipation		P <sub>diss</sub>	120	mW				
Junction temperature		Тj	100	°C				
OUTPUT								
Collector emitter voltage		V <sub>CEO</sub>	32	V				
Emitter collector voltage		V <sub>ECO</sub>	7	V				
Collector current		Ι <sub>C</sub>	50	mA				
Collector peak current	$t_p/T = 0.5, t_p \le 10 \text{ ms}$	I <sub>CM</sub>	100	mA				
Power dissipation		P <sub>diss</sub>	130	mW				
Junction temperature		Тj	100	°C				
COUPLER								
Total power dissipation		P <sub>tot</sub>	250	mW				
Ambient temperature range		T <sub>amb</sub>	-55 to +85	°C				
Storage temperature range		T <sub>stg</sub>	-55 to +100	°C				
Soldering temperature	2 mm from case, t $\leq$ 10 s	T <sub>sld</sub>	260	°C				

#### Note

Stresses in excess of the absolute Maximum Ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
Maximum Rating for extended periods of the time can adversely affect reliability

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)									
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT			
INPUT	INPUT								
Forward voltage	I <sub>F</sub> = 50 mA	V <sub>F</sub>	-	1.25	1.6	V			
OUTPUT									
Collector emitter voltage	I <sub>C</sub> = 1 mA	V <sub>CEO</sub>	32	-	-	V			
Emitter collector voltage	I <sub>E</sub> = 100 μA	V <sub>ECO</sub>	7	-	-	V			
Collector dark current	$V_{CE} = 20 \text{ V}, \text{ I}_{f} = 0, \text{ E} = 0$	I <sub>CEO</sub>	-	-	200	nA			
COUPLER									
Collector saturation voltage	I <sub>F</sub> = 10 mA, I <sub>C</sub> = 1 mA	V <sub>CEsat</sub>	-	-	0.3	V			
Cut-off frequency	$\label{eq:VCE} \begin{array}{l} V_{CE} = 5 \ V, \ I_{F} = 10 \ mA, \\ R_{L} = 100 \ \Omega \end{array}$	f <sub>c</sub>	110	-	-	kHz			
Coupling capacitance	f = 1 MHz	C <sub>k</sub>	-	0.3	-	pF			

#### Note

• Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements

CURRENT TRANSFER RATIO (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER TEST CONDITION PART SYMBOL MIN. TYP. MAX. UN					UNIT		
I <sub>C</sub> /I <sub>F</sub>	$V_{1} = 5V_{1} = 10 m^{4}$	CNY65Exi	CTR	50	100	300	%
	V <sub>CE</sub> = 5 V, I <sub>F</sub> = 10 mA	CNY65BExi	CTR	100	-	200	%



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SAFETY AND INSULATION RATINGS							
PARAMETER	SYMBOL	VALUE	UNIT				
Climatic classification	According to IEC 68 part 1		40 / 85 / 21				
Pollution degree	According to DIN VDE 0109		2				
Comparative tracking index	Insulation group IIIa	CTI	475				
Maximum rated withstanding isolation voltage	t = 1 min	V <sub>ISO</sub>	8200	V <sub>RMS</sub>			
Maximum transient isolation voltage		V <sub>IOTM</sub>	12 000	V <sub>peak</sub>			
Maximum repetitive peak isolation voltage		V <sub>IORM</sub>	1450	V <sub>peak</sub>			
	$T_{amb} = 25 \ ^{\circ}C, \ V_{IO} = 500 \ V$	R <sub>IO</sub>	≥ 10 <sup>12</sup>	Ω			
Isolation resistance	$T_{amb} = 100 \ ^{\circ}C, \ V_{IO} = 500 \ V$	R <sub>IO</sub>	≥ 10 <sup>11</sup>	Ω			
	$T_{amb} = T_S$ , $V_{IO} = 500 V$	R <sub>IO</sub>	≥ 10 <sup>9</sup>	Ω			
Output safety power		P <sub>SO</sub>	250	mW			
Input safety current		I <sub>si</sub>	120	mA			
Input safety temperature		T <sub>S</sub>	150	°C			
Creepage distance			≥ 14	mm			
Clearance distance			≥ 14	mm			
Insulation thickness		DTI	≥3	mm			

SWITCHING CHARACTERISTICS ( $T_{amb} = 25 \text{ °C}$ , unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Delay time	$V_S = 5 \text{ V}, \text{ I}_C = 5 \text{ mA}, \text{ R}_L = 100 \Omega$ , (see Fig. 1)	t <sub>d</sub>	-	2.6	-	μs
Rise time	$V_S$ = 5 V, $I_C$ = 5 mA, $R_L$ = 100 $\Omega$ , (see Fig. 1)	t <sub>r</sub>	-	2.4	-	μs
Fall time	$V_S$ = 5 V, $I_C$ = 5 mA, $R_L$ = 100 $\Omega$ , (see Fig. 1)	t <sub>f</sub>	-	2.4	-	μs
Storage time	$V_S = 5 \text{ V}, \text{ I}_C = 5 \text{ mA}, \text{ R}_L = 100 \Omega$ , (see Fig. 1)	ts	-	0.3	-	μs
Turn-on time	$V_{S}$ = 5 V, $I_{C}$ = 5 mA, $R_{L}$ = 100 $\Omega$ , (see Fig. 1)	t <sub>on</sub>	-	5	-	μs
Turn-off time	$V_S$ = 5 V, $I_C$ = 5 mA, $R_L$ = 100 $\Omega$ , (see Fig. 1)	t <sub>off</sub>	-	3	-	μs
Turn-on time	$V_{S}$ = 5 V, I <sub>F</sub> = 10 mA, R <sub>L</sub> = 1 k $\Omega$ , (see Fig. 2)	t <sub>on</sub>	-	25	-	μs
Turn-off time	$V_{S} = 5 \text{ V}, \text{ I}_{F} = 10 \text{ mA}, \text{ R}_{L} = 1 \text{ k}\Omega$ , (see Fig. 2)	t <sub>off</sub>	-	42.5	-	μs

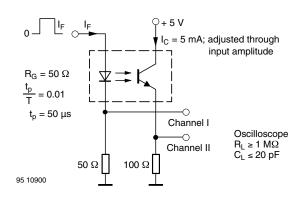
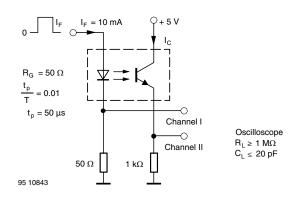
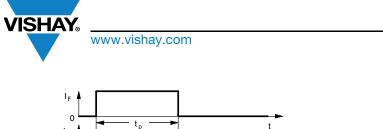


Fig. 1 - Test Circuit, Non-Saturated Operation







## CNY65Exi

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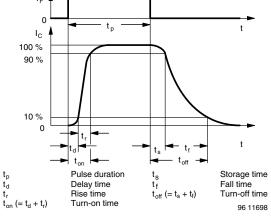


Fig. 3 - Switching Times

### **TYPICAL CHARACTERISTICS** ( $T_{amb} = 25$ °C, unless otherwise specified)

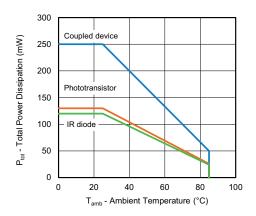


Fig. 4 - Total Power Dissipation vs. Ambient Temperature

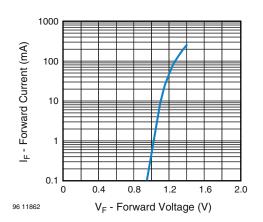


Fig. 5 - Forward Current vs. Forward Voltage

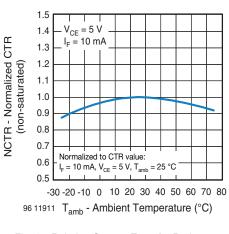


Fig. 6 - Relative Current Transfer Ratio vs. Ambient Temperature

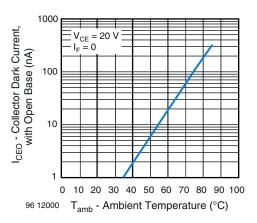
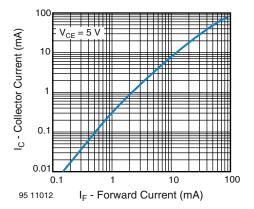


Fig. 7 - Collector Dark Current vs. Ambient Temperature

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Fig. 8 - Collector Current vs. Forward Current

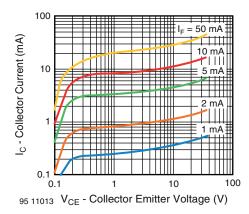


Fig. 9 - Collector Current vs. Collector Emitter Voltage

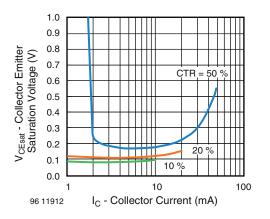


Fig. 10 - Collector Emitter Saturation Voltage vs. Collector Current

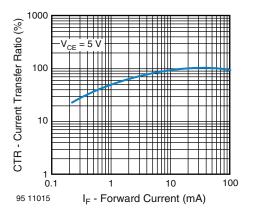


Fig. 11 - Current Transfer Ratio vs. Forward Current

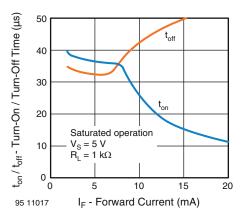


Fig. 12 - Turn-On / Turn-Off Time vs. Forward Current

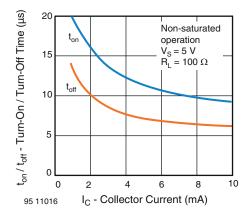


Fig. 13 - Turn-On / Turn-Off Time vs. Collector Current

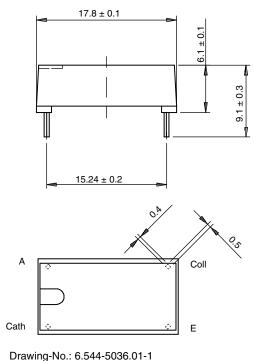
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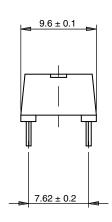
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### **PACKAGE DIMENSIONS** (in millimeters)







Weight: ca. 1.40 g

Issue: 2; 10.11.98 14763

### PACKAGE MARKING (example of CNY65BExi)



Fig. 14 - Top Marking

Theresienstrasse 2 74072 Heilbronn, Germany

Fig. 15 - Side Marking

TUBE INFORMATION							
ТҮРЕ	UNITS/TUBE	TUBES/BOX	UNITS/BOX				
CNY65Exi	30	35	1050				

# **CNY65Exi**



### **Vishay Semiconductors**

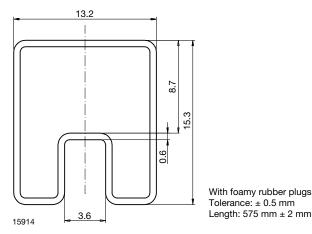


Fig. 16 - CNY65Exi

#### SOLDER PROFILES

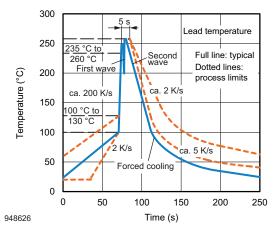


Fig. 17 - Wave Soldering Double Wave Profile According to J-STD-020 for Through-Hole Devices

#### HANDLING AND STORAGE CONDITIONS

ESD level: HBM class 2 Floor life: unlimited Conditions:  $T_{amb} < 30$  °C, RH < 85 % Moisture sensitivity level 1, according to J-STD-020



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