



### Typical Applications

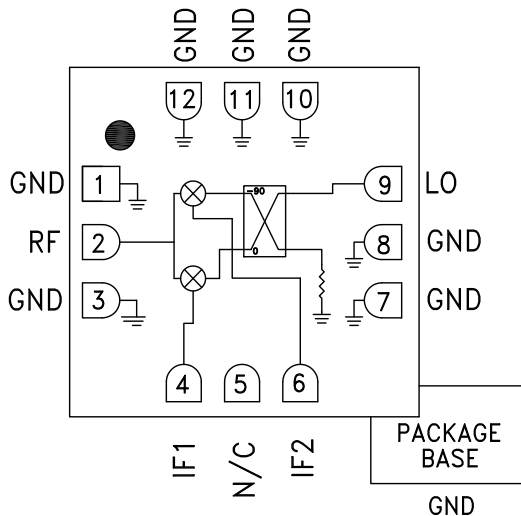
The HMC524LC3B is ideal for:

- Point-to-Point Radios
- Point-to-Multi-Point Radios & VSAT
- Test Equipment & Sensors
- Military End Use

### Features

- Wide IF Bandwidth: DC - 4.5 GHz
- Image Rejection: 20 dB
- LO to RF Isolation: 45 dB
- High Input IP3: +20 dBm
- 12 Lead 3x3mm SMT Package: 9mm<sup>2</sup>

### Functional Diagram



### General Description

The HMC524LC3B is a compact I/Q MMIC mixer which can be used as either an Image Reject Mixer or a Single Sideband Upconverter. The chip utilizes standard Hittite mixer cells and a 90 degree hybrid fabricated in a GaAs MESFET process. A low frequency quadrature hybrid was used to produce a 100 MHz USB IF output. This product is a much smaller alternative to hybrid style Image Reject mixers and single sideband upconverter assemblies. The HMC524LC3B eliminates the need for wirebonding, allowing the use of surface mount techniques.

### Electrical Specifications, $T_A = +25\text{ }^\circ\text{C}$ , IF = 100 MHz, LO = +17 dBm\*

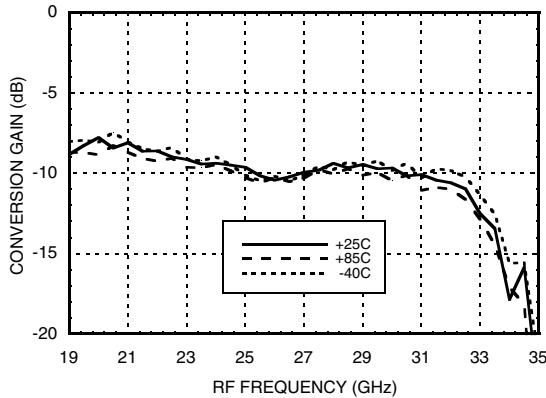
Parameter	Min.	Typ.	Max.	Units
Frequency Range, RF/LO	22 - 32			GHz
Frequency Range, IF	DC - 4.5			GHz
Conversion Loss (As IRM)		10	13	dB
Image Rejection	18	20		dB
1 dB Compression (Input)		+16		dBm
LO to RF Isolation	35	40		dB
LO to IF Isolation	20	30		dB
IP3 (Input)		+20		dBm
Amplitude Balance		$\pm 0.5$		dB
Phase Balance		$\pm 2.5$		Deg

\* Unless otherwise noted, all measurements performed as downconverter.

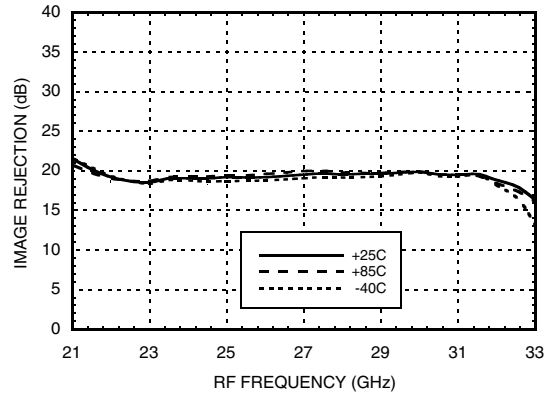


Data Taken As IRM With External IF Hybrid

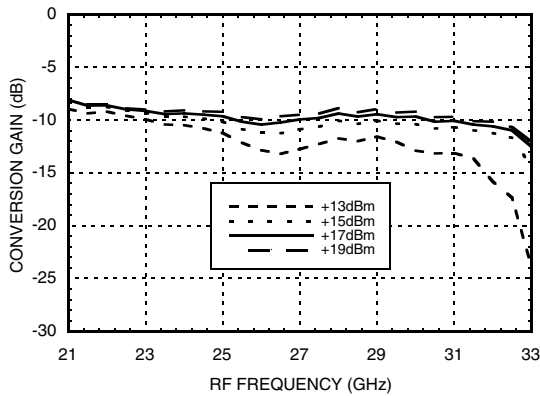
**Conversion Gain vs. Temperature**



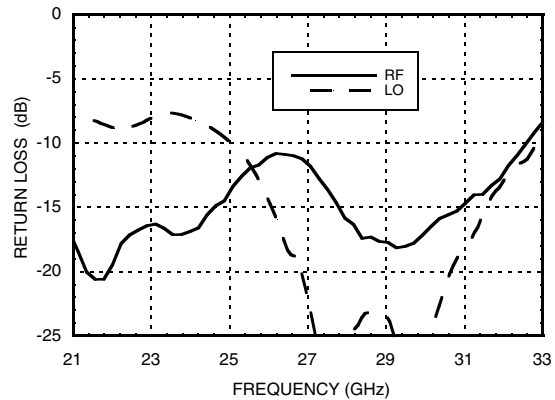
**Image Rejection vs. Temperature**



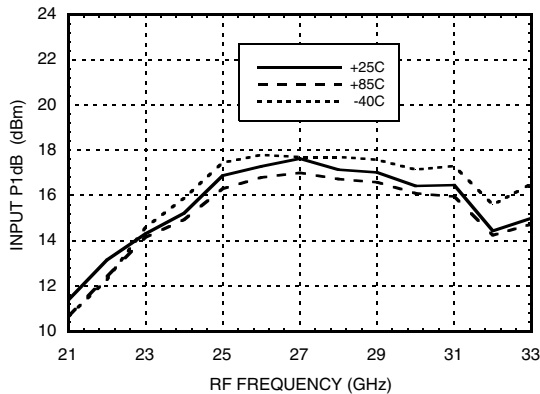
**Conversion Gain vs. LO Drive**



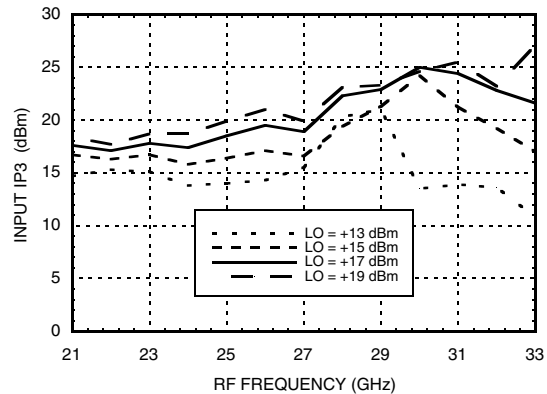
**Return Loss**



**Input P1dB vs. Temperature**



**Input IP3 vs. LO Drive**



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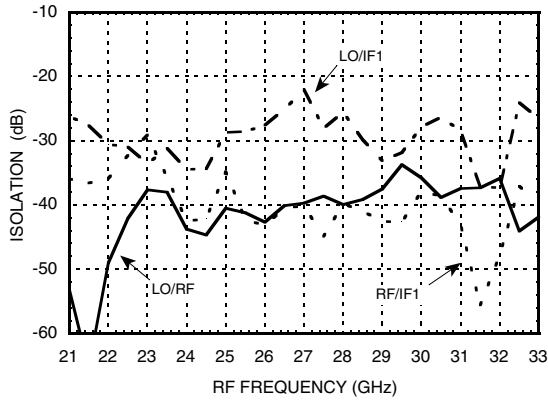
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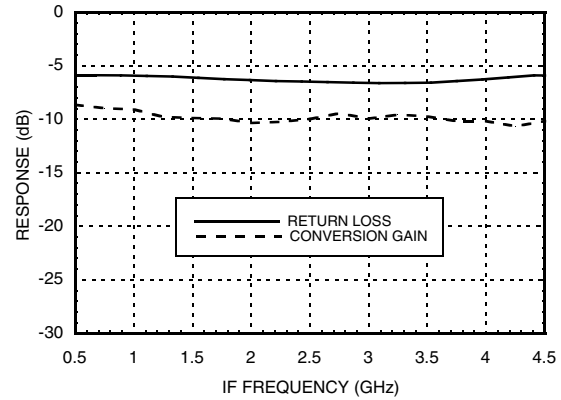
**GaAs MMIC I/Q MIXER  
22 - 32 GHz**

**Data Taken As IRM With External IF Hybrid**

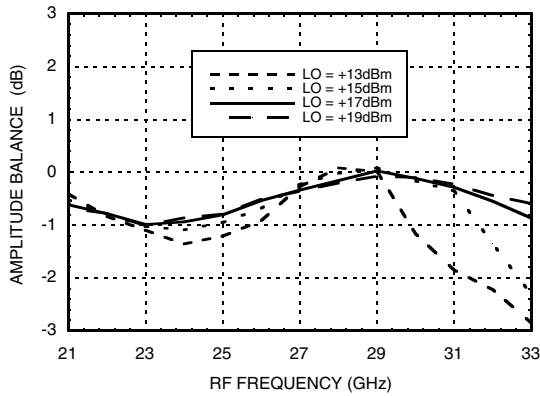
**Isolations**



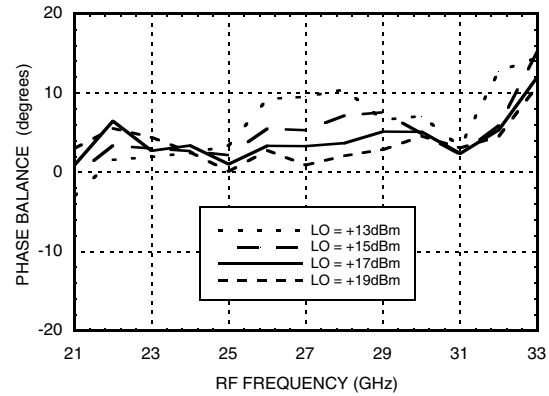
**IF Bandwidth**



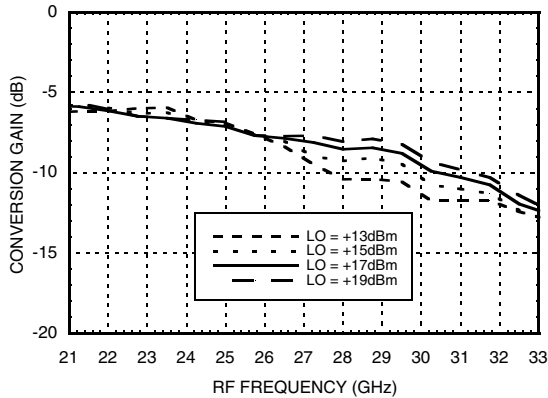
**Amplitude Balance vs. LO Drive**



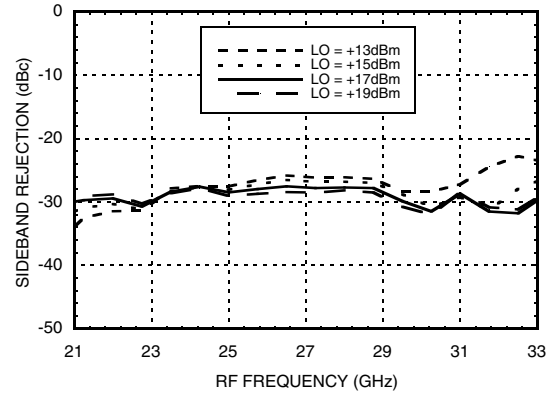
**Phase Balance vs. LO Drive**



**Upconverter Performance  
Conversion Gain vs. LO Drive**



**Upconverter Performance  
Sideband Rejection vs. LO Drive**




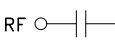
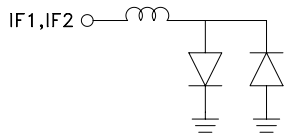
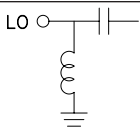
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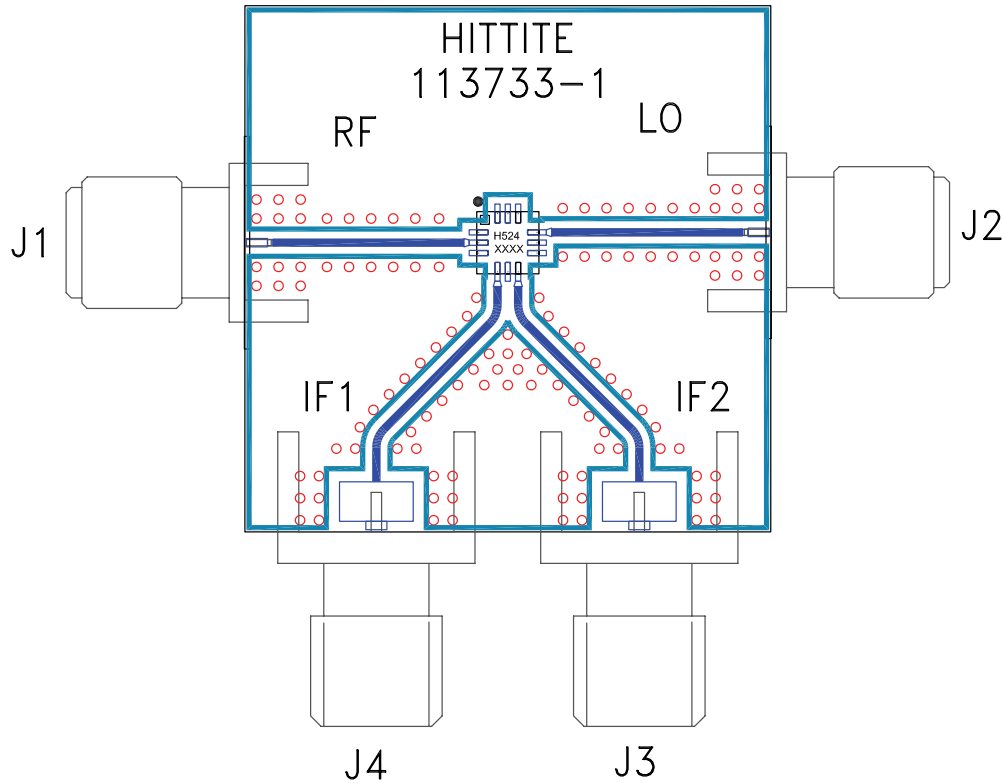


### Pin Descriptions

Pin Number	Function	Description	Interface Schematic
1, 3, 7, 8, 10, 11, 12	GND	The backside of the die must be connected to RF/DC ground.	
2	RF	This pad is AC coupled and matched to 50 Ohms.	
4	IF1	This pad is DC coupled. For applications not requiring operation to DC, this port should be DC blocked externally using a series capacitor whose value has been chosen to pass the necessary IF frequency range. For operation to DC, this pad must not source/sink more than 3mA of current or die non-function and possible die failure will result.	
6	IF2		
5	N/C	No connection required. These pins may be connected to RF/DC ground without affecting performance	
9	LO	This pad is DC coupled and matched to 50 Ohms.	



**Evaluation PCB**



**List of Materials for Evaluation PCB 109998 [1]**

Item	Description
J1, J2	PCB Mount K RF Connector, SRI
J3 - J4	PCB Mount SMA Connector, Johnson
U1	HMC524LC3B Mixer
PCB [2]	109996 Evaluation Board

[1] Reference this number when ordering complete evaluation PCB

[2] Circuit Board Material: Rogers 4350

The circuit board used in the application should use RF circuit design techniques. Signal lines should have 50 Ohm impedance while the package ground leads and exposed paddle should be connected directly to the ground plane similar to that shown. A sufficient number of via holes should be used to connect the top and bottom ground planes. The evaluation circuit board shown is available from Hit-tite upon request.