

DUAL N-CANNEL ENHANCEMENT MODE MOSFET
Product Summary

BV _{DSS}	R _{DS(ON)} MAX	I _D MAX T _A = +25°C
20V	28mΩ @ V _{GS} = 4.5V	5.8A
	32mΩ @ V _{GS} = 2.5V	5.4A

Description

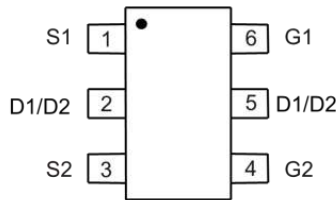
This new generation MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

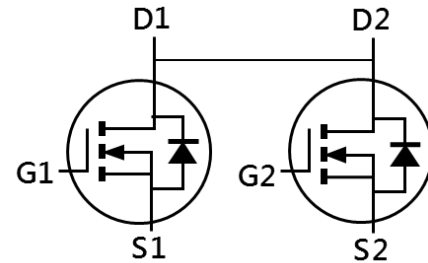
- Backlighting
- DC-DC Converters
- Power Management Functions



TSOT26



Top View



Equivalent Circuit

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please [contact us](mailto:contact@diodes.com) or your local Diodes representative. <https://www.diodes.com/quality/product-definitions/>**

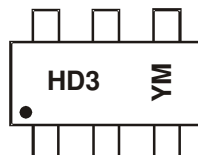
Mechanical Data

- Case: TSOT26
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See Diagram
- Terminals: Finish – Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 e3
- Weight: 0.013 grams (Approximate)

Ordering Information (Note 4)

Part Number	Case	Packaging
DMN2041UVT-7	TSOT26	3000 / Tape & Reel
DMN2041UVT-13	TSOT26	10000 / Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information


HD3 = Product Type Marking Code
 YM = Date Code Marking
 Y or \bar{Y} = Year (ex: I = 2021)
 M = Month (ex: 9 = September)

Date Code Key

Year	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	G	H	I	J	K	L	M	N	O	P	R	S

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage		V _{DSS}	20	V	
Gate-Source Voltage		V _{GSS}	±8	V	
Continuous Drain Current (Note 6) V _{GS} = 4.5V	Steady State	I _D	T _A = +25°C	5.8	A
			T _A = +70°C	4.6	
Maximum Continuous Body Diode Forward Current (Note 6)		I _S	1.3	A	
Pulsed Drain Current (10μs Pulse, Duty Cycle = 1%)		I _{DM}	36	A	

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		P _D	1.1	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	R _{θJA}	113	°C/W
Total Power Dissipation (Note 6)		P _D	1.44	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R _{θJA}	87	°C/W
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	20	—	—	V	V _{GS} = 0V, I _D = 250μA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	1.0	μA	T _J = +25°C, V _{DS} = 20V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±100	nA	V _{GS} = ±8V, V _{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	0.4	—	0.9	V	V _{DS} = V _{GS} , I _D = 250μA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	17	28	mΩ	V _{GS} = 4.5V, I _D = 8.2A
			22	32		V _{GS} = 2.5V, I _D = 3.3A
			32	40		V _{GS} = 1.8V, I _D = 2.0A
Diode Forward Voltage	V _{SD}	—	0.7	0.9	V	V _{GS} = 0V, I _D = 2.25A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iSS}	—	689	—	pF	V _{DS} = 10V, V _{GS} = 0V f = 1.0MHz
Output Capacitance	C _{oss}	—	89	—	pF	
Reverse Transfer Capacitance	C _{rSS}	—	79	—	pF	
Gate Resistance	R _g	—	1.05	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Total Gate Charge	Q _g	—	9.1	—	nC	V _{GS} = 4.5V, V _{DS} = 10V, I _D = 8.2A
Gate-Source Charge	Q _{gs}	—	0.3	—	nC	
Gate-Drain Charge	Q _{gd}	—	2.1	—	nC	
Turn-On Delay Time	t _{D(ON)}	—	9	—	ns	V _{DS} = 10V, V _{GS} = 4.5V, R _L = 10Ω, R _g = 6Ω, I _D = 1A
Turn-On Rise Time	t _r	—	21	—	ns	
Turn-Off Delay Time	t _{D(OFF)}	—	32	—	ns	
Turn-Off Fall Time	t _f	—	17	—	ns	

- Notes:
- Device mounted on FR-4 PCB, with minimum recommended pad layout.
 - Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. copper, single sided.
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to product testing.

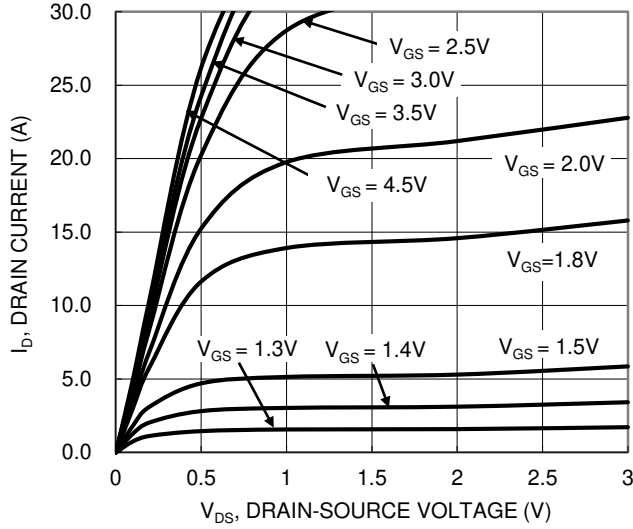


Figure 1. Typical Output Characteristic

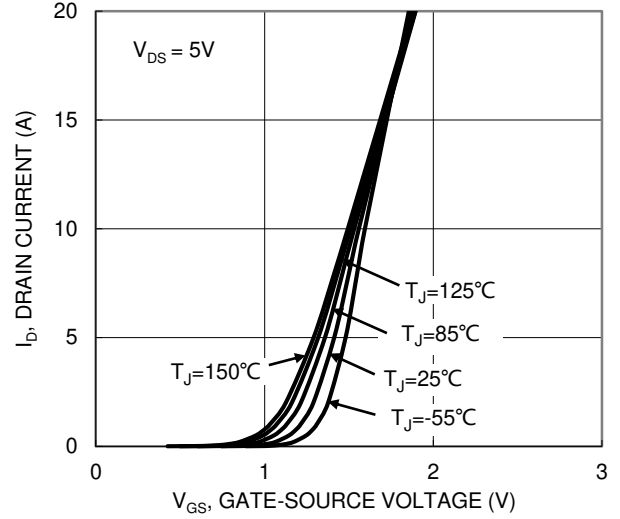


Figure 2. Typical Transfer Characteristic

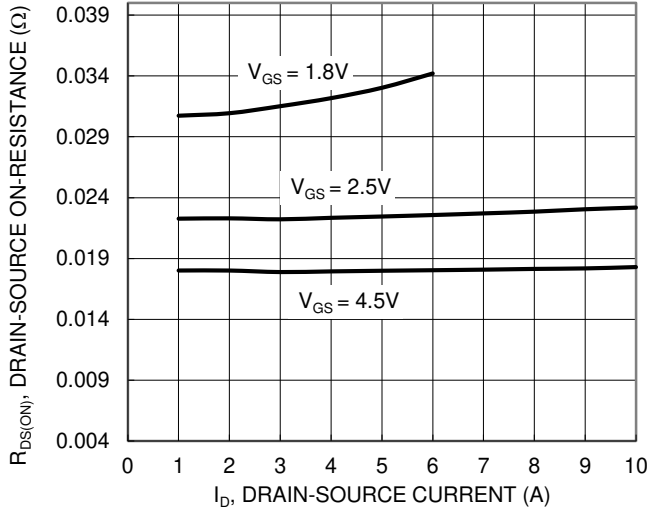


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

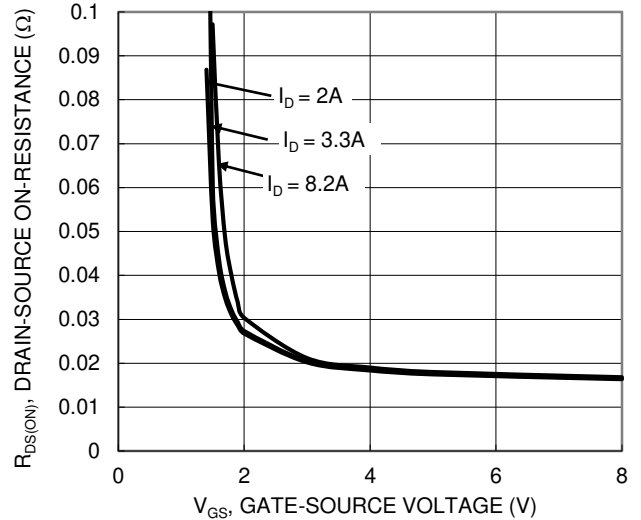


Figure 4. Typical Transfer Characteristic

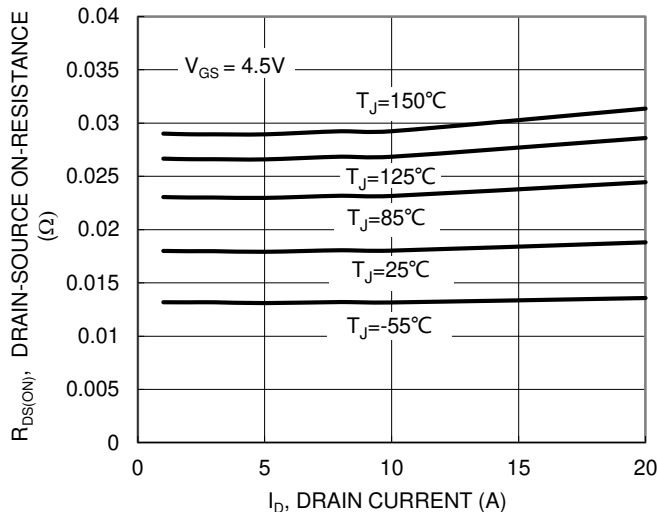


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

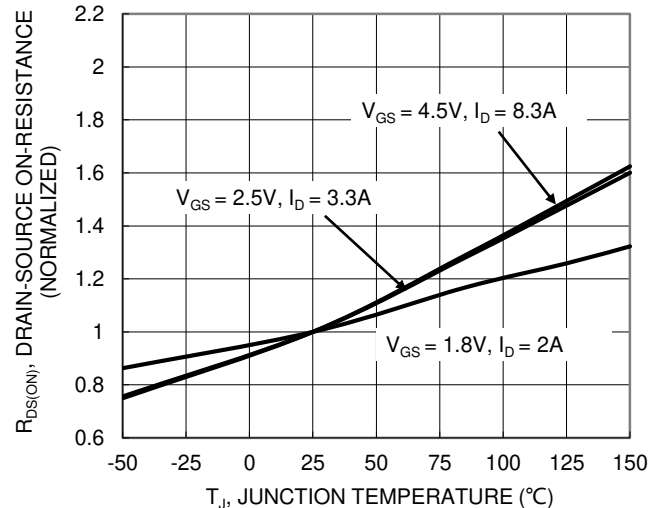


Figure 6. On-Resistance Variation with Junction Temperature

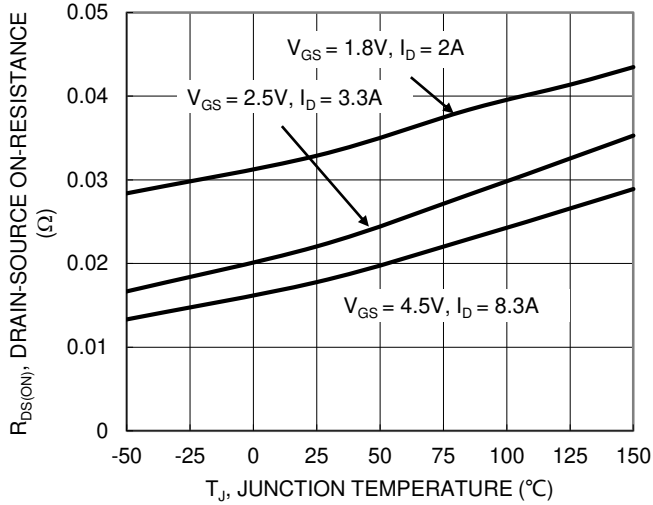


Figure 7. On-Resistance Variation with Junction Temperature

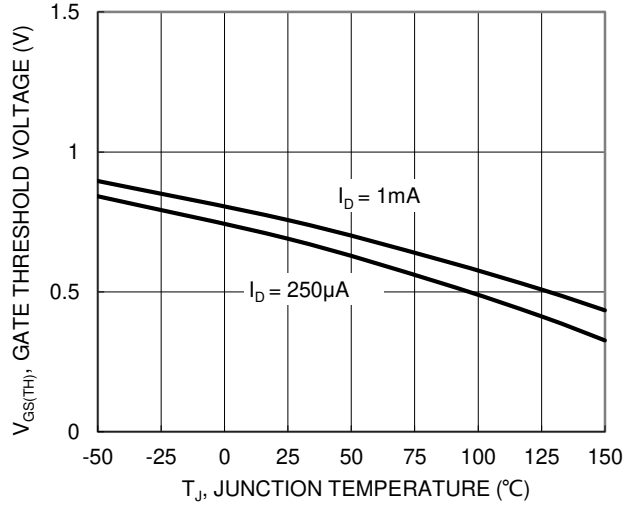


Figure 8. Gate Threshold Variation vs. Junction Temperature

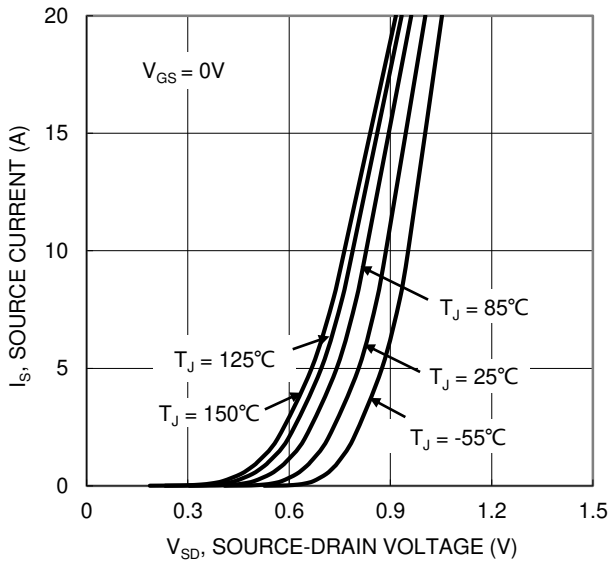


Figure 9. Diode Forward Voltage vs. Current

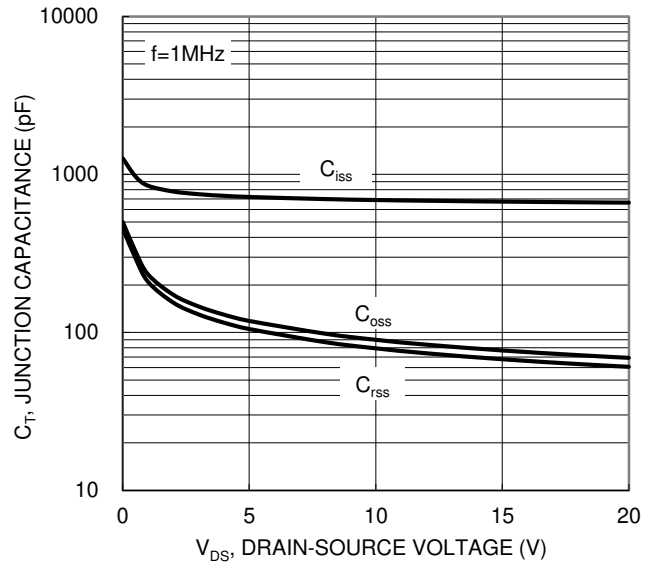


Figure 10. Typical Junction Capacitance

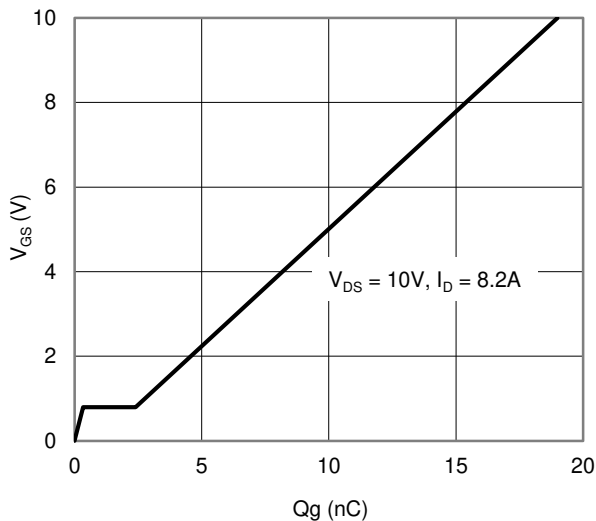


Figure 11. Gate Charge

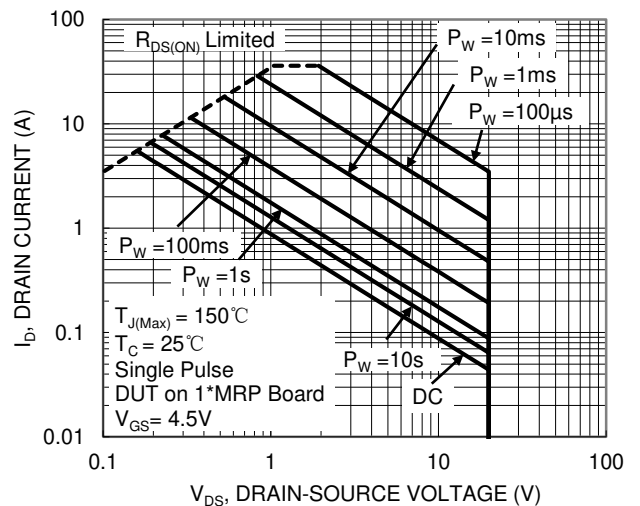


Figure 12. SOA, Safe Operation Area

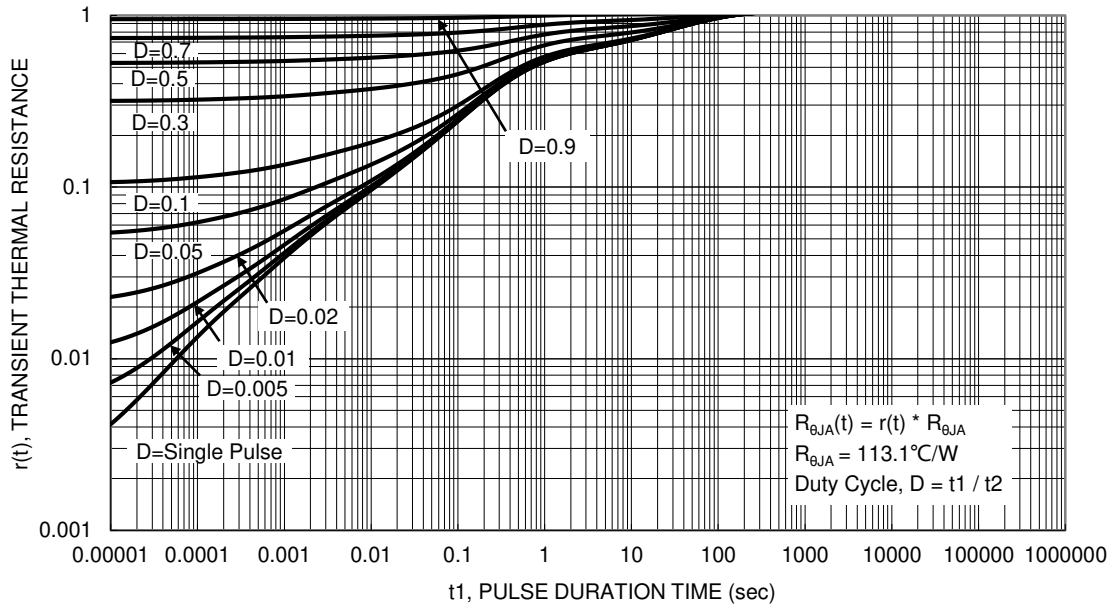
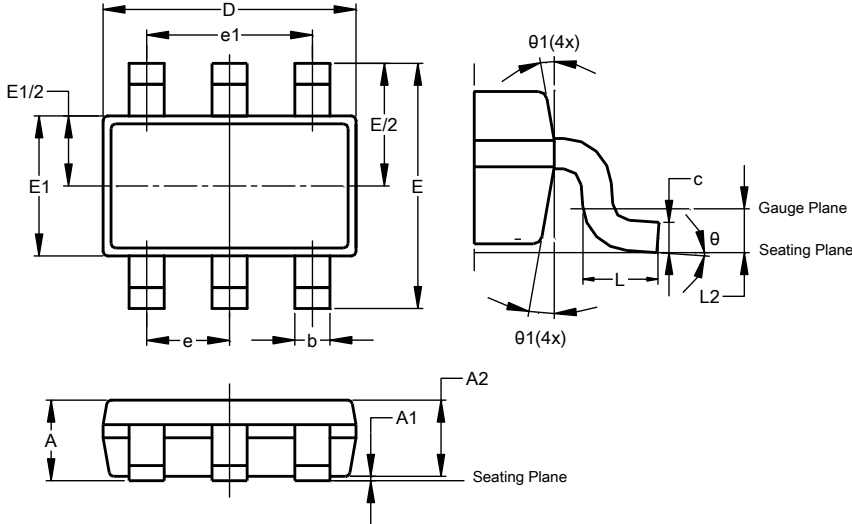


Figure 13. Transient Thermal Resistance

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

TSOT26

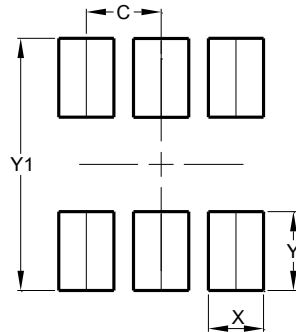


TSOT26			
Dim	Min	Max	Typ
A	-	1.00	-
A1	0.010	0.100	-
A2	0.840	0.900	-
D	2.800	3.000	2.900
E	2.800 BSC		
E1	1.500	1.700	1.600
b	0.300	0.450	-
c	0.120	0.200	-
e	0.950 BSC		
e1	1.900 BSC		
L	0.30	0.50	-
L2	0.250 BSC		
θ	0°	8°	4°
θ 1	4°	12°	-
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

TSOT26



Dimensions	Value (in mm)
C	0.950
X	0.700
Y	1.000
Y1	3.200

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