

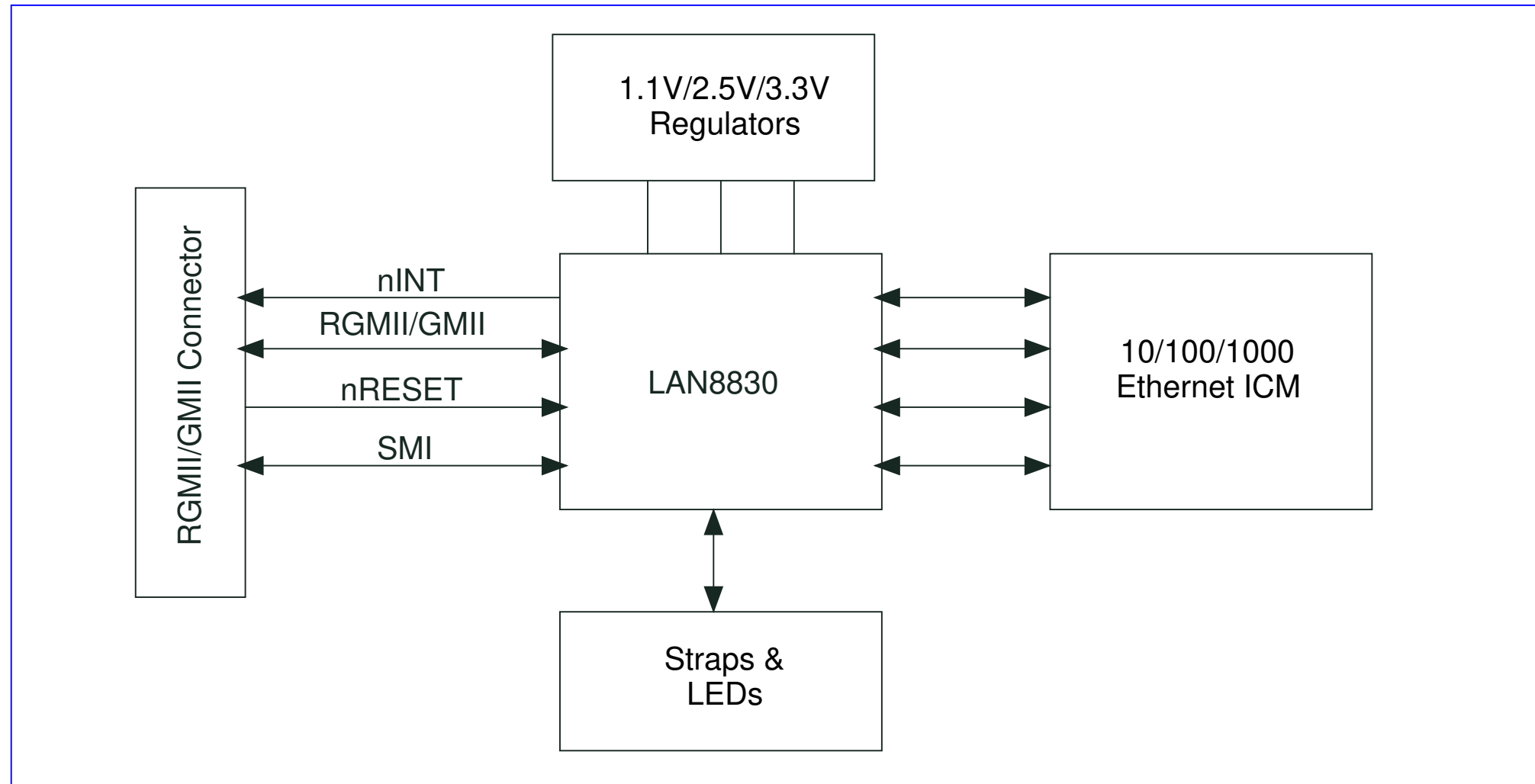
# EVB-LAN8830 Gigabit PHY Daughter Card



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Sheet	Description
1	Title Sheet
2	LAN8830 PHY
3	LAN8830 Power
4	LAN8830 RGMII Interface

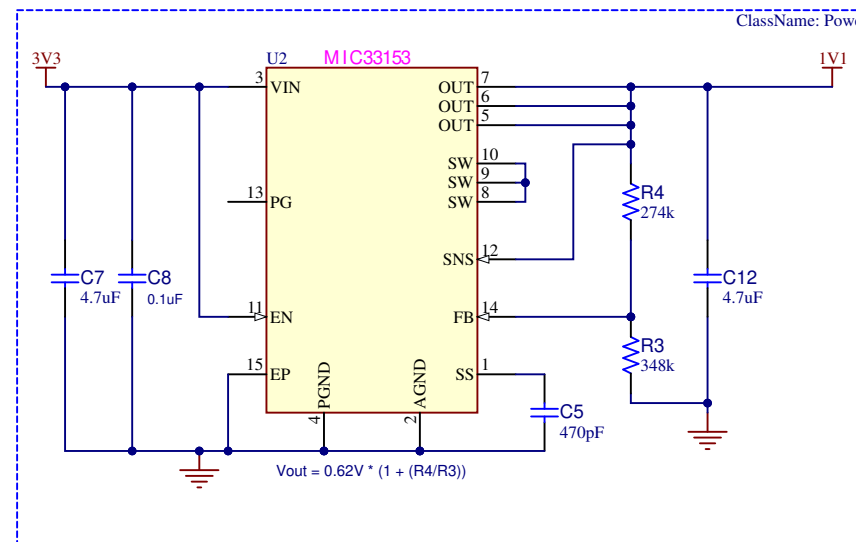
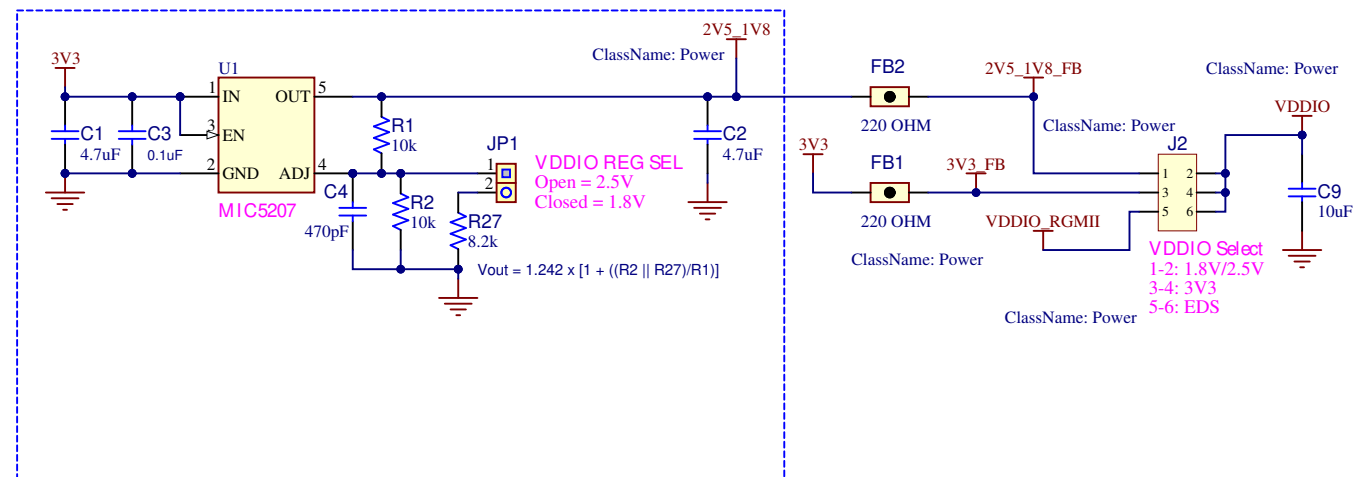
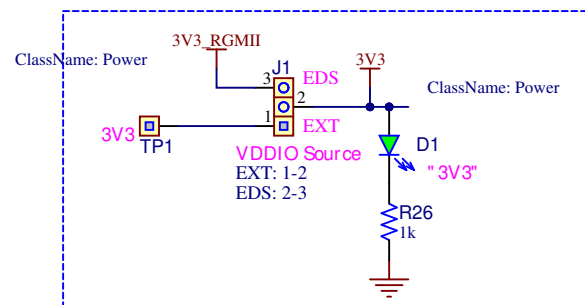
## Revision History

Revision	Date	Revision Summary	Author
A	07/29/2022	Initial release	John MacKay



Project Owner: J. MacKay			
PCB Layout Contact: J. MacKay			
PartNumber: EV03V90A	Project Title EVB-LAN8830 Gigabit Ethernet Plug-In Card	Variant: [No Variations]	
Sheet Title Title			Designed with 
Size B	SCH #: 03-00622 PCB #: 04-11714	Rev: 1 Rev: 1	Date: 9/21/2022 Sheet 1 of 3
File: 1_Title.SchDoc			





LABEL1

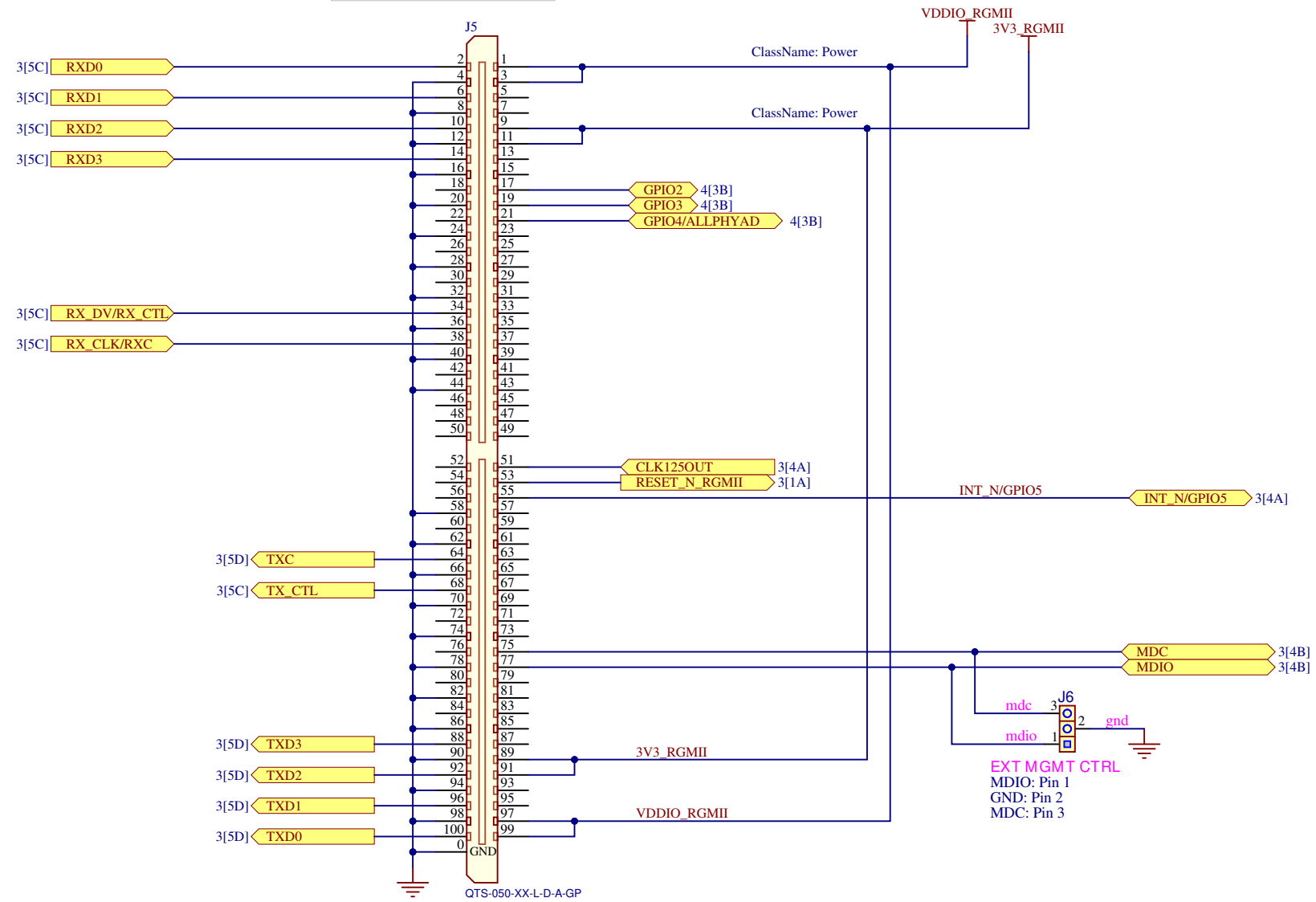
Product number/revision  
Serial number

PCBA LABEL 18X6mm

Fiducials

Project Owner: J. MacKay			
PCB Layout Contact: J. MacKay			
PartNumber: EV03V90A	Project Title EVB-LAN8830 Gigabit Ethernet Plug-In Card	Variant: [No Variations]	
Sheet Title LAN8830 Power		Designed with 	
Size B	SCH #: 03-00622	Rev: 1	Date: 9/21/2022
	PCB #: 04-11714	Rev: 1	Sheet 3 of 4
File: 3_Power.SchDoc			

PCB Layout Constraint: The RGMII TXC – TX signals & RXC – RX signals must be trace length matched between the DUT and the Hi-Speed RGMII connector.



Hi-Speed RGMII Connector mounted on bottom side.

Drawn By: J. MacKay			
Engineer: J. MacKay			
PartNumber: EV03V90A	Project Title EVB-LAN8830 Gigabit Ethernet Plug-In Card	Variant: [No Variations]	
Sheet Title LAN8830 RGMII Interface		Designed with 	
Size B	SCH #: 03-00622	Rev: 1	Date: 9/21/2022
	PCB #: 04-11714	Rev: 1	Sheet 4 of 4
File: 4_RGMII.SchDoc			