

NOT RECOMMENDED FOR NEW DESIGN USE DMN62D0UW





N-CHANNEL ENHANCEMENT MODE MOSFET

Features

- Low On-Resistance
- Very Low Gate Threshold Voltage (1.0V Max)
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected up to 2kV
- Totally Lead-Free & Fully RoHS compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.

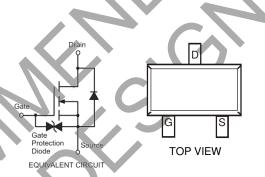
https://www.diodes.com/quality/product-definitions/

 An Automotive-Compliant Part is Available Under Separate Datasheet (DMN5L06WKQ)

Mechanical Data

- Package: SOT-323
- Package Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin annealed over Alloy 42 leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Terminal Connections: See Diagram
- Weight: 0.006 grams (Approximate)





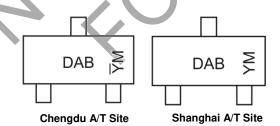
Ordering Information (Note 4)

Part Number	Pankana	Packing		
Part Number	Package	Qty.	Carrier	
DMN5L06WK-7	SOT-323	3000	Tape & Reel	

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



DAB = Product Type Marking Code

YM = Date Code Marking for SAT (Shanghai Assembly/ Test site)

 $\overline{Y}M$ = Date Code Marking for CAT (Chengdu Assembly/ Test site)

Y or \overline{Y} = Year (ex: J = 2022)

M = Month (ex: 6 = June)

Date Code Key

Year	2006		2022	2023	2024	2025	2026	2027	2028	2029	2030	2031
Code	T		J	K	L	M	N	0	Р	R	S	T
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec



Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain Source Voltage	V _{DSS}	50	V
Gate-Source Voltage	V _{GSS}	±20	V
Drain Current (Note 5) Continuous Pulsed (Note 6)	lo	300 800	mA

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P_{D}	250	mW
Thermal Resistance, Junction to Ambient	$R_{ heta JA}$	500	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-65 to +150	°C

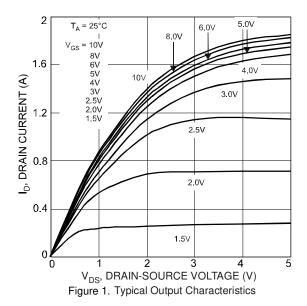
Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

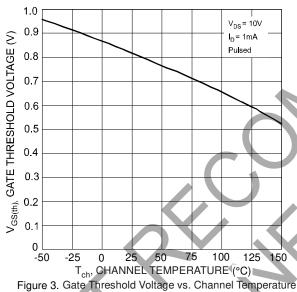
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BVDSS	50			٧	$V_{GS} = 0V, I_{D} = 10\mu A$
Zero Gate Voltage Drain Current $@T_C = +25^{\circ}C$	IDSS	_	_	60	nA	$V_{DS} = 50V$, $V_{GS} = 0V$
		7		1	μA	$V_{GS} = \pm 12V$, $V_{DS} = 0V$
Gate-Body Leakage	Igss	_		500	nA	$V_{GS} = \pm 10V$, $V_{DS} = 0V$
				50	nA	$V_{GS} = \pm 5V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(th)}	0.49		1.0	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$
)	_		3.0	Ω	$V_{GS} = 1.8V, I_D = 50mA$
Static Drain-Source On-Resistance	RDS(on)			2.5 2.0		$V_{GS} = 2.5V, I_{D} = 50mA$
						$V_{GS} = 5.0V, I_{D} = 50mA$
On-State Drain Current	ID(ON)	0.5	1.4	_	Α	$V_{GS} = 10V, V_{DS} = 7.5V$
Forward Transconductance	Y _{fs}	200	_	_	mS	$V_{DS} = 10V, I_{D} = 0.2A$
Source-Drain Diode Forward Voltage	V _{SD}	0.5		1.4	V	$V_{GS} = 0V, I_S = 115mA$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss		_	50	pF	\/ OF\/ \/ O\/
Output Capacitance	Coss		_	25	pF	V _{DS} = 25V, V _{GS} = 0V, f = 1.0MHz
Reverse Transfer Capacitance	Crss	1	_	5.0	pF	1 = 1.0IVII IZ
Turn-On Delay Time	tD(on)		2.1	_	ns	
Turn-On Rise Time	tr	_	1.8	_	ns	V _{DD} = 30V, V _{GS} = 10V,
Turn-Off Delay Time	tD(off)		14.4		ns	$R_G = 25\Omega$, $I_D = 200mA$
Turn-Off Fall Time	tf	_	8.4		ns	

Notes:

5. Device mounted on FR-4 PCB.
6. Pulse width ≤ 10µs, Duty Cycle ≤ 1%.
7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to production testing.







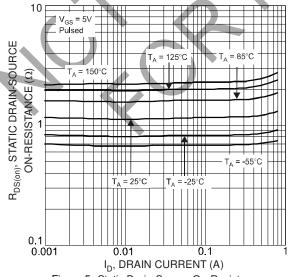
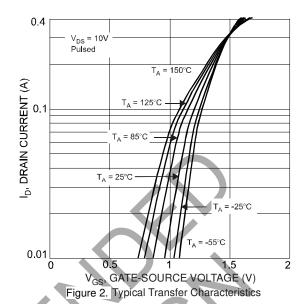


Figure 5. Static Drain-Source On-Resistance vs. Drain Current



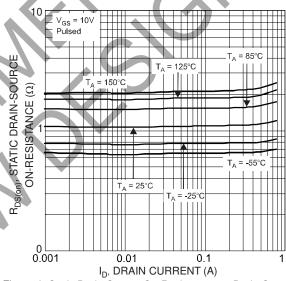
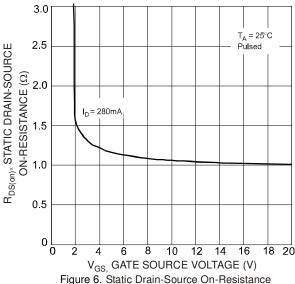


Figure 4. Static Drain-Source On-Resistance vs. Drain Current



vs. Gate-Source Voltage



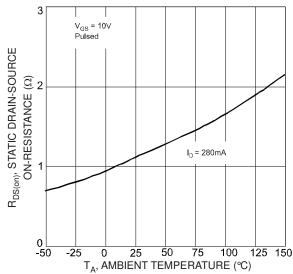


Figure 7. Static Drain-Source On-State Resistance vs. Ambient Temperature

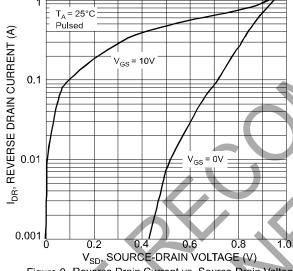
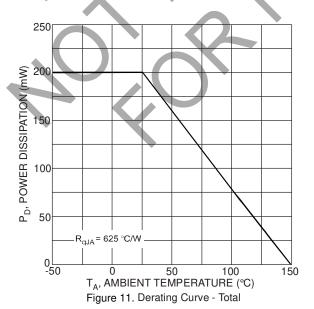
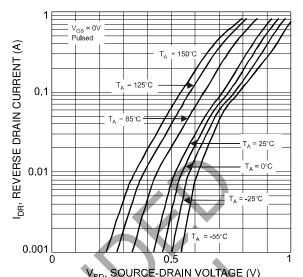


Figure 9. Reverse Drain Current vs. Source-Drain Voltage





V_{SD}, SOURCE-DRAIN VOLTAGE (V) Figure 8. Reverse Drain Current vs. Source-Drain Voltage

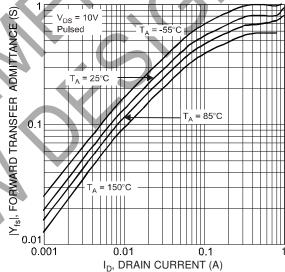


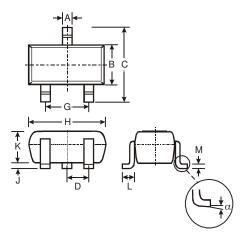
Figure 10. Forward Transfer Admittance vs. Drain Current



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT-323

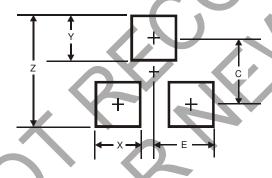


SOT-323						
Dim	Min	Max	Тур			
Α	0.25	0.40	0.30			
В	1.15	1.35	1.30			
С	2.00	2.20	2.10			
D	-	-	0.65			
G	1.20	1.40	1.30			
Н	1.80	2.20	2.15			
J	0.0	0.10	0.05			
K	0.90	1.00	0.95			
L	0.25	0.40	0.30			
M	0.10	0.18	0.11			
α	0°	8°				
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT-323



Dimensions	Value (in mm)
Z	2.8
Х	0.7
Υ	0.9
С	1.9
	10



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