

	500 250	1000 +10/-0 500 +10/-0	2349838-2 2349838-1
	(" M ")	"L"	TE P/N
THIS DRAWING IS A CONTROLLED DOCUMEN DIMENSIONS: TOLERANCES UNLESS OTHERWISE SPECIFIE	<u>I.SMIIH</u> снк 09 JAN 2019	≤ TE	TE Connectivity
OTHERWISE SPECIFIE 0 PLC ±- 1 PLC ±0.5 2 PLC ±0.13 3 PLC ±0.013	PRODUCT SPEC	SLIVER 2.0 2C 33AV STRAIGHT TO RIGHT	
4 PLC ±0.0001 ANGLES ±- MATERIAL FINISH		ze cage code drawing no 200779 C=2349838	RESTRICTED TO
	CUSTOMER DRAWING	SCALE	1:1 SHEET 1 OF 3 REV A

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ſ	THIS DRAWING IS UNPUBLISHED.	RELEASED FOR PUBLICATION	20		1
	C COPYRIGHT 20 BY -	ALL RIGHTS RESERVED.			

A1 GROUND DISCRETE GROUND A2 GROUND DISCRETE GROUND A3 A3 GROUND DISCRETE GROUND A4 A4 GROUND DISCRETE GROUND A4 A4 GROUND DISCRETE GROUND A4 A5 GROUND DISCRETE GROUND A5 A6 GROUND DISCRETE SIDE 3AND A5 A7 SIDE 3AND DISCRETE SIDE 3AND A5 A1 SIDE 3AND DISCRETE SIDE 3AND A7 <			P1				P2	
A2 CROUND DISCRETE GROUND DISCRETE GROUND A A3 CROUND DISCRETE GROUND DISCRETE GROUND A A4 GROUND DISCRETE GROUND A A A A5 CROUND DISCRETE GROUND A A A A7 SIDE BAND DISCRETE SIDE BAND A A A A8 SIDE BAND DISCRETE SIDE BAND A A A A10 SIDE BAND DISCRETE SIDE BAND A A A A11 SIDE BAND DISCRETE SIDE BAND A A A A12 SIDE BAND DISCRETE SIDE BAND A <td< th=""><th></th><th>PIN NO.</th><th>PAIR NO.</th><th>DESIGNATION</th><th>WIRE TYPE</th><th>DESIGNATION</th><th>PAIR NO.</th><th>PIN NO.</th></td<>		PIN NO.	PAIR NO.	DESIGNATION	WIRE TYPE	DESIGNATION	PAIR NO.	PIN NO.
A2 GROUND DISCREFF GROUND A3 GROUND DISCREFF GROUND A A4 GROUND DISCREFF GROUND A A5 GROUND DISCREFF GROUND A A6 GROUND DISCREFF GROUND A A6 GROUND DISCREFF SIDE DAND A A7 SIDE BAND DISCREFF SIDE DAND A A8 SIDE BAND DISCREFF SIDE DAND A A10 SIDE BAND DISCREFF SIDE DAND A A11 CL SIDE BAND DISCREFF SIDE DAND <	р	— A 1		GROUND	DISCRETE	GROUND		A 1
A4 GROUND DISCRIFE GROUND A5 GROUND DISCRIFE GROUND A6 GROUND DISCRIFE GROUND A7 SIDF BAND DISCRIFE SIDF BAND A8 SIDE DAND DISCRIFE SIDF BAND A9 SIDE DAND DISCRIFE SIDE DAND A10 SIDE DAND DISCRIFE SIDE DAND A11 CLK DIFF PAIR CLK A11 CLK DIFF PAIR CLK A11 OPCIETX DIFF PAIR PCIETX A11 OPCIETX DIFF PAIR PCIETX A20 PCIETX DIFF PAIR PCIETX A22 GMD	-	— A 2		GROUND	DISCRETE	GROUND		A 2
A2 OROUND DISCRIPT GROUND A5 GROUND DISCRIPT GROUND A6 GROUND DISCRIPT GROUND A7 SIDE GAND DISCRIPT GROUND A8 SIDE GAND DISCRIPT SIDE BAND A9 SIDE GAND DISCRIPT SIDE BAND A10 S.DE GAND DISCRIPT SIDE BAND A11 SIDE GAND DISCRIPT SIDE BAND A12 SIDE GAND DISCRIPT SIDE BAND A13 GND GNS GNS A14 CLK DISCRIPT SIDE BAND A13 GND GNS GNS A14 CLK DIFT PAIR CLK A15 CLK DIF PAIR CLK A16 GND GNS GNS A18 PCLE TX DIFF PAIR PCLE X A20 PCLE TX DIFF PAIR PCLE X A22 PCLE TX DIFF PAIR PCLE X		— A 3		GROUND	DISCRETE	GROUND		A 3
A6 GROUND DISCRETE GROUND A7 SIDE BAND DISCRETE SIDE BAND A7 SIDE BAND DISCRETE SIDE BAND A9 SIDE BAND DISCRETE SIDE BAND A10 SIDE BAND DISCRETE SIDE BAND A11 SIDE BAND DISCRETE SIDE BAND A12 SIDE BAND DISCRETE SIDE BAND A13 GND GMD GMD A14 CLK DIFF PAIR CLK A15 CLK DIFF PAIR CLK A16 GND GND GND A17 C PCIETX DIFF PAIR PCIETX A18 GND GND GND GND A18 A18 GND GND GND GND A22 GND GND A22 GOND GND GND GND A23 2 PCIETX DIFF PAIR PCIETX 2 A24 PCIETX <td< td=""><td></td><td>— A 4</td><td></td><td>GROUND</td><td>DISCRETE</td><td>GROUND</td><td></td><td>A 4</td></td<>		— A 4		GROUND	DISCRETE	GROUND		A 4
A7 SIDE BAND DISCRETE SIDE BAND A8 SIDE UAND DISCRETE SIDE DAND A9 SIDE BAND DISCRETE SIDE BAND A10 SIDE BAND DISCRETE SIDE BAND A11 SIDE BAND DISCRETE SIDE BAND A11 SIDE BAND DISCRETE SIDE BAND A11 SIDE BAND DISCRETE SIDE BAND A13 GND OND OND A13 GND GND SND A13 GND SND GND A14 CLK DIFF PAIR CLK A15 CLK DIFF PAIR CLK A16 GND SND GND A17 O PCIeTX DIFF PAIR PCIeTX A18 O PCIeTX DIFF PAIR PCIeTX 1 A20 1 PCIeTX DIFF PAIR PCIeTX 1 A21 PCIeTX DIFF PAIR PCIeTX 2	-	— A 5		GROUND	DISCRETE	GROUND		A 5
A8 SIDE BAND DISCRETE SIDE BAND A9 SIDE BAND DISCRETE SIDE BAND A10 SIDE BAND DISCRETE SIDE BAND A11 SIDE BAND DISCRETE SIDE BAND A12 SIDE BAND DISCRETE SIDE BAND A13 GND GND GND A14 CLK DIFF PAIR CLK A15 CLK DIFF PAIR CLK A16 GND GND GND A17 PCIeTX DIFF PAIR PCIeX A16 GND GND GND A17 PCIeTX DIFF PAIR PCIeX A18 PCIeTX DIFF PAIR PCIeTX A19 GND GND GND A20 PCIeTX DIFF PAIR PCIeTX A22 GND GND GND A23 PCIeTX DIFF PAIR PCIeTX A24 PCIeTX DIFF PAIR PCIeTX A25<		— A 6		GROUND	DISCRETE	GROUND		A 6
A9 SIDL HAND DISCR_IL SIDL BAND SIDL BAND A10 SIDE BAND DISCRITE SIDE BAND AI A11 SIDE BAND DISCRITE SIDE BAND AI A12 SIDE BAND DISCRITE SIDE BAND AI A13 GND CNO CNO AI A14 CLK DIFF PAIR CL4 CL4 A15 CLK DIFF PAIR CL4 CL4 A16 GND GNO GNO CAI A17 O PCIeTX DIFF PAIR PCIeTX 0 A18 O PCIeTX DIFF PAIR PCIeTX 0 A17 O PCIeTX DIFF PAIR PCIeTX 0 A19 GND GND GND AI 0 A20 PCIeTX DIFF PAIR PCIeTX 1 A22 GND GND GND AI A24 PCIeTX DIFF PAIR PCIeTX		Α7		SIDE BAND	DISCRETE	SIDE BAND		A 7
AIO SIDE BAND DISCRETE SIDE BAND AI1 SIDE BAND DISCRETE SIDE BAND AI AI2 SIDE BAND DISCRETE SIDE BAND AI AI3 GVD GND GND AI AI3 GVD GND GND AI AI4 CLK DIFF PAIR CLK AI AI5 CI K DIFF PAIR CLK AI AI8 GND GND GND AI AI8 PCIeTX DIFF PAIR PCIeTX 0 AI9 GND GND GND AI A21 PCIeTX DIFF PAIR PCIeTX 1 A22 GND GND GND A23 A23 PCIeTX DIFF PAIR PCIeTX 1 A22 GND GND GND A23 A23 PCIeTX DIFF PAIR PCIeTX 2 A24 PCIeTX DIFF PAIR PCIeTX		A 8		SIDE BAND	DISCRETE	SIDE BAND		A 8
A11 SIDE BAND DISCRETE SIDE BAND A12 A12 SIDE BAND DISCRETE SIDE BAND A13 A13 GND GND A14 A13 GND GND A14 A14 CLK DIFF PAIR CLK A15 A14 CLK DIFF PAIR CLK A16 A16 GND GND A17 O PCLeTX DIFF PAIR PCLETX O A19 GND GND GND GND A19 A20 PCLETX DIFF PAIR PCLETX 1 A21 PCLETX DIFF PAIR PCLETX 1 A22 GND GND A23 2 PCLETX DIFF PAIR PCLETX 2 A24 2 PCLETX DIFF PAIR PCLETX 2 A25 GND GND A26 GND GND A26 GND A28 GND A28 GND A28 GND A30 A29 GND		A 9		SIDE BAND	DISCRETE	SIDE BAND		A 9
A12 SIDI 5AND DISCRETE SIDE BAND A13 GND GND GND A14 CLK DIFF PAIR CLK A15 CIK DIFF PAIR CLK A16 GND GND GND A17 0 PCIeTX DIFF PAIR PCIeTX A18 0 PCIeTX DIFF PAIR PCIeTX 0 A19 GND GND GND A19 A20 1 PCIeTX DIFF PAIR PCIeTX 1 A22 GND GND GND GND A23 2 PCIeTX DIFF PAIR PCIeTX 2 A22 GND GND GND GND A23 2 PCIeTX DIFF PAIR PCIeTX 2 A24 2 PCIeTX DIFF PAIR PCIeTX 3 3 A26 3 PCIeTX DIFF PAIR PCIeTX 3 A27 A28 GND GND GND		A 1 0		SIDE BAND	DISCRETE	SIDE BAND		A 1 0
A13 GND GND GND A14 CLK DIFF PAIR CLK A A15 CLK DIFF PAIR CLK A A15 CLK DIFF PAIR CLK A A16 GND GND A A O A17 0 PCLeTX DIFF PAIR PCLeTX O A18 0 PCLETX DIFF PAIR PCLETX O A19 GND GND GND A O A20 ' PCLETX DIFF PAIR PCLETX 1 A21 ' PCLETX DIFF PAIR PCLETX 1 A22 GND GND GND A A A23 2 PCLETX DIFF PAIR PCLETX 2 A24 2 PCLETX DIFF PAIR PCLETX 2 A24 PCLETX DIFF PAIR PCLETX 3 A A25 GND GN		A 1 1		SIDE BAND	DISCRETE	SIDE BAND		A 1 1
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A15 CLK DIFF PAIR CLK A16 GND GND GND A17 0 PCIeTX DIFF PAIR PCIeTX 0 A18 0 PCIeTX DIFF PAIR PCIeTX 0 A18 0 PCIeTX DIFF PAIR PCIeTX 0 A19 GND GND GND 0 0 A20 1 PCIeTX DIFF PAIR PCIeTX 1 A21 1 PCIeTX DIFF PAIR PCIeTX 1 A22 GND GND GND 0 0 A23 2 PCIeTX DIFF PAIR PCIeTX 2 A24 2 PCIeTX DIFF PAIR PCIeTX 3 A26 GND GND GND 0 0 A28 CND GND GND 0 0 A28 GND GND GND 0 0 A29 GND	C	A 1 3		GND		GND		A 1 3
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A19 GND GND GND A20 A20 1 PCIeTX DIFF PAIR PCIeTX 1 A21 1 PCIeTX DIFF PAIR PCIeTX 1 A22 GND GND GND - A23 2 PCIeTX DIFF PAIR PCIeTX 2 A23 2 PCIeTX DIFF PAIR PCIeTX 2 A23 2 PCIeTX DIFF PAIR PCIeTX 2 A24 2 PCIeTX DIFF PAIR PCIeTX 3 A25 GND GND GND - 3 A26 3 PCIeTX DIFF PAIR PCIeTX 3 A27 3 PCIeTX DIFF PAIR PCIeTX 3 A28 GND GND - - - A29 GND GND - - - A30 4 PCIeTX DIFF PAIR PCIeTX 4		A 1 7	0	РСІеТХ		РСІеТХ	0	A 1 7
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A29 GND GND GND A30 A30 4 PCIeTX DIFF PAIR PCIeTX 4 A31 4 PCIeTX DIFF PAIR PCIeTX 4 A32 GND GND GND 6 A33 5 PCIeTX DIFF PAIR PCIeTX 5 A34 5 PCIeTX DIFF PAIR PCIeTX 5 A35 GND GND GND 6 A36 6 PCIeTX DIFF PAIR PCIeTX 6 A37 6 PCIeTX DIFF PAIR PCIeTX 6 A38 GND GND 6 6 7 A40 7 PCIeTX DIFF PAIR PCIeTX 7 A41 GND GND GND 6		A 2 8						A 2 8
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A35GNDGNDGNDA366PCIeTXDIFF PAIRPCIeTX6A376PCIe TXDIFF PAIRPCIe TX6A38GNDGNDGND00A397PCIeTXDIFF PAIRPCIeTX7A407PCIe TXDIFF PAIRPCIe TX7A41GNDGNDGND00								A 3 3
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AA397PCIeTXDIFF PAIRPCIeTX7A407PCIeTXDIFF PAIRPCIeTX7A41GNDGNDGNDGND			6		UIFF PAIK		6	A 3 7
A33TTCTCTXDITTTAINTCTCTXA407PCIe TXDIFF PAIRPCIe TX7A41GNDGNDGNDGND0	A							A 3 8
A 4 1 GND GND GND								A 3 9
					UIFF PAIK			A 4 0
								A 4 1
A42 SIDE DAND DISCRETE SIDE DAND		A 4 2		SIDE BAND	DISCRETE	SIDE BAND		A 4 2

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REVISIONS

LTR DESCRIPTION	DATE	DWN	APVD
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THIS DRAWING IS A C	CONTROLLED DOCUMEN	<u>I.SMIIH</u> снк 09.JAN2019	TE TE Connectivi	t y
	TOLERANCES UNLESS OTHERWISE SPECIFIED 0 PLC ±- 1 PLC ±0.5 2 PLC ±0.13 3 PLC ±0.013	- PRODUCT SPEC - APPLICATION SPEC	SLIVER 2.0 2C 33AWG 100 OHM STRAIGHT TO RIGHT ANGLE	
MATERIAL – –	4 PLC ±0.0001 ANGLES ±- FINISH – –		size cage code drawing no $A \ge 0.0779$ C = 2349838 scale 1 · 1 Sheet 2 OF	RESTRICTED TO

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		P1			P2			
	PIN NO.	PAIR NO.	DESIGNATION	WIRE TYPE	DESIGNATION	PAIR NO.	PIN NO	
٦	— B 1		12V POWER	DISCRETE	12V POWER		B 1	
-	B 2		12V POWER	DISCRETE	12V POWER		B 2	
	— ВЗ		12V POWER	DISCRETE	12V POWER		В3	
COMMONED —	— B 4		12V POWER	DISCRETE	12V POWER		Β4	
-	B 5		12V POWER	DISCRETE	12V POWER		В 5	
l	B 6		12V POWER	DISCRETE	12V POWER		Β6	
	Β7		SIDE BAND	DISCRETE	SIDE BAND		Β7	
	B 8		SIDE BAND	DISCRETE	SIDE BAND		B8	
	В9		SIDE BAND	DISCRETE	SIDE BAND		В9	
	B10		SIDE BAND	DISCRETE	SIDE BAND		B10	
	B 1 1		3.3 AUX POWER	DISCRETE	3.3 AUX POWER		B 1 1	
	B12		SIDE BAND	DISCRETE	SIDE BAND		B12	
	B13		GND		GND		B13	
	B14		CLK	DIFF PAIR	CLK		B14	
	B15		CLK	DIFF PAIR	CLK		B15	
	B16		GND		GND		B16	
	B 1 7	0	PCIe RX	DIFF PAIR	PCIe RX	0	B 1 7	
	B18	0	PCIe RX	DIFF PAIR	PCIe RX	0	B18	
	B19		GND		GND		B19	
	B 2 0	1	PCIe RX	DIFF PAIR	PCIe RX	1	B 2 0	
	B 2 1	1	PCIe RX	DIFF PAIR	PCIe RX	1	B 2 1	
	B 2 2		GND		GND		B 2 2	
	B 2 3	2	PCIe RX	DIFF PAIR	PCIe RX	2	B 2 3	
	B 2 4	2	PCIe RX	DIFF PAIR	PCIe RX	2	B 2 4	
	B 2 5		GND		GND		B 2 5	
	B 2 6	3	PCIe RX	DIFF PAIR	PCIe RX	3	B26	
	B 2 7	3	PCIe RX	DIFF PAIR	PCIe RX	3	B 2 7	
	B28		GND		GND		B28	
			KEY		KEY			
	B 2 9		GND		GND		B 2 9	
	B 3 0	4	PCIe RX	DIFF PAIR	PCIe RX	4	B 3 0	
	B 3 1	4	PCIe RX	DIFF PAIR	PCIe RX	4	B 3 1	
	B 3 2		GND		GND		B 3 2	
	B33	5	PCIe RX	DIFF PAIR	PCIe RX	5	B33	
	B 3 4	5	PCIe RX	DIFF PAIR	PCIe RX	5	B 3 4	
	B 3 5		GND		GND		B35	
	B36	6	PCIe RX	DIFF PAIR	PCIe RX	6	B36	
	B 3 7	6	PCIe RX	DIFF PAIR	PCIe RX	6	B 3 7	
	B 3 8		GND		GND	_	B38	
	B 3 9	7	PCIe RX	DIFF PAIR	PCIe RX	7	B39	
	B 4 0	7	PCIe RX	DIFF PAIR	PCIe RX	7	B 4 0	
	B 4 1		GND		GND		B 4 1	
	B 4 2		SIDE BAND	DISCRETE	SIDE BAND		B42	

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REVISIONS

Ρ	LTR	DESCRIPTION	DATE	DWN	APVD
	-	SEE SHEET 1	-	-	-

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COMMONED

THIS DRAWING IS A C	CONTROLL	ED DOCUMENT.	DWN 09JAN2019 <u>Т.SMITH</u> снк 09JAN2019 G. GUTGOLD	9 9 TE Connectivity
DIMENSIONS:	TOLEN OTHERW 0 PLC 1 PLC 2 PLC 3 PLC	RANCES UNLESS 1SE SPECIFIED: ±- ±0.5 ±0.13 ±0.013	APVD	- SLIVER 2.0 2C 33AWG 100 OHM STRAIGHT TO RIGHT ANGLE -
MATERIAL	4 PLC ANGLES FINISH	±0.0001 ±0.0001 ±-	weight CUSTOMER DRAWING	SIZE CAGE CODE DRAWING NO A 2 0 0 7 7 9 C = 2 3 4 9 8 3 8 - SCALE 1 1 SHEET 3 OF 3 REV L

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